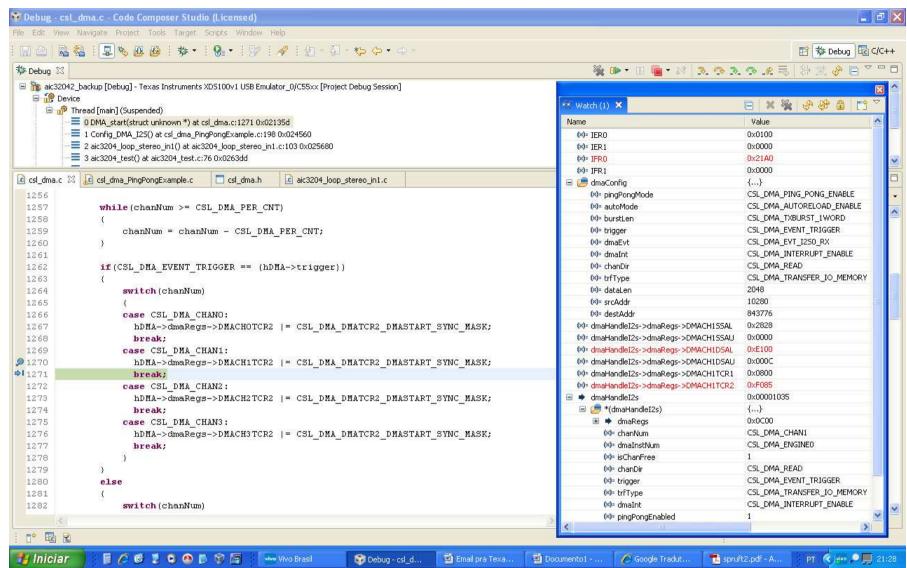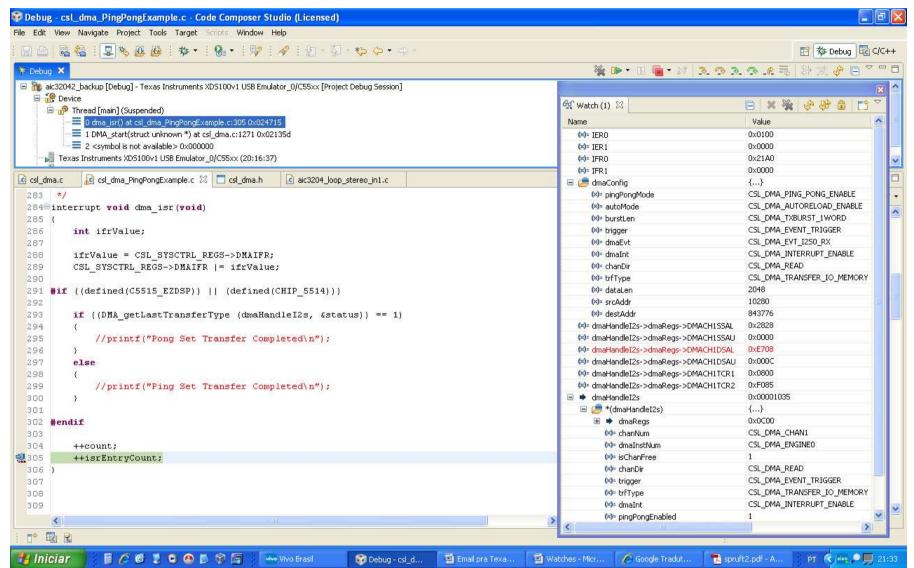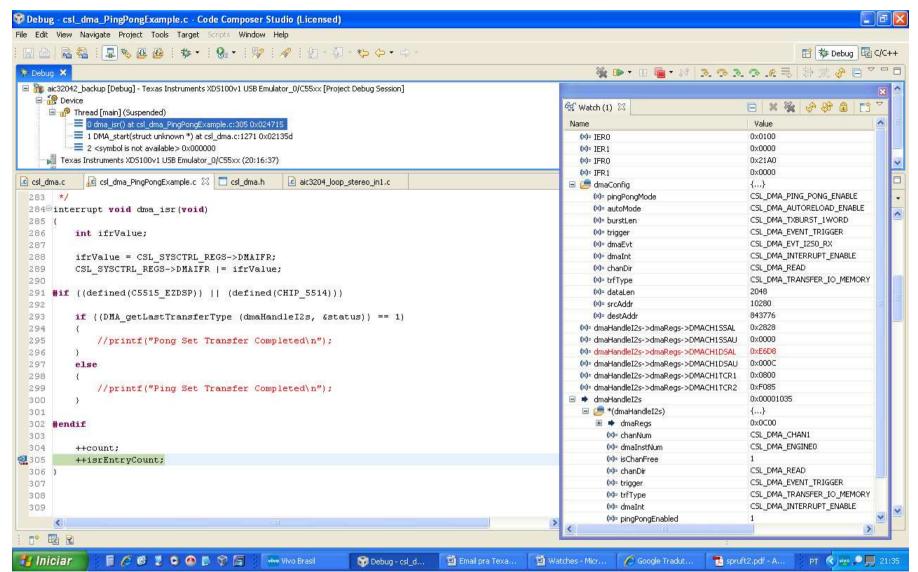Watch before the function `DMA_start(dmaHandleI2s)`. Note that the IER0, IER1, IFR0, IFR1, dmaConfig and dmaHandleI2S are correct.
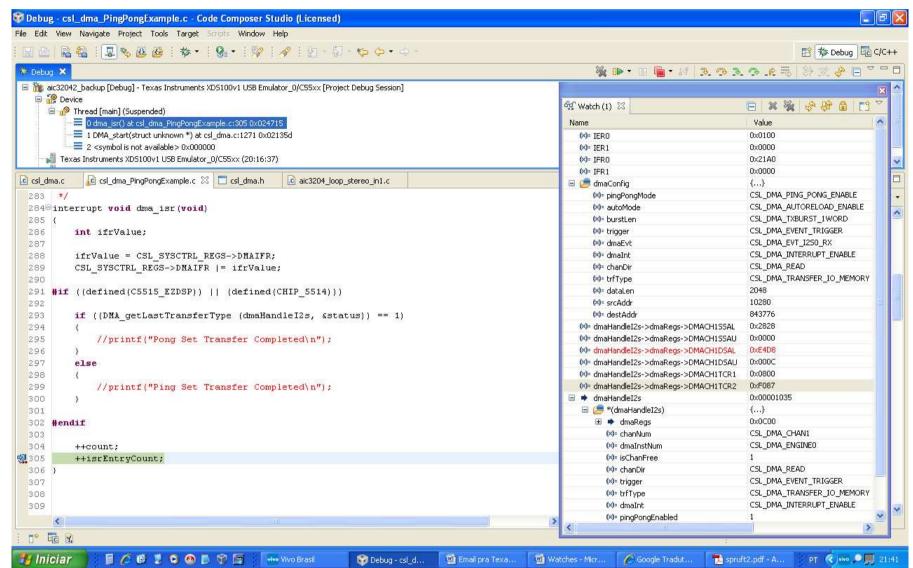
Watch after line 1270 on csl_dma.c file. Note that DMACH1DSAL assumes a strange value 0xE100.
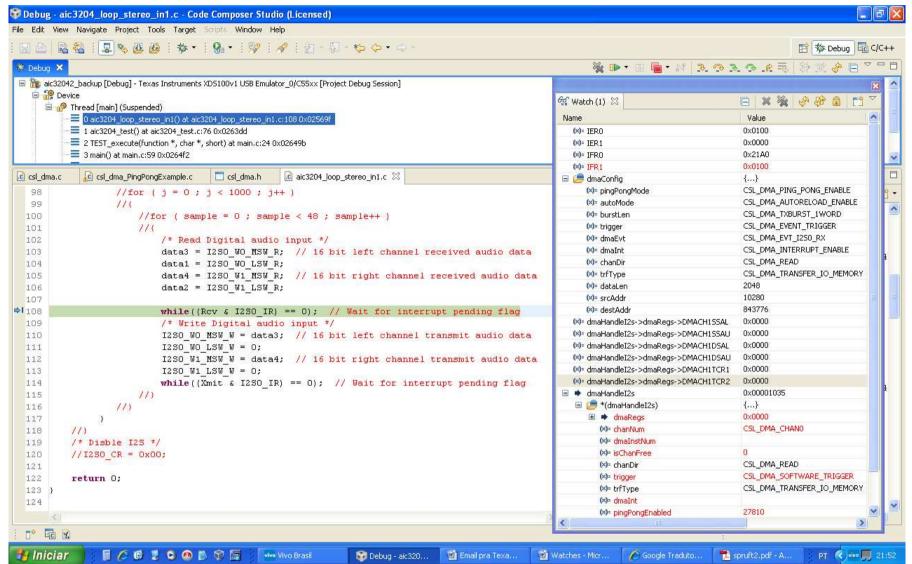
Watch at the end of first `interrupt void dma_isr(void)`. Note that DMACH1DSAL assumes a strange value 0xE708.
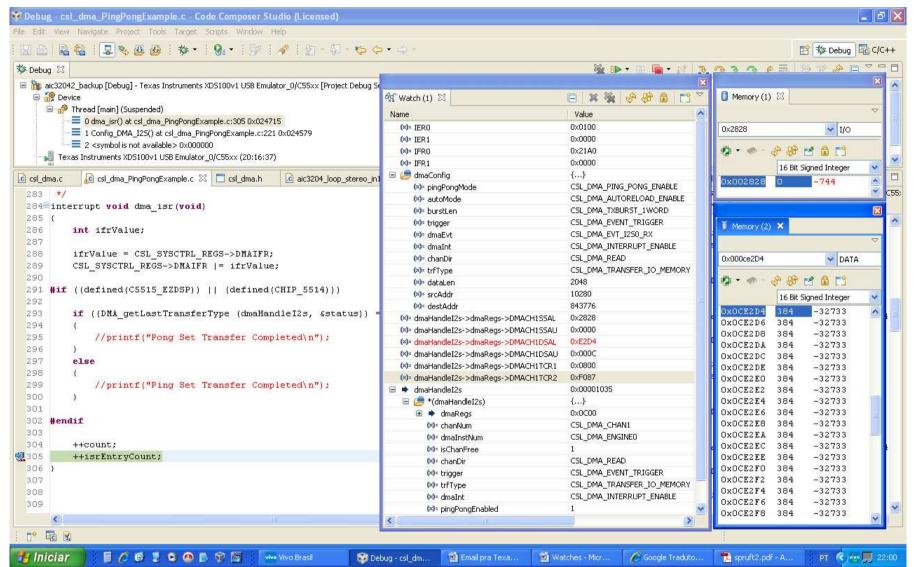
Watch at the end of second `interrupt void dma_isr(void)`. Note that DMACH1TCR2 do not change (still ping interrupt) and DMACH1DSAL assumes a strange value 0xE6D8.

Watch at the end of third `interrupt` `void` `dma_isr(void)`. Note that DMACH1TCR2 changed (pong interrupt) and DMACH1DSAL assumes a strange value 0xE4D8.

Watch after run without breakpoint for 5 seconds and press Halt button. Note that dmaHandleI2S registers are reset and IFR1 shows BUS ERROR.

Screen including memory, the source address 0x2828 (I2S0) is update but the destination address 0xE2D4 do not change.