

P	V <sub>SS</sub>	GP[14]	GP[16]	$\overline{\text{TRST}}$	I2S2_CLK/ GP[18]/ SPI_CLK	SD0_D1/ I2S0_RX/ GP[3]	SD0_D3/ GP[5]	GP[17]	I2S2_RX/ GP[20]/ SPI_RX	SD1_D1/ I2S1_RX/ GP[9]	I2S2_DX/ GP[27]/ SPI_TX	UART_CTS/ GP[29]/ I2S3_FS	UART_RXD/ GP[30]/ I2S3_RX	V <sub>SS</sub>
N	TDO	SPI_CS2	TCK	SPI_RX	GP[13]	TMS	GP[15]	DV <sub>DDIO</sub>	CV <sub>DD</sub>	I2S2_FS/ GP[19]/ SPI_CS0	DV <sub>DDIO</sub>	UART_RTS/ GP[28]/ I2S3_CLK	SD0_D2/ GP[4]	DV <sub>DDIO</sub>
M	EMU1	SPI_CS1	DV <sub>DDIO</sub>	DV <sub>DDIO</sub>	SPI_CS3	CV <sub>DD</sub>	V <sub>SS</sub>	SD0_CLK/ I2S0_CLK/ GP[0]	CV <sub>DD</sub>	SD0_CMD/ I2S0_FS/ GP[1]	UART_TXD/ GP[31]/ I2S3_DX	SD1_D3/ GP[11]	SD1_D0/ I2S1_DX/ GP[8]	SD1_CLK/ I2S1_CLK/ GP[6]
L	SPI_CS0	EMU0	SPI_CLK	DV <sub>DDIO</sub>	V <sub>SS</sub>					V <sub>SS</sub>	SD1_CMD/ I2S1_FS/ GP[7]	SD1_D2/ GP[10]	RSV2	USB_VBUS
K	SPI_TX	TDI	V <sub>SS</sub>	V <sub>SS</sub>							CV <sub>DD</sub>	RSV1	USB_VDD1P3	USB_VSS1P3
J	SD0_D0/ I2S0_DX/ GP[2]	GP[12]	XF									USB_VSSA1P3	V <sub>SS</sub>	USB_DM
H	RSV10	CV <sub>DD</sub>	V <sub>SS</sub>									USB_VDDA1P3	USB_VSSA3P3	USB_DP
G	RSV9	RSV12	CV <sub>DD</sub>									USB_VDDA3P3	USB_VDDPLL	USB_R1
F	RSV8	CV <sub>DD</sub>	V <sub>SS</sub>									USB_VSSREF	USB_VSSPLL	USB_VDD1P3
E	RSV7	RSV11	V <sub>SS</sub>	V <sub>SS</sub>							V <sub>SS</sub>	USB_VDD1P3	USB_VDDOSC	USB_MX1
D	CLK_SEL	RESET	CV <sub>DD</sub>	V <sub>SS</sub>	V <sub>SS</sub>					V <sub>SS</sub>	CV <sub>DD</sub>	USB_VSSOSC	USB_LDOO	USB_MX0
C	CLKIN	$\overline{\text{INT0}}$	DV <sub>DDRTC</sub>	SCL	V <sub>SSRTC</sub>	DV <sub>DDIO</sub>	V <sub>DDA_PLL</sub>	V <sub>SS</sub>	V <sub>SSA_ANA</sub>	BG_CAP	CV <sub>DD</sub>	V <sub>SS</sub>	DSP_LDOO <sub>(1)</sub> EN	LDOI
B	$\overline{\text{INT1}}$	V <sub>SS</sub>	V <sub>SS</sub>	CV <sub>DDRTC</sub>	CV <sub>DDRTC</sub>	V <sub>SSA_ANA</sub>	V <sub>DDA_ANA</sub>	NC	ANA_LDOO	LDOI	RSV5	RSV3	RSV6	LDOI
A	V <sub>SSA_PLL</sub>	CLKOUT	RTC_CLKOUT	SDA	WAKEUP	RTC_XO	RTC_XI	NC	NC	NC	RSV4	RSV0	DSP_LDOO	V <sub>SS</sub>

1 2 3 4 5 6 7 8 9 10 11 12 13 14

- (1) Pin is not supported on this device. To ensure proper device operation, this pin must be hooked up properly. See [Table 3-15, Regulators and Power Management Terminal Functions](#).
- (2) Shaded pins are not supported on this device. To ensure proper device operation, these pins must be hooked up properly. See [Table 3-9, Unsupported USB 2.0 Terminal Functions](#).

**Figure 3-3. C5533 Pin Map**