

XIO2213 PM State Power Consumption
4/13/2007

Link State	Bridge D-State	OHCI D-State	CLKREQ#	1.5V(VDDA_15, VDD_15), mA	1.95V(PLLVDD_Core, DVDD_Core), mA	3.3V(PLLVDD_33, AVDD_33, VDDA_33, VDD_33, DVDD_33), mA	Total (mW)
L0	D0	D0	disabled	157.2	18.5	26.5	359.3
L0	D0	D1	disabled	157.2	18.3	26.4	358.6
L0	D0	D2	disabled	155.5	5.94	7.06	268.1
L0	D0	D3	disabled	155.5	5.94	7.06	268.1
L0s	D0	D0	disabled	129.8	18.5	23.79	309.3
L0s	D0	D1	disabled	129.8	18.6	23.77	309.4
L0s	D0	D2	disabled	127.5	5.9	4.22	216.7
L0s	D0	D3	disabled	127.5	5.9	4.21	216.6
L1-ASPM	D0	D0	disabled	51.44	18.69	23.75	192.0
L1-ASPM	D0	D1	disabled	51.27	18.69	23.76	191.8
L1-ASPM	D0	D2	disabled	48.49	5.99	4.13	98.0
L1-ASPM	D0	D3	disabled	48.49	5.98	4.13	98.0
L1	D1	D1	disabled	42.34	18.69	23.74	178.3
L1	D1	D2	disabled	33.11	6	4.13	75.0
L1	D1	D3	disabled	33.11	6	4.13	75.0
L1	D3	D3	disabled	33.1	5.27	4.13	73.6
L3(Standby)	D3	D3	disabled	28.59	1.7	4.27	60.1

Link State - Chipset and XIO2213 Capability Structure Link state configuration