

Mode: All Lines

Left file: C:\...\Desktop\Battery Training\BATTERY-DEEP-DIVES-MAVED\mini deep dive\SENC file discussion\BQ20z75_R00.gg

Right file: C:\...\Battery Training\BATTERY-DEEP-DIVES-MAVED\mini deep dive\SENC file discussion\BQ20z75_R00_golden.gg

| | | | | |
|----|--------------------------------------|----|----|--------------------------------------|
| 1 | [Header] | = | 1 | [Header] |
| 2 | bq EVSW Version = 0.9.65 | | 2 | bq EVSW Version = 0.9.65 |
| 3 | DeviceName = bq20z75 v1.60 | | 3 | DeviceName = bq20z75 v1.60 |
| 4 | Time = 10/12/2010 11:32:02 AM | <> | 4 | Time = 10/12/2010 11:52:47 AM |
| 5 | | = | 5 | |
| 6 | | | 6 | |
| 7 | [Voltage(1st Level Safety)] | | 7 | [Voltage(1st Level Safety)] |
| 8 | COV Threshold = 4275 | | 8 | COV Threshold = 4275 |
| 9 | COV Recovery = 4100 | | 9 | COV Recovery = 4100 |
| 10 | CUV Threshold = 2800 | | 10 | CUV Threshold = 2800 |
| 11 | CUV Time = 2 | | 11 | CUV Time = 2 |
| 12 | CUV Recovery = 3400 | | 12 | CUV Recovery = 3400 |
| 13 | [Current(1st Level Safety)] | | 13 | [Current(1st Level Safety)] |
| 14 | OC (1st Tier) Chg = 2600 | | 14 | OC (1st Tier) Chg = 2600 |
| 15 | OC (1st Tier) Dsg = 5200 | | 15 | OC (1st Tier) Dsg = 5200 |
| 16 | Current Recovery Time = 8 | | 16 | Current Recovery Time = 8 |
| 17 | AFE OC Dsg = 02 | | 17 | AFE OC Dsg = 02 |
| 18 | AFE OC Dsg Time = 0F | | 18 | AFE OC Dsg Time = 0F |
| 19 | AFE SC Chg Cfg = F0 | | 19 | AFE SC Chg Cfg = F0 |
| 20 | AFE SC Dsg Cfg = F0 | | 20 | AFE SC Dsg Cfg = F0 |
| 21 | [Temperature(1st Level Safety)] | | 21 | [Temperature(1st Level Safety)] |
| 22 | Over Temp Chg = 40.0 | | 22 | Over Temp Chg = 40.0 |
| 23 | OT Chg Recovery = 37.0 | | 23 | OT Chg Recovery = 37.0 |
| 24 | Over Temp Dsg = 65.0 | | 24 | Over Temp Dsg = 65.0 |
| 25 | OT Dsg Recovery = 55.0 | | 25 | OT Dsg Recovery = 55.0 |
| 26 | | | 26 | |
| 27 | [Voltage(2nd Level Safety)] | | 27 | [Voltage(2nd Level Safety)] |
| 28 | SOV Threshold = 12900 | | 28 | SOV Threshold = 12900 |
| 29 | SOV Time = 2 | | 29 | SOV Time = 2 |
| 30 | Cell Imbalance Current = 5 | | 30 | Cell Imbalance Current = 5 |
| 31 | Cell Imbalance Fail Voltage = 500 | | 31 | Cell Imbalance Fail Voltage = 500 |
| 32 | Cell Imbalance Time = 5 | | 32 | Cell Imbalance Time = 5 |
| 33 | Battery Rest Time = 1800 | | 33 | Battery Rest Time = 1800 |
| 34 | Min CIM-check voltage = 3300 | | 34 | Min CIM-check voltage = 3300 |
| 35 | PFIN Detect Time = 5 | | 35 | PFIN Detect Time = 5 |
| 36 | [Current(2nd Level Safety)] | | 36 | [Current(2nd Level Safety)] |
| 37 | SOC Chg = 4000 | | 37 | SOC Chg = 4000 |
| 38 | SOC Chg Time = 5 | | 38 | SOC Chg Time = 5 |
| 39 | SOC Dsg = 7000 | | 39 | SOC Dsg = 7000 |
| 40 | SOC Dsg Time = 5 | | 40 | SOC Dsg Time = 5 |
| 41 | [Temperature(2nd Level Safety)] | | 41 | [Temperature(2nd Level Safety)] |
| 42 | SOT Chg = 60.0 | | 42 | SOT Chg = 60.0 |
| 43 | SOT Chg Time = 4 | | 43 | SOT Chg Time = 4 |
| 44 | SOT Dsg = 75.0 | | 44 | SOT Dsg = 75.0 |
| 45 | SOT Dsg Time = 4 | | 45 | SOT Dsg Time = 4 |
| 46 | [FET Verification(2nd Level Safety)] | | 46 | [FET Verification(2nd Level Safety)] |
| 47 | FET Fail Time = 5 | | 47 | FET Fail Time = 5 |
| 48 | [AFE Verification(2nd Level Safety)] | | 48 | [AFE Verification(2nd Level Safety)] |
| 49 | AFE Fail Limit = 10 | | 49 | AFE Fail Limit = 10 |
| 50 | | | 50 | |
| 51 | [Charge Inhibit Cfg(Charge Control)] | | 51 | [Charge Inhibit Cfg(Charge Control)] |
| 52 | Chg Inhibit Temp Low = 0.0 | | 52 | Chg Inhibit Temp Low = 0.0 |
| 53 | Chg Inhibit Temp High = 39.0 | | 53 | Chg Inhibit Temp High = 39.0 |
| 54 | Temp Hys = 3.0 | | 54 | Temp Hys = 3.0 |
| 55 | [Pre-Charge Cfg(Charge Control)] | | 55 | [Pre-Charge Cfg(Charge Control)] |
| 56 | Pre-chg Current = 255 | | 56 | Pre-chg Current = 255 |

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|-----|--------------------------------------|----|-----|--------------------------------------|
| 57 | Pre-chg Temp = 5.0 | | 57 | Pre-chg Temp = 5.0 |
| 58 | Pre-chg Voltage = 2900 | | 58 | Pre-chg Voltage = 2900 |
| 59 | Recovery Voltage = 3000 | | 59 | Recovery Voltage = 3000 |
| 60 | [Fast Charge Cfg(Charge Control)] | | 60 | [Fast Charge Cfg(Charge Control)] |
| 61 | Fast Charge Current = 1275 | | 61 | Fast Charge Current = 1275 |
| 62 | Charging Voltage = 12600 | | 62 | Charging Voltage = 12600 |
| 63 | Suspend Low Temp = 0.0 | | 63 | Suspend Low Temp = 0.0 |
| 64 | Suspend High Temp = 40.0 | | 64 | Suspend High Temp = 40.0 |
| 65 | [Termination Cfg.(Charge Control)] | | 65 | [Termination Cfg.(Charge Control)] |
| 66 | Taper Current = 150 | | 66 | Taper Current = 150 |
| 67 | Taper Voltage = 150 | | 67 | Taper Voltage = 150 |
| 68 | TCA Clear % = 95 | | 68 | TCA Clear % = 95 |
| 69 | FC Clear % = 98 | | 69 | FC Clear % = 98 |
| 70 | [Cell Balancing Cfg(Charge Control)] | | 70 | [Cell Balancing Cfg(Charge Control)] |
| 71 | Min Cell Deviation = 1680 | | 71 | Min Cell Deviation = 1680 |
| 72 | [Charging Faults(Charge Control)] | | 72 | [Charging Faults(Charge Control)] |
| 73 | Over Charge Capacity = 300 | | 73 | Over Charge Capacity = 300 |
| 74 | | | 74 | |
| 75 | [Data(SBS Configuration)] | | 75 | [Data(SBS Configuration)] |
| 76 | Rem Cap Alarm = 255 | | 76 | Rem Cap Alarm = 255 |
| 77 | Rem Energy Alarm = 3820 | | 77 | Rem Energy Alarm = 3820 |
| 78 | Rem Time Alarm = 10 | | 78 | Rem Time Alarm = 10 |
| 79 | Init Battery Mode = 0081 | | 79 | Init Battery Mode = 0081 |
| 80 | Design Voltage = 11100 | | 80 | Design Voltage = 11100 |
| 81 | Spec Info = 0031 | | 81 | Spec Info = 0031 |
| 82 | Manuf Date = 08-Oct-2010 | | 82 | Manuf Date = 08-Oct-2010 |
| 83 | Ser. Num. = 0001 | | 83 | Ser. Num. = 0001 |
| 84 | Cycle Count = 4 | <> | 84 | Cycle Count = 0 |
| 85 | CC Threshold = 2300 | = | 85 | CC Threshold = 2300 |
| 86 | CF MaxError Limit = 10 | | 86 | CF MaxError Limit = 10 |
| 87 | Design Capacity = 2500 | | 87 | Design Capacity = 2500 |
| 88 | Design Energy = 27750 | | 88 | Design Energy = 27750 |
| 89 | Manuf Name = TI | | 89 | Manuf Name = TI |
| 90 | Device Name = TI example | | 90 | Device Name = TI example |
| 91 | Device Chemistry = LION | | 91 | Device Chemistry = LION |
| 92 | [Configuration(SBS Configuration)] | | 92 | [Configuration(SBS Configuration)] |
| 93 | TDA Set % = 5 | | 93 | TDA Set % = 5 |
| 94 | TDA Clear % = 7 | | 94 | TDA Clear % = 7 |
| 95 | FD Set % = 2 | | 95 | FD Set % = 2 |
| 96 | FD Clear % = 4 | | 96 | FD Clear % = 4 |
| 97 | TDA Set Volt Threshold = 5000 | | 97 | TDA Set Volt Threshold = 5000 |
| 98 | TDA Set Volt Time = 0 | | 98 | TDA Set Volt Time = 0 |
| 99 | TDA Clear Volt = 5500 | | 99 | TDA Clear Volt = 5500 |
| 100 | | | 100 | |
| 101 | [Manufacturer Info(System Data)] | | 101 | [Manufacturer Info(System Data)] |
| 102 | Manuf. Info = TI example | | 102 | Manuf. Info = TI example |
| 103 | [Lifetime Data(System Data)] | | 103 | [Lifetime Data(System Data)] |
| 104 | Lifetime Max Temp = 29.6 | <> | 104 | Lifetime Max Temp = 25.0 |
| 105 | Lifetime Min Temp = 22.7 | | 105 | Lifetime Min Temp = 24.0 |
| 106 | | = | 106 | |
| 107 | [Registers(Configuration)] | | 107 | [Registers(Configuration)] |
| 108 | Operation Cfg A = 0229 | | 108 | Operation Cfg A = 0229 |
| 109 | Operation Cfg B = 2CD0 | | 109 | Operation Cfg B = 2CD0 |
| 110 | Operation Cfg C = 0001 | | 110 | Operation Cfg C = 0001 |
| 111 | Permanent Fail Cfg = 0C0C | | 111 | Permanent Fail Cfg = 0C0C |
| 112 | Non-Removable Cfg = 3027 | | 112 | Non-Removable Cfg = 3027 |
| 113 | [AFE(Configuration)] | | 113 | [AFE(Configuration)] |

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|-----|-----------------------------------|----|-----|-----------------------------------|
| 114 | AFE.State_CTL = 00 | | 114 | AFE.State_CTL = 00 |
| 115 | | | 115 | |
| 116 | [Power(Power)] | | 116 | [Power(Power)] |
| 117 | Flash Update OK Voltage = 9000 | <> | 117 | Flash Update OK Voltage = 7500 |
| 118 | Shutdown Voltage = 7200 | = | 118 | Shutdown Voltage = 7200 |
| 119 | Shutdown Time = 10 | | 119 | Shutdown Time = 10 |
| 120 | Cell Shutdown Voltage = 2400 | | 120 | Cell Shutdown Voltage = 2400 |
| 121 | Cell Shutdown Time = 10 | | 121 | Cell Shutdown Time = 10 |
| 122 | Charger Present = 3000 | | 122 | Charger Present = 3000 |
| 123 | Sleep Current = 5 | | 123 | Sleep Current = 5 |
| 124 | Wake Current Reg = 00 | | 124 | Wake Current Reg = 00 |
| 125 | | | 125 | |
| 126 | [IT Cfg(Gas Gauging)] | | 126 | [IT Cfg(Gas Gauging)] |
| 127 | Load Select = 3 | | 127 | Load Select = 3 |
| 128 | Load Mode = 1 | | 128 | Load Mode = 1 |
| 129 | Term Voltage = 9400 | | 129 | Term Voltage = 9400 |
| 130 | User Rate-mA = 0 | | 130 | User Rate-mA = 0 |
| 131 | User Rate-mW = 0 | | 131 | User Rate-mW = 0 |
| 132 | Reserve Cap-mAh = 64 | | 132 | Reserve Cap-mAh = 64 |
| 133 | Reserve Cap-mWh = 710 | | 133 | Reserve Cap-mWh = 710 |
| 134 | [Current Thresholds(Gas Gauging)] | | 134 | [Current Thresholds(Gas Gauging)] |
| 135 | Dsg Current Threshold = 50 | | 135 | Dsg Current Threshold = 50 |
| 136 | Chg Current Threshold = 25 | | 136 | Chg Current Threshold = 25 |
| 137 | Quit Current = 10 | | 137 | Quit Current = 10 |
| 138 | [State(Gas Gauging)] | | 138 | [State(Gas Gauging)] |
| 139 | Qmax Cell 0 = 2494 | <> | 139 | Qmax Cell 0 = 2500 |
| 140 | Qmax Cell 1 = 2506 | | 140 | Qmax Cell 1 = 2500 |
| 141 | Qmax Cell 2 = 2493 | | 141 | Qmax Cell 2 = 2500 |
| 142 | Qmax Cell 3 = 2500 | = | 142 | Qmax Cell 3 = 2500 |
| 143 | Qmax Pack = 2493 | <> | 143 | Qmax Pack = 2500 |
| 144 | Update Status = 06 | | 144 | Update Status = 02 |
| 145 | Avg I Last Run = -518 | = | 145 | Avg I Last Run = -518 |
| 146 | Avg P Last Run = -590 | | 146 | Avg P Last Run = -590 |
| 147 | Delta Voltage = 5 | | 147 | Delta Voltage = 5 |
| 148 | | | 148 | |
| 149 | [R_a0(Ra Table)] | | 149 | [R_a0(Ra Table)] |
| 150 | Cell10 R_a flag = 0055 | | 150 | Cell10 R_a flag = 0055 |
| 151 | Cell10 R_a 0 = 164 | | 151 | Cell10 R_a 0 = 164 |
| 152 | Cell10 R_a 1 = 170 | | 152 | Cell10 R_a 1 = 170 |
| 153 | Cell10 R_a 2 = 173 | | 153 | Cell10 R_a 2 = 173 |
| 154 | Cell10 R_a 3 = 199 | | 154 | Cell10 R_a 3 = 199 |
| 155 | Cell10 R_a 4 = 175 | | 155 | Cell10 R_a 4 = 175 |
| 156 | Cell10 R_a 5 = 164 | | 156 | Cell10 R_a 5 = 164 |
| 157 | Cell10 R_a 6 = 181 | | 157 | Cell10 R_a 6 = 181 |
| 158 | Cell10 R_a 7 = 186 | | 158 | Cell10 R_a 7 = 186 |
| 159 | Cell10 R_a 8 = 259 | | 159 | Cell10 R_a 8 = 259 |
| 160 | Cell10 R_a 9 = 283 | | 160 | Cell10 R_a 9 = 283 |
| 161 | Cell10 R_a 10 = 317 | | 161 | Cell10 R_a 10 = 317 |
| 162 | Cell10 R_a 11 = 600 | | 162 | Cell10 R_a 11 = 600 |
| 163 | Cell10 R_a 12 = 2607 | | 163 | Cell10 R_a 12 = 2607 |
| 164 | Cell10 R_a 13 = 4007 | | 164 | Cell10 R_a 13 = 4007 |
| 165 | Cell10 R_a 14 = 5835 | | 165 | Cell10 R_a 14 = 5835 |
| 166 | [R_a1(Ra Table)] | | 166 | [R_a1(Ra Table)] |
| 167 | Cell11 R_a flag = 0055 | | 167 | Cell11 R_a flag = 0055 |
| 168 | Cell11 R_a 0 = 178 | <> | 168 | Cell11 R_a 0 = 164 |
| 169 | Cell11 R_a 1 = 185 | | 169 | Cell11 R_a 1 = 170 |
| 170 | Cell11 R_a 2 = 188 | | 170 | Cell11 R_a 2 = 173 |

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|-----|-------------------------|----|-----|-------------------------|
| 171 | Cell11 R_a 3 = 216 | | 171 | Cell11 R_a 3 = 199 |
| 172 | Cell11 R_a 4 = 189 | | 172 | Cell11 R_a 4 = 175 |
| 173 | Cell11 R_a 5 = 181 | | 173 | Cell11 R_a 5 = 164 |
| 174 | Cell11 R_a 6 = 205 | | 174 | Cell11 R_a 6 = 181 |
| 175 | Cell11 R_a 7 = 196 | | 175 | Cell11 R_a 7 = 186 |
| 176 | Cell11 R_a 8 = 272 | | 176 | Cell11 R_a 8 = 259 |
| 177 | Cell11 R_a 9 = 304 | | 177 | Cell11 R_a 9 = 283 |
| 178 | Cell11 R_a 10 = 320 | | 178 | Cell11 R_a 10 = 317 |
| 179 | Cell11 R_a 11 = 508 | | 179 | Cell11 R_a 11 = 600 |
| 180 | Cell11 R_a 12 = 2228 | | 180 | Cell11 R_a 12 = 2607 |
| 181 | Cell11 R_a 13 = 3427 | | 181 | Cell11 R_a 13 = 4007 |
| 182 | Cell11 R_a 14 = 4992 | | 182 | Cell11 R_a 14 = 5835 |
| 183 | [R_a2(Ra Table)] | = | 183 | [R_a2(Ra Table)] |
| 184 | Cell12 R_a flag = 0000 | <> | 184 | Cell12 R_a flag = 0055 |
| 185 | Cell12 R_a 0 = 168 | | 185 | Cell12 R_a 0 = 164 |
| 186 | Cell12 R_a 1 = 174 | | 186 | Cell12 R_a 1 = 170 |
| 187 | Cell12 R_a 2 = 179 | | 187 | Cell12 R_a 2 = 173 |
| 188 | Cell12 R_a 3 = 202 | | 188 | Cell12 R_a 3 = 199 |
| 189 | Cell12 R_a 4 = 184 | | 189 | Cell12 R_a 4 = 175 |
| 190 | Cell12 R_a 5 = 170 | | 190 | Cell12 R_a 5 = 164 |
| 191 | Cell12 R_a 6 = 196 | | 191 | Cell12 R_a 6 = 181 |
| 192 | Cell12 R_a 7 = 196 | | 192 | Cell12 R_a 7 = 186 |
| 193 | Cell12 R_a 8 = 269 | | 193 | Cell12 R_a 8 = 259 |
| 194 | Cell12 R_a 9 = 293 | | 194 | Cell12 R_a 9 = 283 |
| 195 | Cell12 R_a 10 = 332 | | 195 | Cell12 R_a 10 = 317 |
| 196 | Cell12 R_a 11 = 630 | | 196 | Cell12 R_a 11 = 600 |
| 197 | Cell12 R_a 12 = 2730 | | 197 | Cell12 R_a 12 = 2607 |
| 198 | Cell12 R_a 13 = 4198 | | 198 | Cell12 R_a 13 = 4007 |
| 199 | Cell12 R_a 14 = 6105 | | 199 | Cell12 R_a 14 = 5835 |
| 200 | [R_a3(Ra Table)] | = | 200 | [R_a3(Ra Table)] |
| 201 | Cell13 R_a flag = FF55 | | 201 | Cell13 R_a flag = FF55 |
| 202 | Cell13 R_a 0 = 160 | <> | 202 | Cell13 R_a 0 = 164 |
| 203 | Cell13 R_a 1 = 166 | | 203 | Cell13 R_a 1 = 170 |
| 204 | Cell13 R_a 2 = 153 | | 204 | Cell13 R_a 2 = 173 |
| 205 | Cell13 R_a 3 = 151 | | 205 | Cell13 R_a 3 = 199 |
| 206 | Cell13 R_a 4 = 145 | | 206 | Cell13 R_a 4 = 175 |
| 207 | Cell13 R_a 5 = 152 | | 207 | Cell13 R_a 5 = 164 |
| 208 | Cell13 R_a 6 = 176 | | 208 | Cell13 R_a 6 = 181 |
| 209 | Cell13 R_a 7 = 204 | | 209 | Cell13 R_a 7 = 186 |
| 210 | Cell13 R_a 8 = 222 | | 210 | Cell13 R_a 8 = 259 |
| 211 | Cell13 R_a 9 = 254 | | 211 | Cell13 R_a 9 = 283 |
| 212 | Cell13 R_a 10 = 315 | | 212 | Cell13 R_a 10 = 317 |
| 213 | Cell13 R_a 11 = 437 | | 213 | Cell13 R_a 11 = 600 |
| 214 | Cell13 R_a 12 = 651 | | 214 | Cell13 R_a 12 = 2607 |
| 215 | Cell13 R_a 13 = 1001 | | 215 | Cell13 R_a 13 = 4007 |
| 216 | Cell13 R_a 14 = 1458 | | 216 | Cell13 R_a 14 = 5835 |
| 217 | [R_a0x(Ra Table)] | = | 217 | [R_a0x(Ra Table)] |
| 218 | xCell10 R_a flag = 0000 | <> | 218 | xCell10 R_a flag = FFFF |
| 219 | xCell10 R_a 0 = 164 | | 219 | xCell10 R_a 0 = 164 |
| 220 | xCell10 R_a 1 = 170 | = | 220 | xCell10 R_a 1 = 170 |
| 221 | xCell10 R_a 2 = 173 | | 221 | xCell10 R_a 2 = 173 |
| 222 | xCell10 R_a 3 = 199 | | 222 | xCell10 R_a 3 = 199 |
| 223 | xCell10 R_a 4 = 175 | | 223 | xCell10 R_a 4 = 175 |
| 224 | xCell10 R_a 5 = 164 | | 224 | xCell10 R_a 5 = 164 |
| 225 | xCell10 R_a 6 = 181 | | 225 | xCell10 R_a 6 = 181 |
| 226 | xCell10 R_a 7 = 186 | | 226 | xCell10 R_a 7 = 186 |
| 227 | xCell10 R_a 8 = 259 | | 227 | xCell10 R_a 8 = 259 |

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|-----|-------------------------|----|-----|-------------------------|
| 228 | xCell10 R_a 9 = 283 | | 228 | xCell10 R_a 9 = 283 |
| 229 | xCell10 R_a 10 = 317 | | 229 | xCell10 R_a 10 = 317 |
| 230 | xCell10 R_a 11 = 600 | | 230 | xCell10 R_a 11 = 600 |
| 231 | xCell10 R_a 12 = 2607 | | 231 | xCell10 R_a 12 = 2607 |
| 232 | xCell10 R_a 13 = 4007 | | 232 | xCell10 R_a 13 = 4007 |
| 233 | xCell10 R_a 14 = 5835 | | 233 | xCell10 R_a 14 = 5835 |
| 234 | [R_alx(Ra Table)] | | 234 | [R_alx(Ra Table)] |
| 235 | xCell11 R_a flag = 0000 | <> | 235 | xCell11 R_a flag = FFFF |
| 236 | xCell11 R_a 0 = 178 | | 236 | xCell11 R_a 0 = 164 |
| 237 | xCell11 R_a 1 = 185 | | 237 | xCell11 R_a 1 = 170 |
| 238 | xCell11 R_a 2 = 188 | | 238 | xCell11 R_a 2 = 173 |
| 239 | xCell11 R_a 3 = 216 | | 239 | xCell11 R_a 3 = 199 |
| 240 | xCell11 R_a 4 = 189 | | 240 | xCell11 R_a 4 = 175 |
| 241 | xCell11 R_a 5 = 181 | | 241 | xCell11 R_a 5 = 164 |
| 242 | xCell11 R_a 6 = 205 | | 242 | xCell11 R_a 6 = 181 |
| 243 | xCell11 R_a 7 = 196 | | 243 | xCell11 R_a 7 = 186 |
| 244 | xCell11 R_a 8 = 272 | | 244 | xCell11 R_a 8 = 259 |
| 245 | xCell11 R_a 9 = 304 | | 245 | xCell11 R_a 9 = 283 |
| 246 | xCell11 R_a 10 = 320 | | 246 | xCell11 R_a 10 = 317 |
| 247 | xCell11 R_a 11 = 508 | | 247 | xCell11 R_a 11 = 600 |
| 248 | xCell11 R_a 12 = 2228 | | 248 | xCell11 R_a 12 = 2607 |
| 249 | xCell11 R_a 13 = 3427 | | 249 | xCell11 R_a 13 = 4007 |
| 250 | xCell11 R_a 14 = 4992 | | 250 | xCell11 R_a 14 = 5835 |
| 251 | [R_a2x(Ra Table)] | = | 251 | [R_a2x(Ra Table)] |
| 252 | xCell12 R_a flag = 0055 | <> | 252 | xCell12 R_a flag = FFFF |
| 253 | xCell12 R_a 0 = 168 | | 253 | xCell12 R_a 0 = 164 |
| 254 | xCell12 R_a 1 = 174 | | 254 | xCell12 R_a 1 = 170 |
| 255 | xCell12 R_a 2 = 179 | | 255 | xCell12 R_a 2 = 173 |
| 256 | xCell12 R_a 3 = 202 | | 256 | xCell12 R_a 3 = 199 |
| 257 | xCell12 R_a 4 = 184 | | 257 | xCell12 R_a 4 = 175 |
| 258 | xCell12 R_a 5 = 170 | | 258 | xCell12 R_a 5 = 164 |
| 259 | xCell12 R_a 6 = 196 | | 259 | xCell12 R_a 6 = 181 |
| 260 | xCell12 R_a 7 = 196 | | 260 | xCell12 R_a 7 = 186 |
| 261 | xCell12 R_a 8 = 269 | | 261 | xCell12 R_a 8 = 259 |
| 262 | xCell12 R_a 9 = 293 | | 262 | xCell12 R_a 9 = 283 |
| 263 | xCell12 R_a 10 = 332 | | 263 | xCell12 R_a 10 = 317 |
| 264 | xCell12 R_a 11 = 630 | | 264 | xCell12 R_a 11 = 600 |
| 265 | xCell12 R_a 12 = 2730 | | 265 | xCell12 R_a 12 = 2607 |
| 266 | xCell12 R_a 13 = 4198 | | 266 | xCell12 R_a 13 = 4007 |
| 267 | xCell12 R_a 14 = 6105 | | 267 | xCell12 R_a 14 = 5835 |
| 268 | [R_a3x(Ra Table)] | = | 268 | [R_a3x(Ra Table)] |
| 269 | xCell13 R_a flag = FFFF | | 269 | xCell13 R_a flag = FFFF |
| 270 | xCell13 R_a 0 = 160 | <> | 270 | xCell13 R_a 0 = 164 |
| 271 | xCell13 R_a 1 = 166 | | 271 | xCell13 R_a 1 = 170 |
| 272 | xCell13 R_a 2 = 153 | | 272 | xCell13 R_a 2 = 173 |
| 273 | xCell13 R_a 3 = 151 | | 273 | xCell13 R_a 3 = 199 |
| 274 | xCell13 R_a 4 = 145 | | 274 | xCell13 R_a 4 = 175 |
| 275 | xCell13 R_a 5 = 152 | | 275 | xCell13 R_a 5 = 164 |
| 276 | xCell13 R_a 6 = 176 | | 276 | xCell13 R_a 6 = 181 |
| 277 | xCell13 R_a 7 = 204 | | 277 | xCell13 R_a 7 = 186 |
| 278 | xCell13 R_a 8 = 222 | | 278 | xCell13 R_a 8 = 259 |
| 279 | xCell13 R_a 9 = 254 | | 279 | xCell13 R_a 9 = 283 |
| 280 | xCell13 R_a 10 = 315 | | 280 | xCell13 R_a 10 = 317 |
| 281 | xCell13 R_a 11 = 437 | | 281 | xCell13 R_a 11 = 600 |
| 282 | xCell13 R_a 12 = 651 | | 282 | xCell13 R_a 12 = 2607 |
| 283 | xCell13 R_a 13 = 1001 | | 283 | xCell13 R_a 13 = 4007 |
| 284 | xCell13 R_a 14 = 1458 | | 284 | xCell13 R_a 14 = 5835 |

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|-----|---------------------------------|----|-----|---------------------------------|
| 285 | | = | 285 | |
| 286 | [Device Status Data(PF Status)] | | 286 | [Device Status Data(PF Status)] |
| 287 | PF Flags 1 = 0000 | | 287 | PF Flags 1 = 0000 |
| 288 | PF Flags 2 = 0000 | | 288 | PF Flags 2 = 0000 |
| 289 | | | 289 | |
| 290 | [Data(Calibration)] | | 290 | [Data(Calibration)] |
| 291 | CC Gain = 10.032 | | 291 | CC Gain = 10.032 |
| 292 | CC Delta = 10.032 | | 292 | CC Delta = 10.032 |
| 293 | Ref Voltage = 1228.85 | | 293 | Ref Voltage = 1228.85 |
| 294 | AFE Pack Gain = 726.90 | | 294 | AFE Pack Gain = 726.90 |
| 295 | CC Offset = -0.221 | <> | 295 | CC Offset = -0.222 |
| 296 | Board Offset = -26.9 | = | 296 | Board Offset = -26.9 |
| 297 | Int Temp Offset = 0.0 | | 297 | Int Temp Offset = 0.0 |
| 298 | Ext1 Temp Offset = -2.0 | | 298 | Ext1 Temp Offset = -2.0 |
| 299 | Ext2 Temp Offset = 0.0 | | 299 | Ext2 Temp Offset = 0.0 |
| 300 | [Config(Calibration)] | | 300 | [Config(Calibration)] |
| 301 | CC Current = 3000 | | 301 | CC Current = 3000 |
| 302 | Voltage Signal = 12000 | | 302 | Voltage Signal = 12000 |
| 303 | Temp Signal = 298.0 | | 303 | Temp Signal = 298.0 |
| 304 | CC Offset Time = 250 | | 304 | CC Offset Time = 250 |
| 305 | ADC Offset Time = 32 | | 305 | ADC Offset Time = 32 |
| 306 | CC Gain Time = 250 | | 306 | CC Gain Time = 250 |
| 307 | Voltage Time = 1984 | | 307 | Voltage Time = 1984 |
| 308 | Temperature Time = 32 | | 308 | Temperature Time = 32 |
| 309 | Cal Mode Timeout = 300 | | 309 | Cal Mode Timeout = 300 |
| 310 | [Temp Model(Calibration)] | | 310 | [Temp Model(Calibration)] |
| 311 | Ext Coef 1 = -28285 | | 311 | Ext Coef 1 = -28285 |
| 312 | Ext Coef 2 = 20848 | | 312 | Ext Coef 2 = 20848 |
| 313 | Ext Coef 3 = -7537 | | 313 | Ext Coef 3 = -7537 |
| 314 | Ext Coef 4 = 401.2 | | 314 | Ext Coef 4 = 401.2 |
| 315 | Ext Min AD = 0 | | 315 | Ext Min AD = 0 |
| 316 | Ext Max Temp = 401.2 | | 316 | Ext Max Temp = 401.2 |
| 317 | Int Coef 1 = 0 | | 317 | Int Coef 1 = 0 |
| 318 | Int Coef 2 = 0 | | 318 | Int Coef 2 = 0 |
| 319 | Int Coef 3 = -11136 | | 319 | Int Coef 3 = -11136 |
| 320 | Int Coef 4 = 575.4 | | 320 | Int Coef 4 = 575.4 |
| 321 | Int Min AD = 0 | | 321 | Int Min AD = 0 |
| 322 | Int Max Temp = 575.4 | | 322 | Int Max Temp = 575.4 |
| 323 | [Current(Calibration)] | | 323 | [Current(Calibration)] |
| 324 | Filter = 239 | | 324 | Filter = 239 |
| 325 | Deadband = 3 | | 325 | Deadband = 3 |
| 326 | CC Deadband = 10.0 | | 326 | CC Deadband = 10.0 |