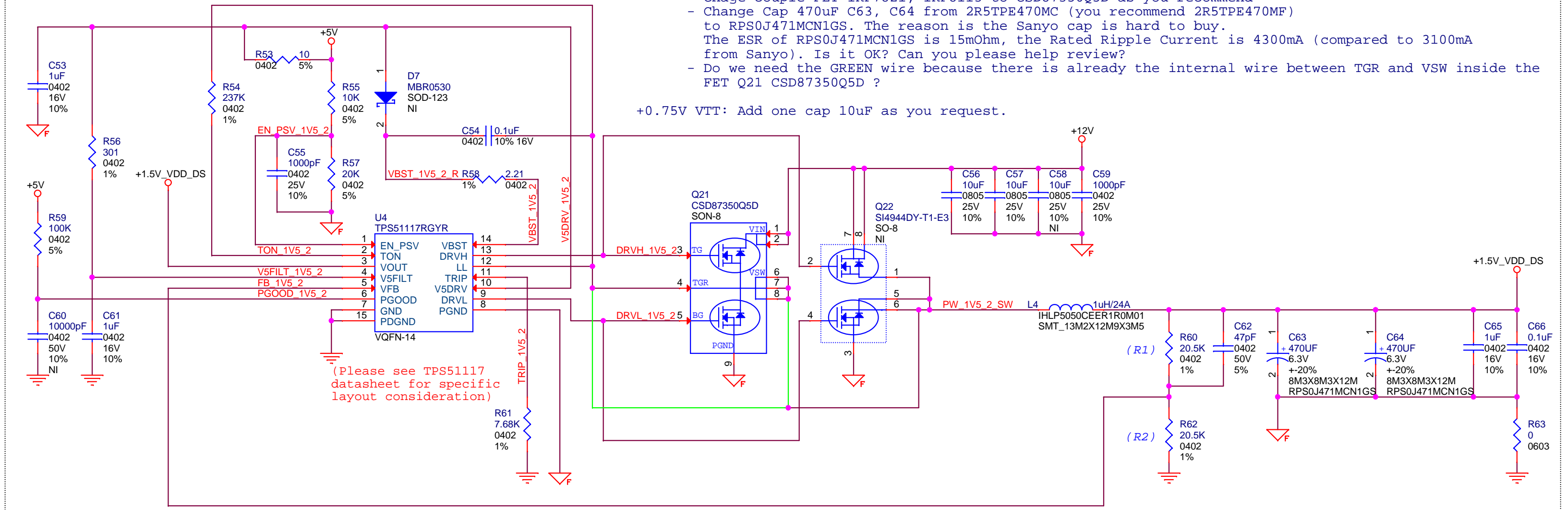


+1.5V Power Rail:

- Change R54 from 200K to 237K --> set switching frequency = 330KHz
- Change couple FET IRF7821, IRF8113 to CSD87350Q5D as you recommend
- Change Cap 470uF C63, C64 from 2R5TPE470MC (you recommend 2R5TPE470MF) to RPS0J471MCN1GS. The reason is the Sanyo cap is hard to buy. The ESR of RPS0J471MCN1GS is 15mOhm, the Rated Ripple Current is 4300mA (compared to 3100mA from Sanyo). Is it OK? Can you please help review?
- Do we need the GREEN wire because there is already the internal wire between TGR and VSW inside the FET Q21 CSD87350Q5D ?

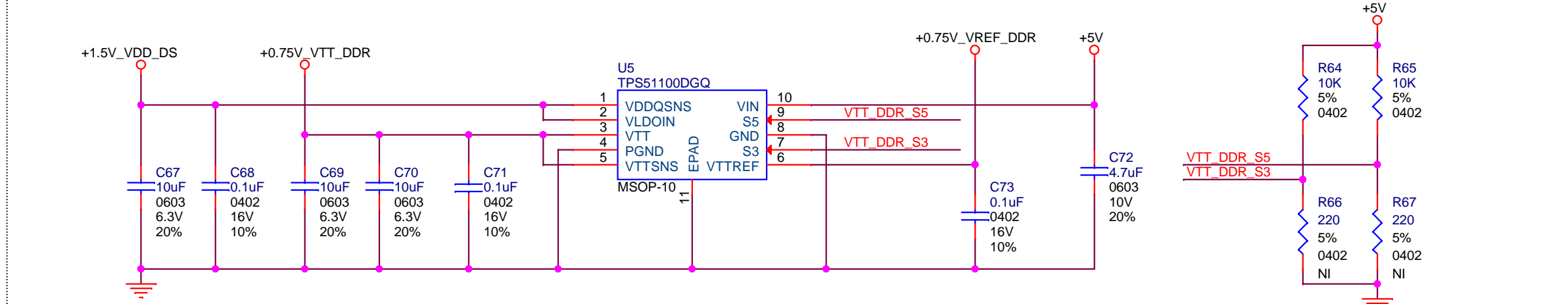
+1.5V @ 10A Max
 $(V_{OUT} = 0.75 \times (1 + R1/R2))$

+0.75V VTT: Add one cap 10uF as you request.

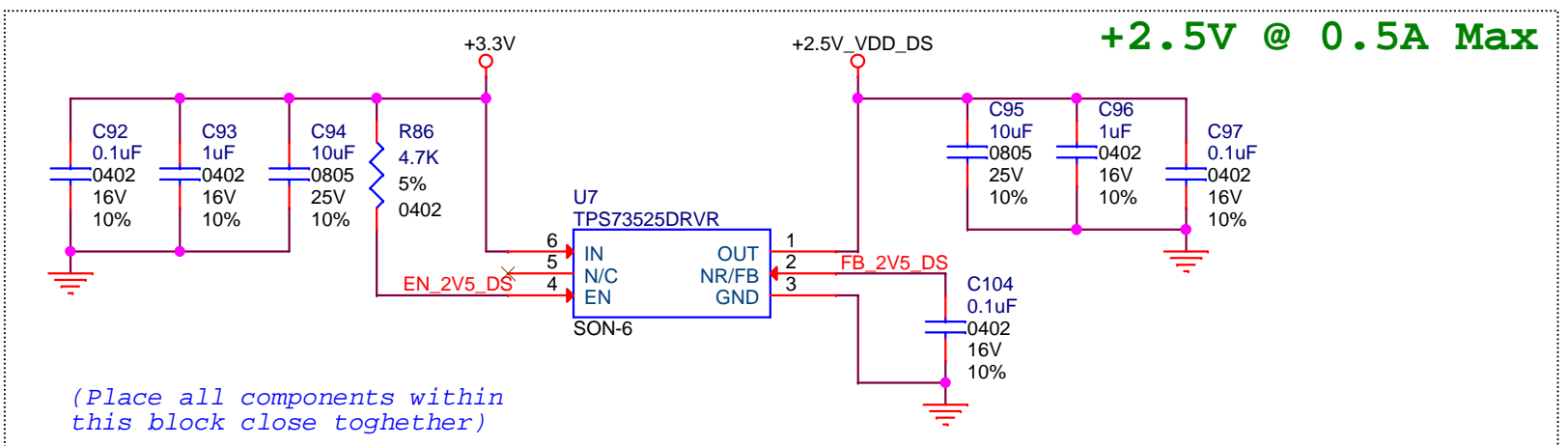
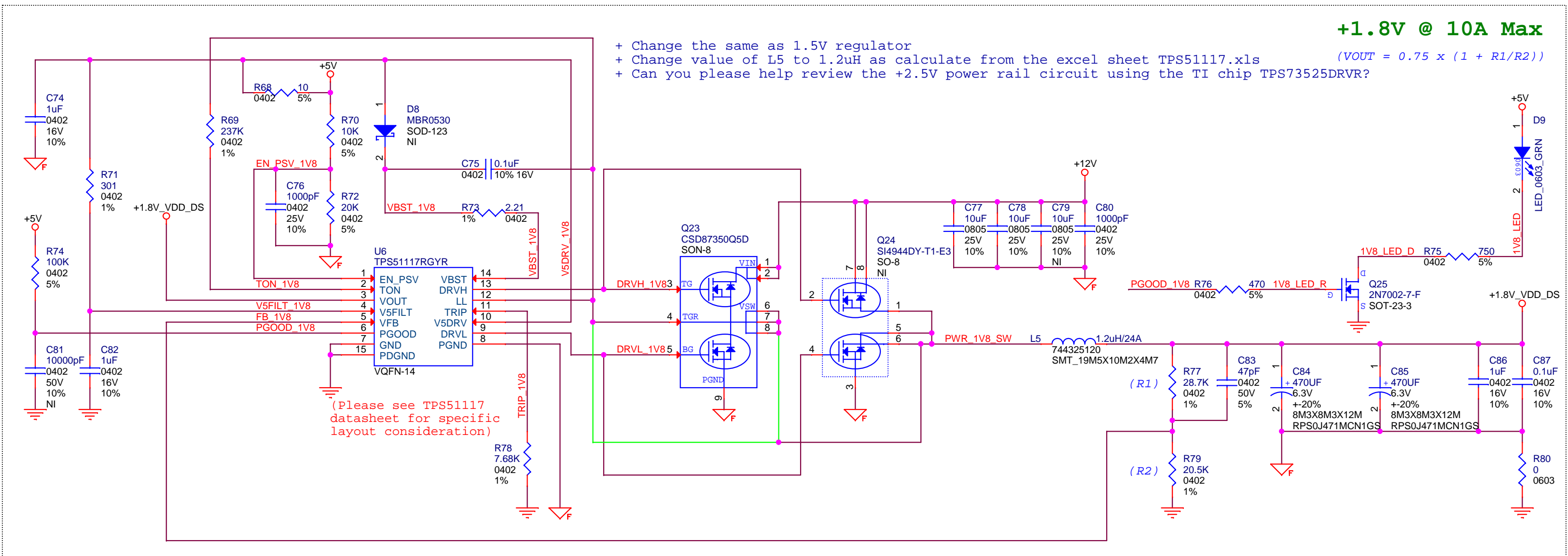


(Please see TPS51117 datasheet for specific layout consideration)

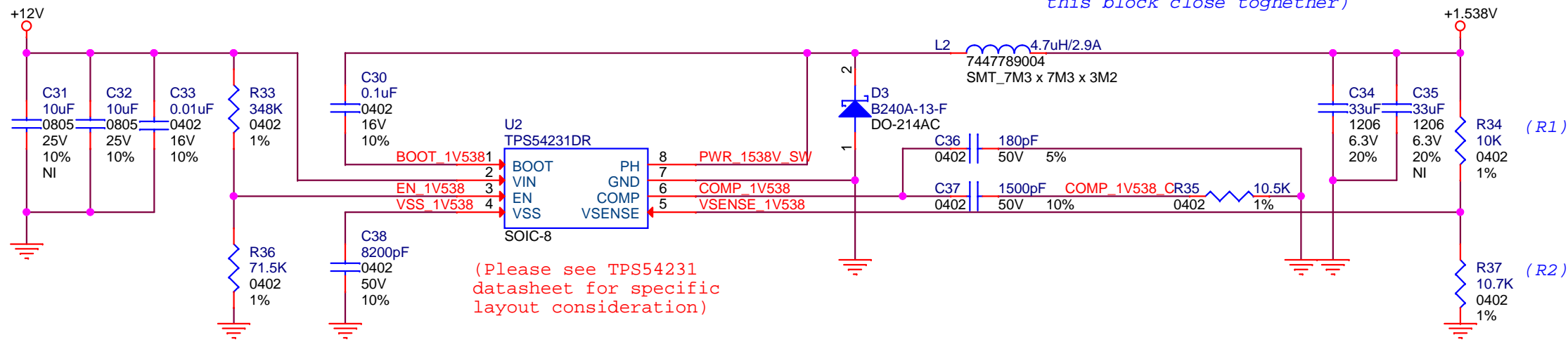
+0.75V_VTT @ 3A Max



NOTE:
 Route VTTSENS signal directly from DDR and separate from VTT signal
 Refer to TPS51100 for specific routing



(Place all components within this block close together)

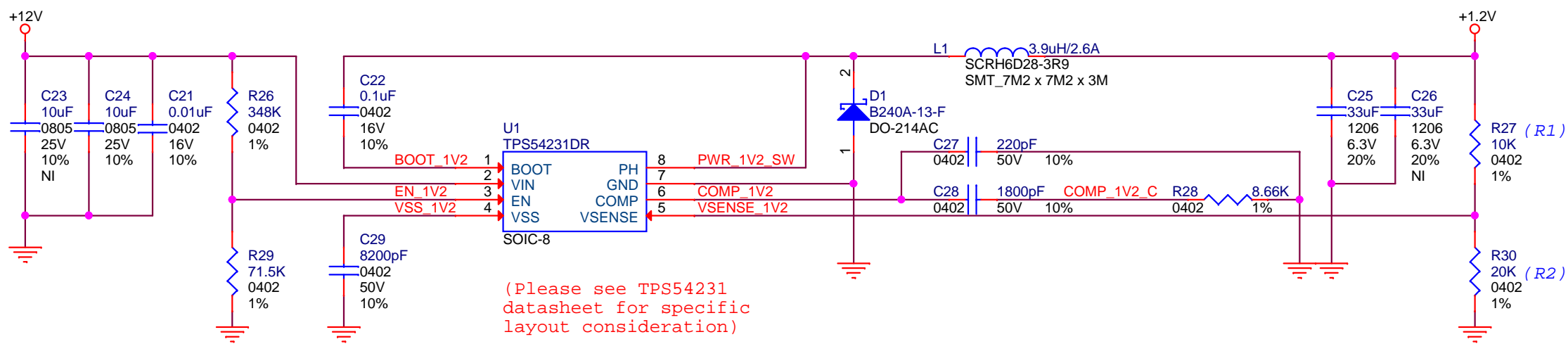


+1.538V @ 2A Max

$$(V_{OUT} = 0.8 \times (1 + R1/R2))$$

(Please see TPS54231 datasheet for specific layout consideration)

(Place all components within this block close together)



+1.2V @ 2A Max

$$(V_{OUT} = 0.8 \times (1 + R1/R2))$$

(Please see TPS54231 datasheet for specific layout consideration)

+ 1.538V power rail: change L2 to Wurth 7447789004 4.7uH, IDC = 2.9A, Isat = 3.9A. Is it OK?

+ 1.2V power rail: change L1 to SCRH6D28-3R9 3.9uH, IDC= 2.6A, Isat = 2.6A. Is it OK?