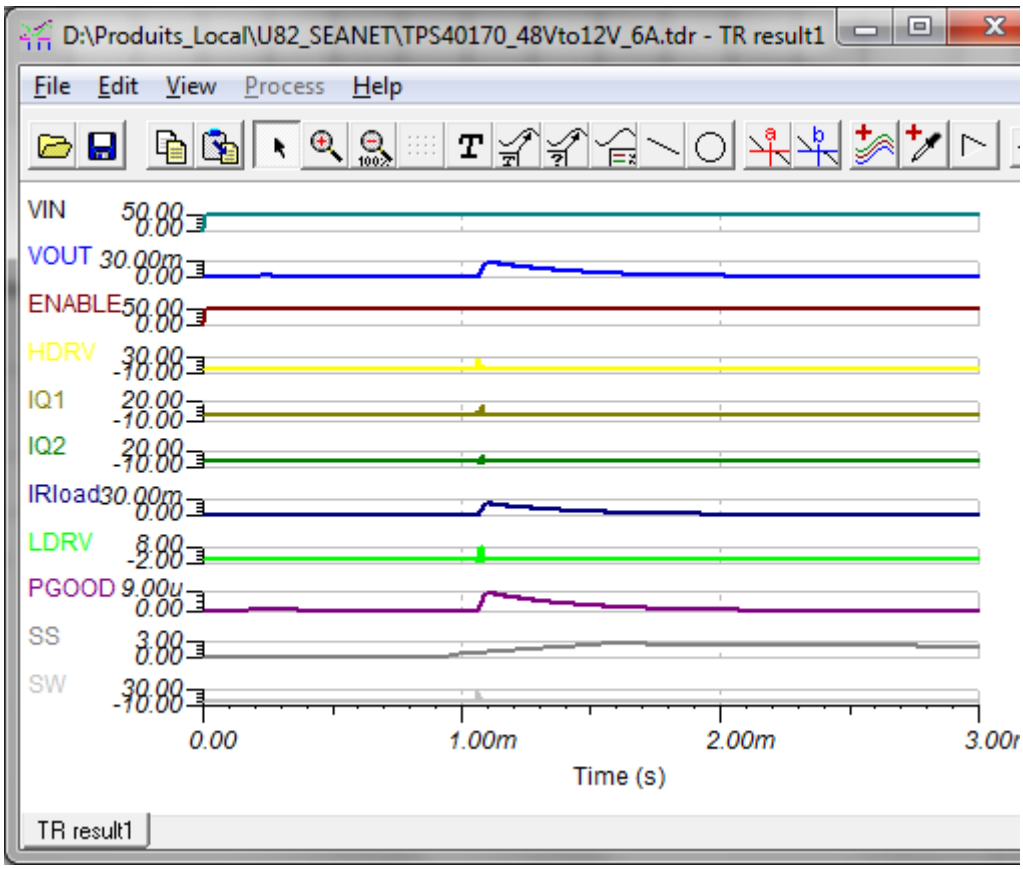
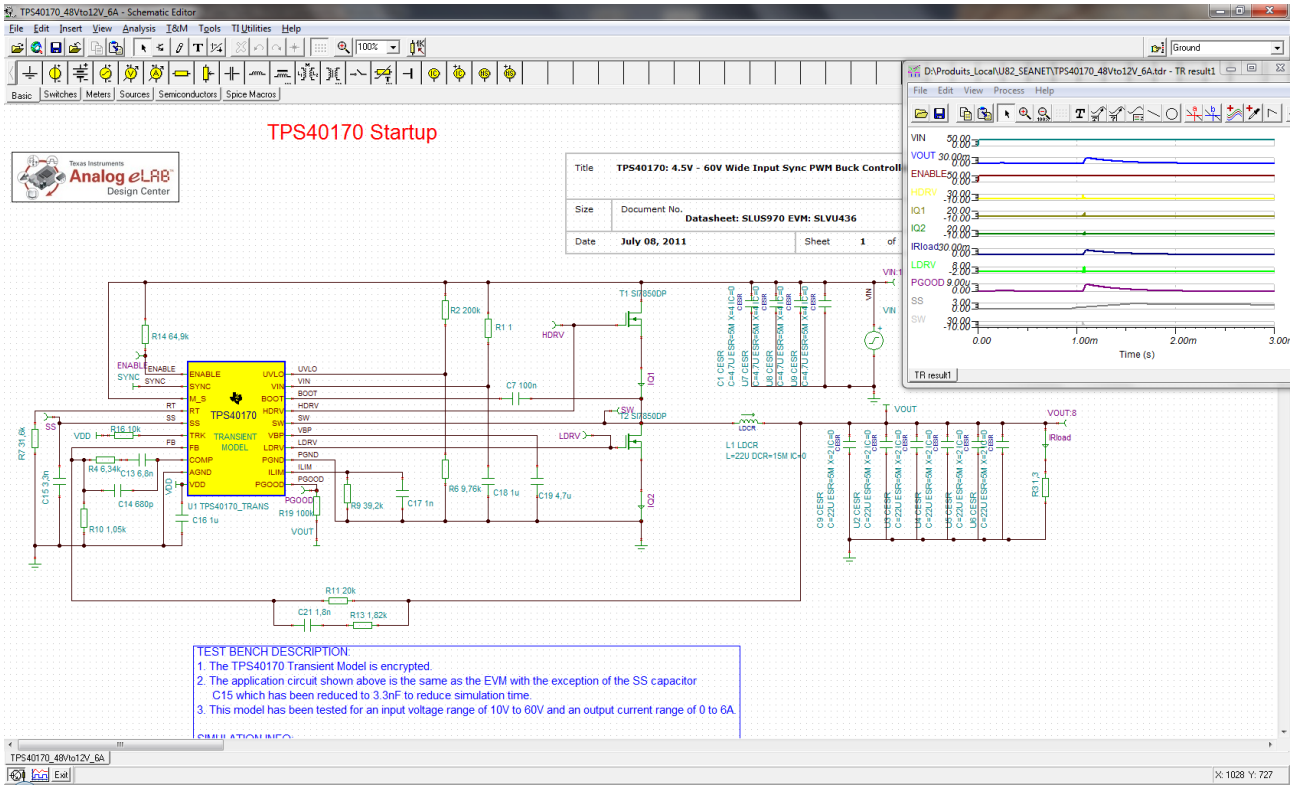

D:\Produits_Local\U82_SEANET\TPS40170_48Vto12V_6A.tdr - TR result1
File Edit View Process Help
VIN
VOUT
ENABLE
HDRV
IQ1
IQ2
IRload
LDRV
PGOOD
SS
SW
0.00
1.00m
2.00m
Time (s)
TR result1


TPS40170_48Vto12V_6A - Schematic Editor
TPS40170 Startup
Analog eLAB Design Center
Title TPS40170: 4.5V - 60V Wide Input Sync PWM Buck Controll
Size Document No. Datasheet: SLUS970 EVM: SLVU436
Date July 08, 2011 Sheet 1 of
TEST BENCH DESCRIPTION:
1. The TPS40170 Transient Model is encrypted.
2. The application circuit shown above is the same as the EVM with the exception of the SS capacitor C15 which has been reduced to 3.3nF to reduce simulation time.
3. This model has been tested for an input voltage range of 10V to 60V and an output current range of 0 to 6A.