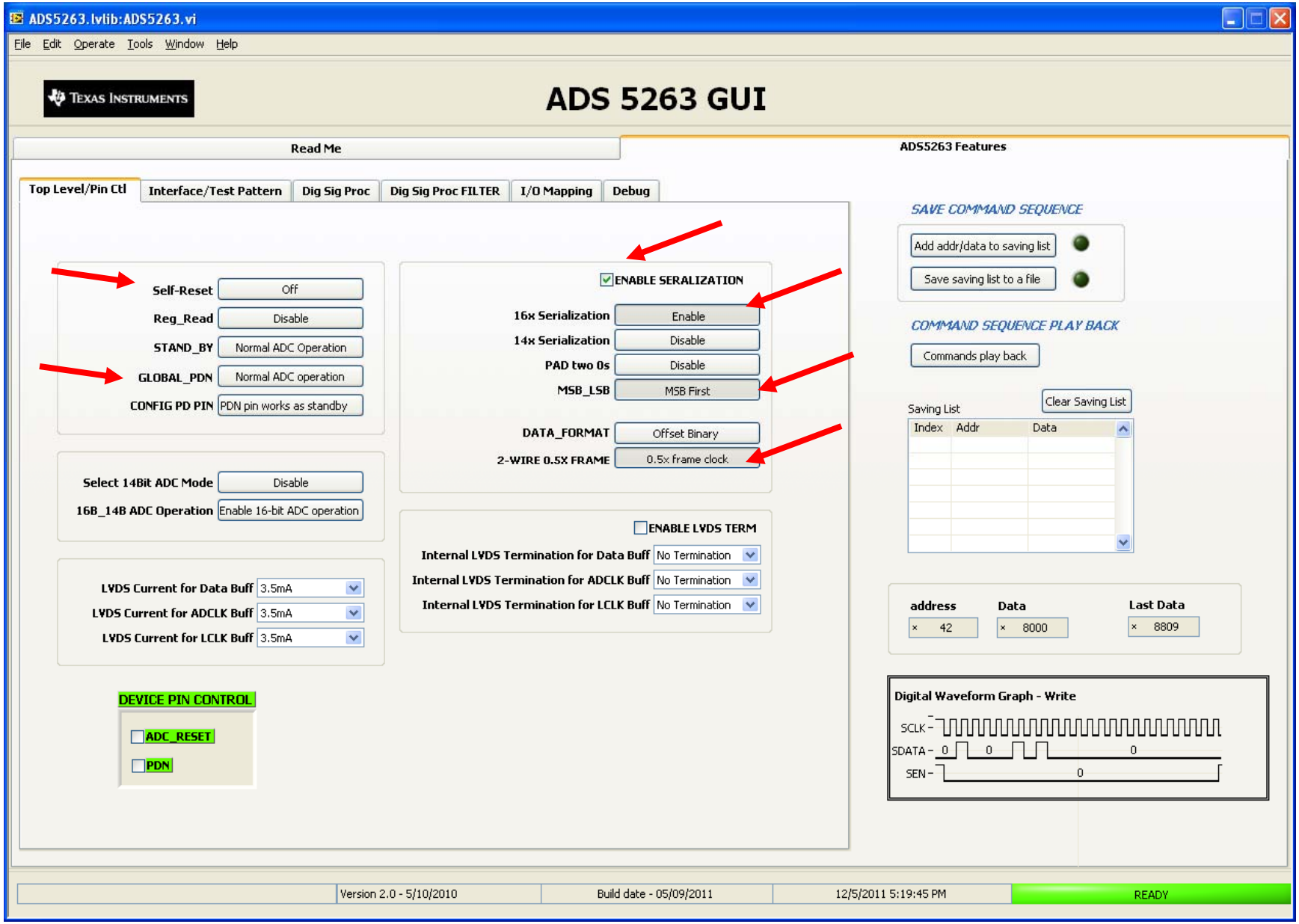
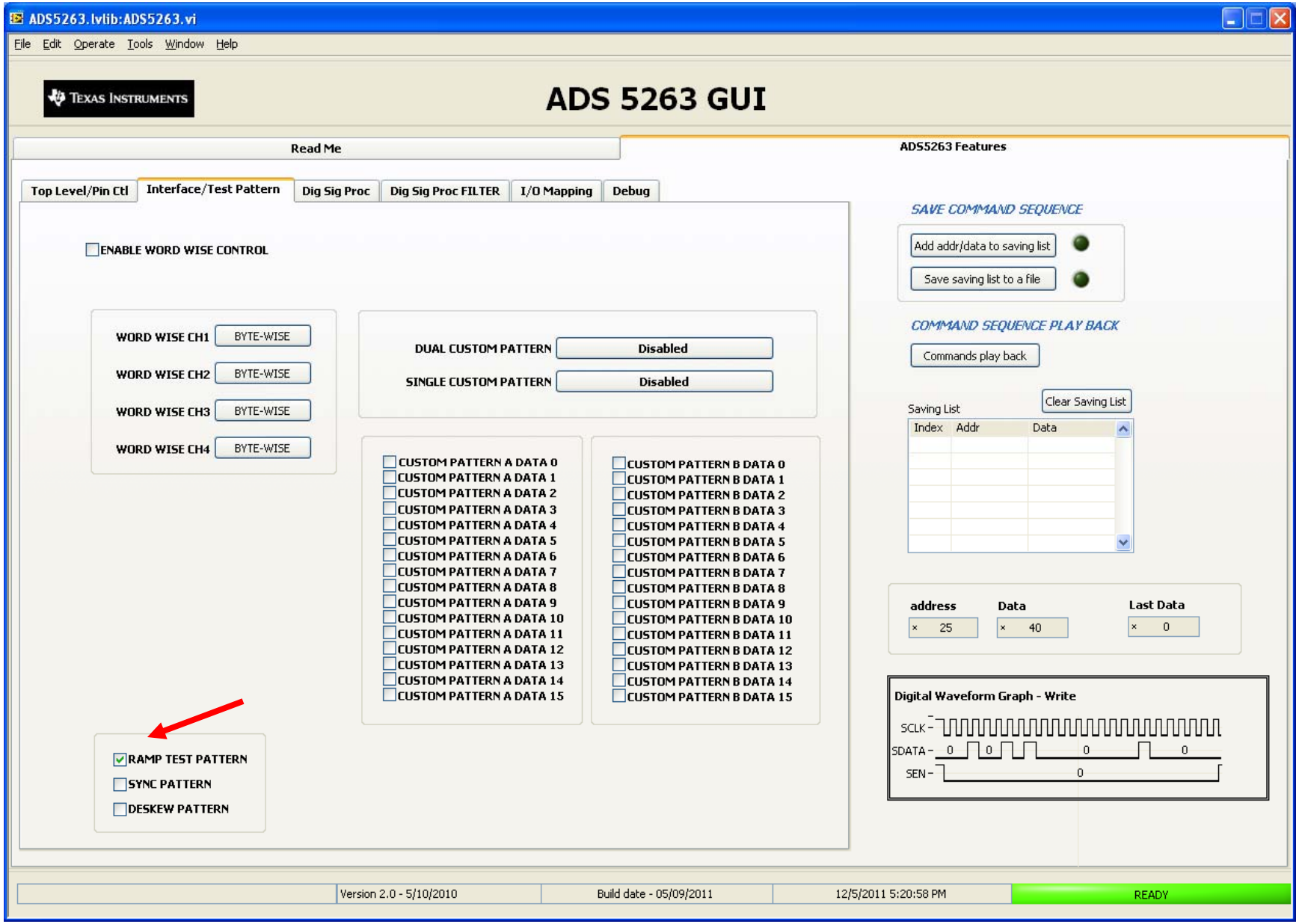
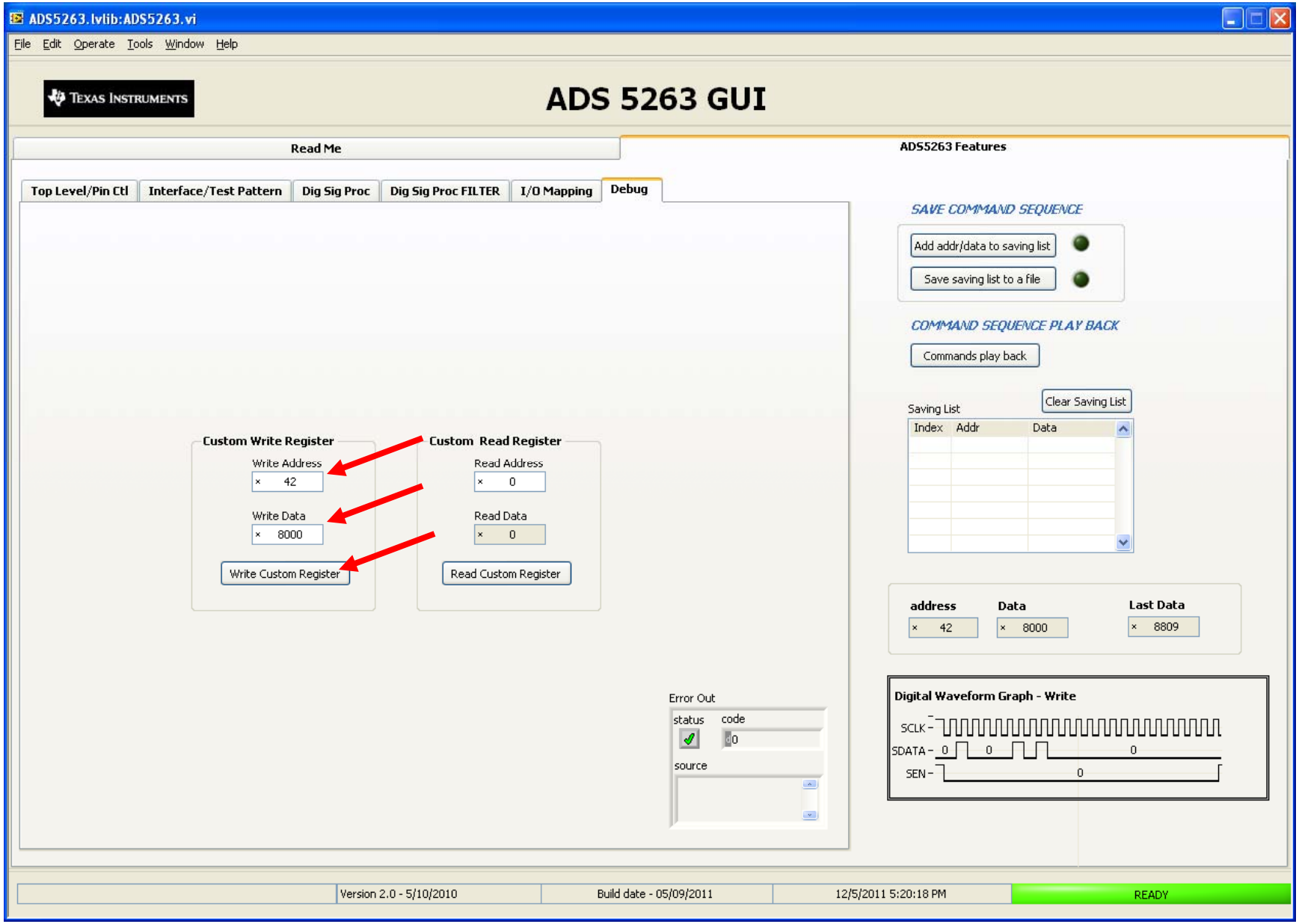
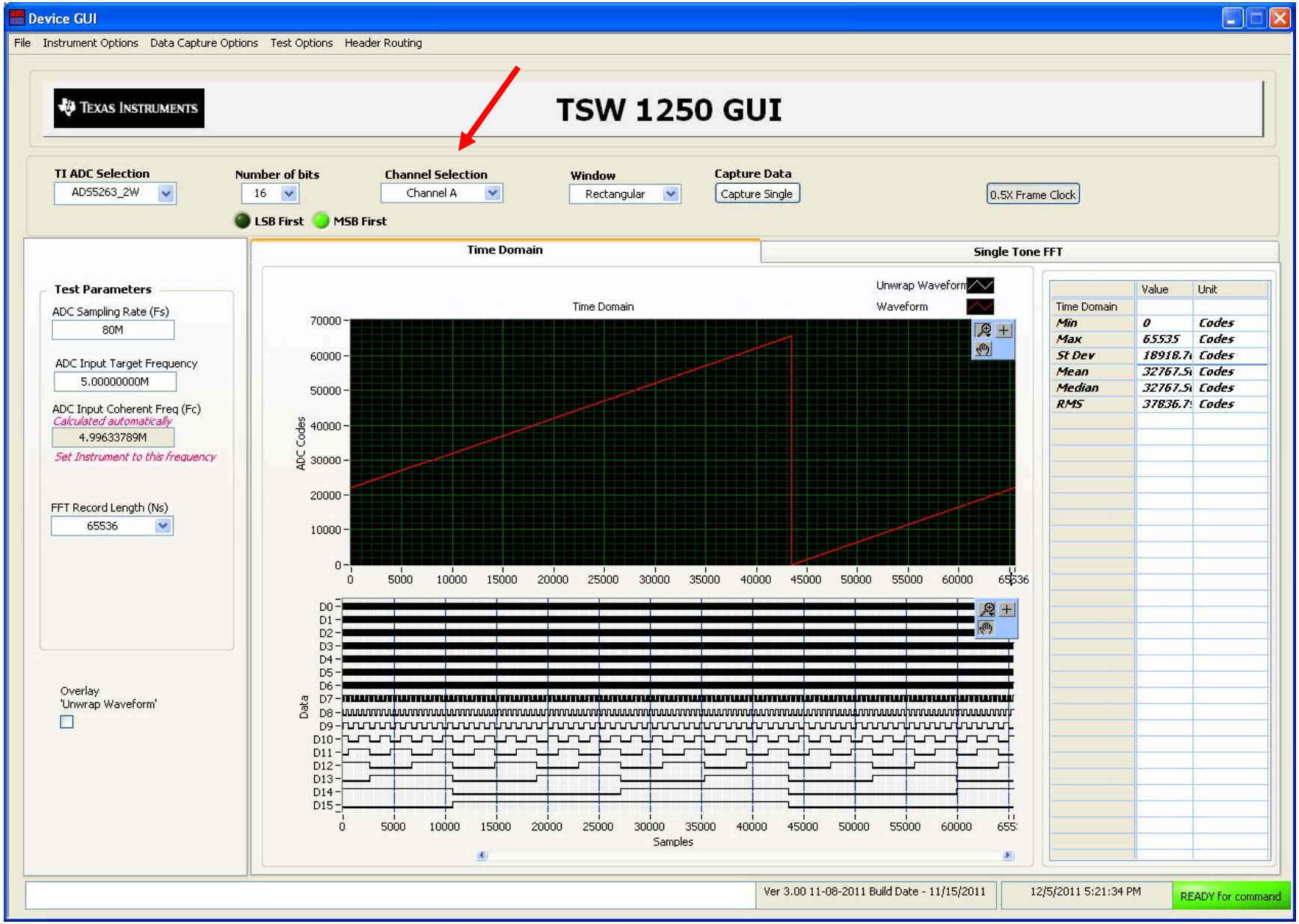
ADS5263EVM Connected to TSW1250EVM

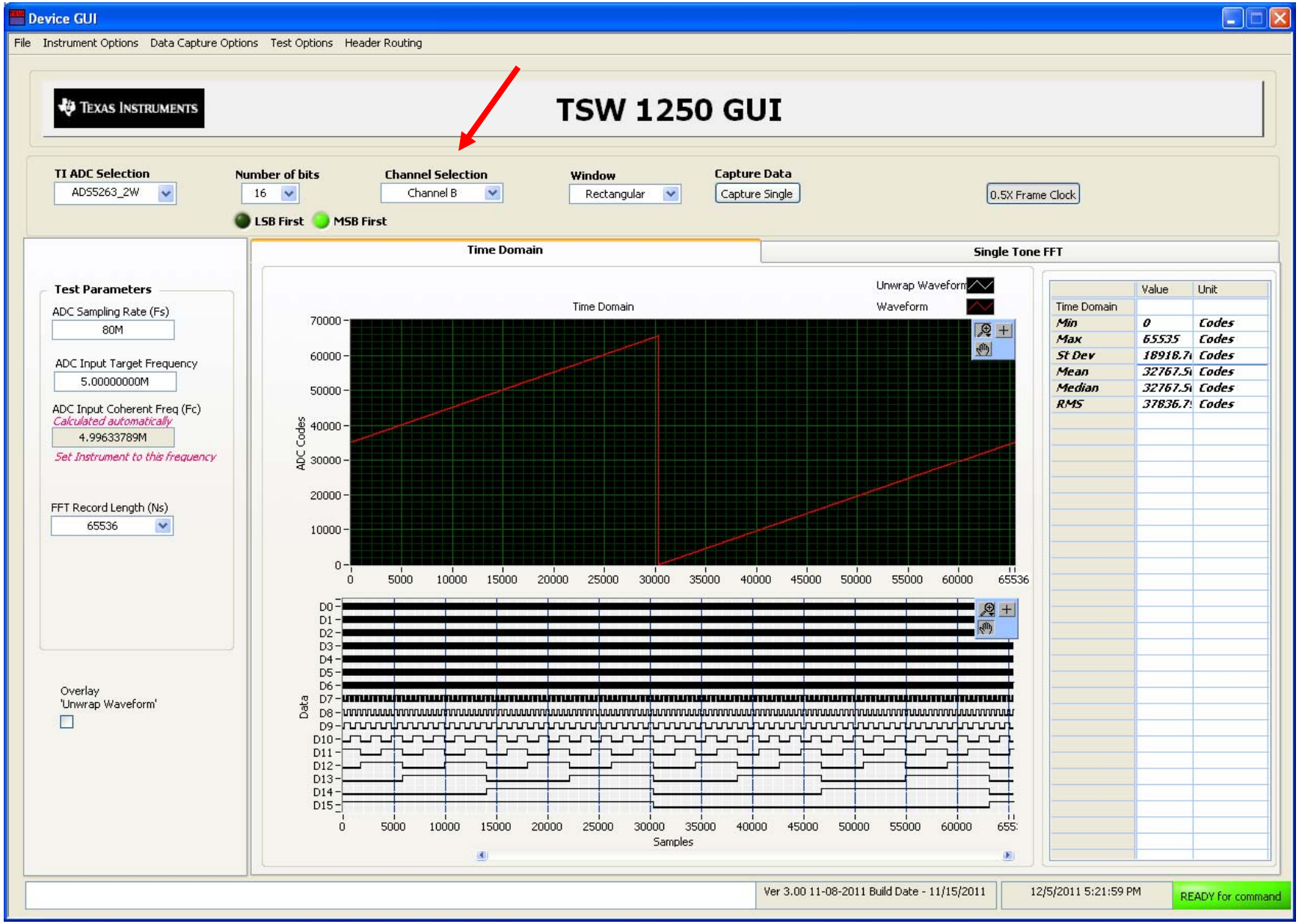
Texas Instruments MBU

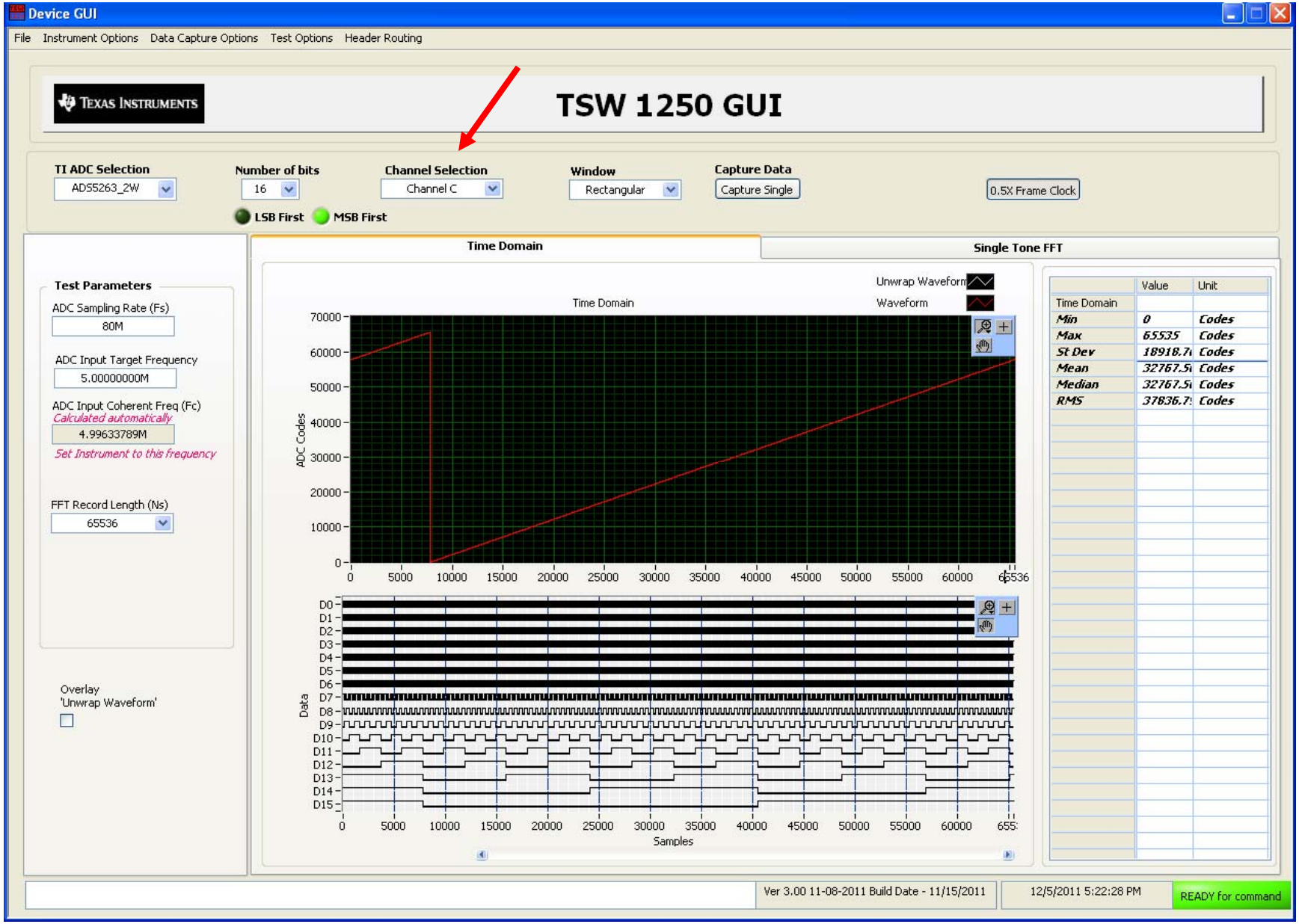

ADS5263.lvlib:ADS5263.vi
File Edit Operate Tools Window Help
TEXAS INSTRUMENTS
ADS 5263 GUI
Read Me
ADS5263 Features
Top Level/Pin Ctl
Interface/Test Pattern
Dig Sig Proc
Dig Sig Proc FILTER
I/O Mapping
Debug
Self-Reset
Off
Reg_Read
Disable
STAND_BY
Normal ADC Operation
GLOBAL_PDN
Normal ADC operation
CONFIG PD PIN
PDN pin works as standby
ENABLE SERALIZATION
16x Serialization
Enable
14x Serialization
Disable
PAD two 0s
Disable
MSB_LSB
MSB First
DATA_FORMAT
Offset Binary
2-WIRE 0.5X FRAME
0.5x frame clock
Select 14Bit ADC Mode
Disable
16B_14B ADC Operation
Enable 16-bit ADC operation
ENABLE LVDS TERM
Internal LVDS Termination for Data Buff
No Termination
Internal LVDS Termination for ADCLK Buff
No Termination
Internal LVDS Termination for LCLK Buff
No Termination
LVDS Current for Data Buff
3.5mA
LVDS Current for ADCLK Buff
3.5mA
LVDS Current for LCLK Buff
3.5mA
DEVICE PIN CONTROL
ADC_RESET
PDN
SAVE COMMAND SEQUENCE
Add addr/data to saving list
Save saving list to a file
COMMAND SEQUENCE PLAY BACK
Commands play back
Saving List
Clear Saving List
Index
Addr
Data
address
Data
Last Data
42
8000
8809
Digital Waveform Graph - Write
SCLK
SDATA
SEN
Version 2.0 - 5/10/2010
Build date - 05/09/2011
12/5/2011 5:19:45 PM
READY

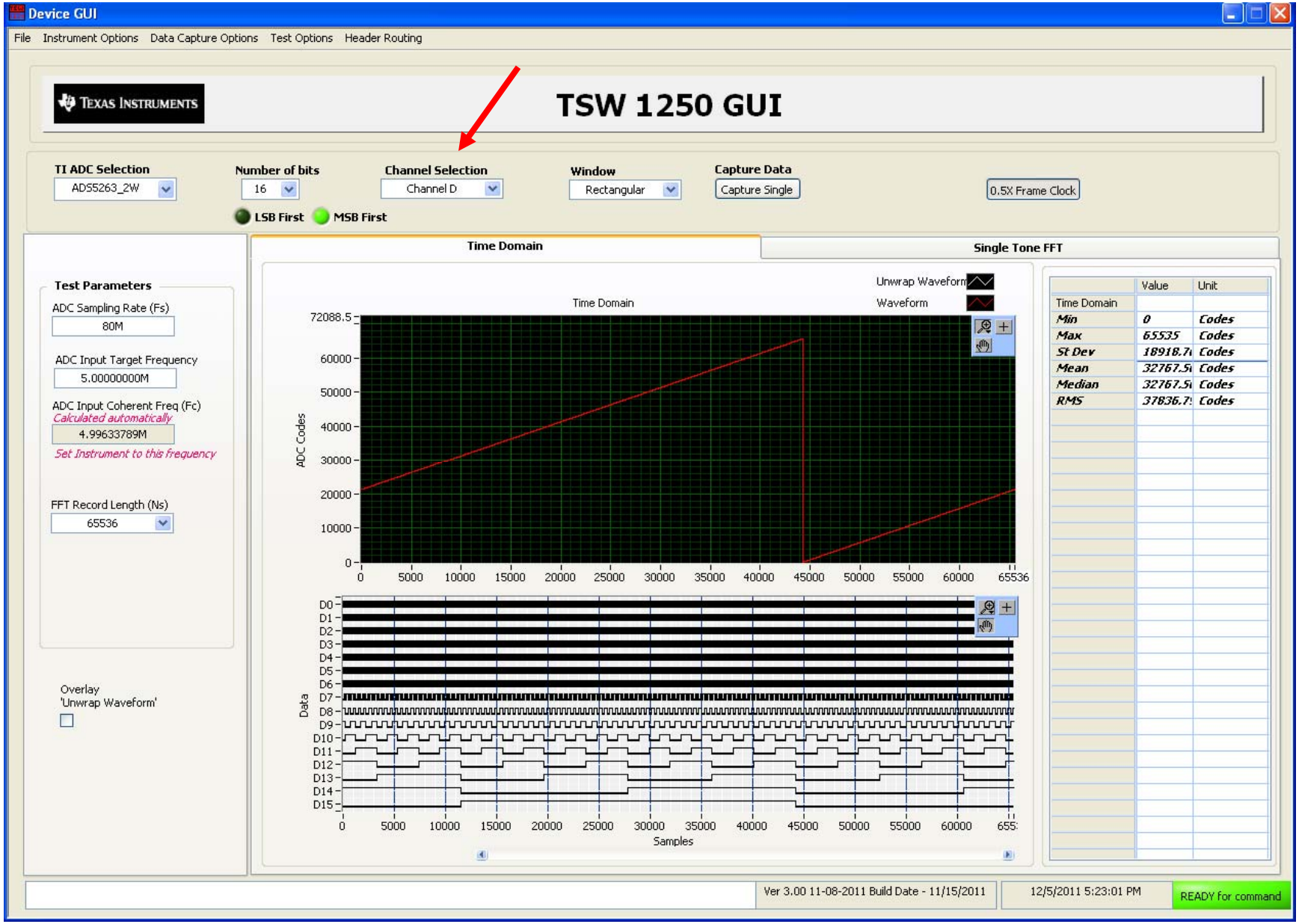

ADS5263.lvlib:ADS5263.vi
File Edit Operate Tools Window Help
Texas Instruments
ADS 5263 GUI
Read Me
ADS5263 Features
Top Level/Pin Ctl
Interface/Test Pattern
Dig Sig Proc
Dig Sig Proc FILTER
I/O Mapping
Debug
ENABLE WORD WISE CONTROL
WORD WISE CH1
WORD WISE CH2
WORD WISE CH3
WORD WISE CH4
BYTE-WISE
DUAL CUSTOM PATTERN
SINGLE CUSTOM PATTERN
Disabled
CUSTOM PATTERN A DATA 0
CUSTOM PATTERN A DATA 1
CUSTOM PATTERN A DATA 2
CUSTOM PATTERN A DATA 3
CUSTOM PATTERN A DATA 4
CUSTOM PATTERN A DATA 5
CUSTOM PATTERN A DATA 6
CUSTOM PATTERN A DATA 7
CUSTOM PATTERN A DATA 8
CUSTOM PATTERN A DATA 9
CUSTOM PATTERN A DATA 10
CUSTOM PATTERN A DATA 11
CUSTOM PATTERN A DATA 12
CUSTOM PATTERN A DATA 13
CUSTOM PATTERN A DATA 14
CUSTOM PATTERN A DATA 15
CUSTOM PATTERN B DATA 0
CUSTOM PATTERN B DATA 1
CUSTOM PATTERN B DATA 2
CUSTOM PATTERN B DATA 3
CUSTOM PATTERN B DATA 4
CUSTOM PATTERN B DATA 5
CUSTOM PATTERN B DATA 6
CUSTOM PATTERN B DATA 7
CUSTOM PATTERN B DATA 8
CUSTOM PATTERN B DATA 9
CUSTOM PATTERN B DATA 10
CUSTOM PATTERN B DATA 11
CUSTOM PATTERN B DATA 12
CUSTOM PATTERN B DATA 13
CUSTOM PATTERN B DATA 14
CUSTOM PATTERN B DATA 15
RAMP TEST PATTERN
SYNC PATTERN
DESKEW PATTERN
SAVE COMMAND SEQUENCE
Add addr/data to saving list
Save saving list to a file
COMMAND SEQUENCE PLAY BACK
Commands play back
Saving List
Clear Saving List
Index
Addr
Data
address
Data
Last Data
25
40
0
Digital Waveform Graph - Write
SCLK
SDATA
SEN
Version 2.0 - 5/10/2010
Build date - 05/09/2011
12/5/2011 5:20:58 PM
READY

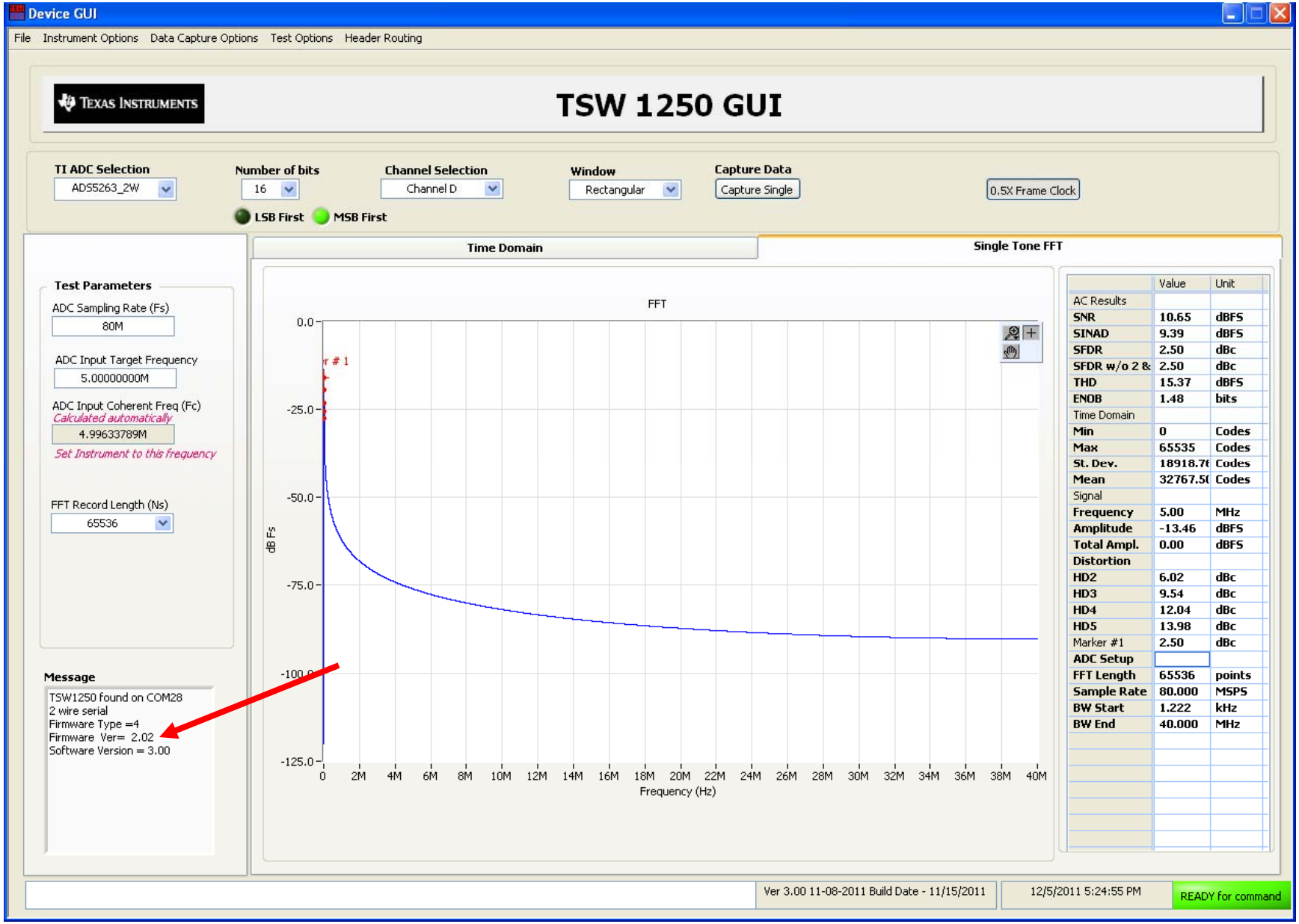

ADS5263.lvlib:ADS5263.vi
File Edit Operate Tools Window Help
Texas Instruments
ADS 5263 GUI
Read Me
ADS5263 Features
Top Level/Pin Ctl
Interface/Test Pattern
Dig Sig Proc
Dig Sig Proc FILTER
I/O Mapping
Debug
SAVE COMMAND SEQUENCE
Add addr/data to saving list
Save saving list to a file
COMMAND SEQUENCE PLAY BACK
Commands play back
Clear Saving List
Saving List
Index
Addr
Data
Custom Write Register
Write Address
42
Write Data
8000
Write Custom Register
Custom Read Register
Read Address
0
Read Data
0
Read Custom Register
address
Data
Last Data
42
8000
8809
Error Out
status
code
source
Digital Waveform Graph - Write
SCLK
SDATA
SEN
Version 2.0 - 5/10/2010
Build date - 05/09/2011
12/5/2011 5:20:18 PM
READY


Device GUI
File
Instrument Options
Data Capture Options
Test Options
Header Routing
Texas Instruments
TSW 1250 GUI
TI ADC Selection
ADS5263_2W
Number of bits
16
Channel Selection
Channel A
Window
Rectangular
Capture Data
Capture Single
0.5X Frame Clock
LSB First
MSB First
Test Parameters
ADC Sampling Rate (Fs)
80M
ADC Input Target Frequency
5.00000000M
ADC Input Coherent Freq (Fc)
Calculated automatically
4.99633789M
Set Instrument to this frequency
FFT Record Length (Ns)
65536
Overlay
'Unwrap Waveform'
Time Domain
Single Tone FFT
Unwrap Waveform
Waveform
Time Domain
ADC Codes
Data
Samples
Value
Unit
Time Domain
Min
Max
St Dev
Mean
Median
RMS
Codes
Ver 3.00 11-08-2011 Build Date - 11/15/2011
12/5/2011 5:21:34 PM
READY for command


Device GUI
File
Instrument Options
Data Capture Options
Test Options
Header Routing
TEXAS INSTRUMENTS
TSW 1250 GUI
TI ADC Selection
ADS5263_2W
Number of bits
16
Channel Selection
Channel B
Window
Rectangular
Capture Data
Capture Single
0.5X Frame Clock
LSB First
MSB First
Test Parameters
ADC Sampling Rate (Fs)
80M
ADC Input Target Frequency
5.00000000M
ADC Input Coherent Freq (Fc)
Calculated automatically
4.99633789M
Set Instrument to this frequency
FFT Record Length (Ns)
65536
Overlay
'Unwrap Waveform'
Time Domain
Single Tone FFT
Unwrap Waveform
Waveform
Time Domain
ADC Codes
Data
Samples
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
Value
Unit
Time Domain
Min
Max
St Dev
Mean
Median
RMS
Codes
Ver 3.00 11-08-2011 Build Date - 11/15/2011
12/5/2011 5:21:59 PM
READY for command


Device GUI
File
Instrument Options
Data Capture Options
Test Options
Header Routing
Texas Instruments
TSW 1250 GUI
TI ADC Selection
ADS5263_2W
Number of bits
16
Channel Selection
Channel C
Window
Rectangular
Capture Data
Capture Single
0.5X Frame Clock
LSB First
MSB First
Time Domain
Single Tone FFT
Test Parameters
ADC Sampling Rate (Fs)
80M
ADC Input Target Frequency
5.00000000M
ADC Input Coherent Freq (Fc)
Calculated automatically
4.99633789M
Set Instrument to this frequency
FFT Record Length (Ns)
65536
Overlay
'Unwrap Waveform'
Unwrap Waveform
Waveform
Time Domain
ADC Codes
Data
Samples
Value
Unit
Time Domain
Min
Max
St Dev
Mean
Median
RMS
Codes
Ver 3.00 11-08-2011 Build Date - 11/15/2011
12/5/2011 5:22:28 PM
READY for command


Device GUI
File Instrument Options Data Capture Options Test Options Header Routing
TEXAS INSTRUMENTS
TSW 1250 GUI
TI ADC Selection
ADS5263_2W
Number of bits
16
Channel Selection
Channel D
Window
Rectangular
Capture Data
Capture Single
0.5X Frame Clock
LSB First
MSB First
Time Domain
Single Tone FFT
Test Parameters
ADC Sampling Rate (Fs)
80M
ADC Input Target Frequency
5.00000000M
ADC Input Coherent Freq (Fc)
Calculated automatically
4.99633789M
Set Instrument to this frequency
FFT Record Length (Ns)
65536
Overlay
'Unwrap Waveform'
Unwrap Waveform
Waveform
Time Domain
ADC Codes
Data
Samples
Value
Unit
Time Domain
Min
Max
St Dev
Mean
Median
RMS
Codes
Ver 3.00 11-08-2011 Build Date - 11/15/2011
12/5/2011 5:23:01 PM
READY for command


Device GUI
File Instrument Options Data Capture Options Test Options Header Routing
TEXAS INSTRUMENTS
TSW 1250 GUI
TI ADC Selection
ADS5263_2W
Number of bits
16
Channel Selection
Channel D
Window
Rectangular
Capture Data
Capture Single
0.5X Frame Clock
LSB First
MSB First
Test Parameters
ADC Sampling Rate (Fs)
80M
ADC Input Target Frequency
5.00000000M
ADC Input Coherent Freq (Fc)
Calculated automatically
4.99633789M
Set Instrument to this frequency
FFT Record Length (Ns)
65536
Message
TSW1250 found on COM28
2 wire serial
Firmware Type =4
Firmware Ver= 2.02
Software Version = 3.00
Time Domain
Single Tone FFT
FFT
dB Fs
Frequency (Hz)
0.0
-25.0
-50.0
-75.0
-125.0
0
2M
4M
6M
8M
10M
12M
14M
16M
18M
20M
22M
24M
26M
28M
30M
32M
34M
36M
38M
40M
Value
Unit
AC Results
SNR 10.65 dBFS
SINAD 9.39 dBFS
SFDR 2.50 dBc
SFDR w/o 2 & 2.50 dBc
THD 15.37 dBFS
ENOB 1.48 bits
Time Domain
Min 0 Codes
Max 65535 Codes
St. Dev. 18918.7 Codes
Mean 32767.5 Codes
Signal
Frequency 5.00 MHz
Amplitude -13.46 dBFS
Total Ampl. 0.00 dBFS
Distortion
HD2 6.02 dBc
HD3 9.54 dBc
HD4 12.04 dBc
HD5 13.98 dBc
Marker #1 2.50 dBc
ADC Setup
FFT Length 65536 points
Sample Rate 80.000 MSPS
BW Start 1.222 kHz
BW End 40.000 MHz
Ver 3.00 11-08-2011 Build Date - 11/15/2011
12/5/2011 5:24:55 PM
READY for command