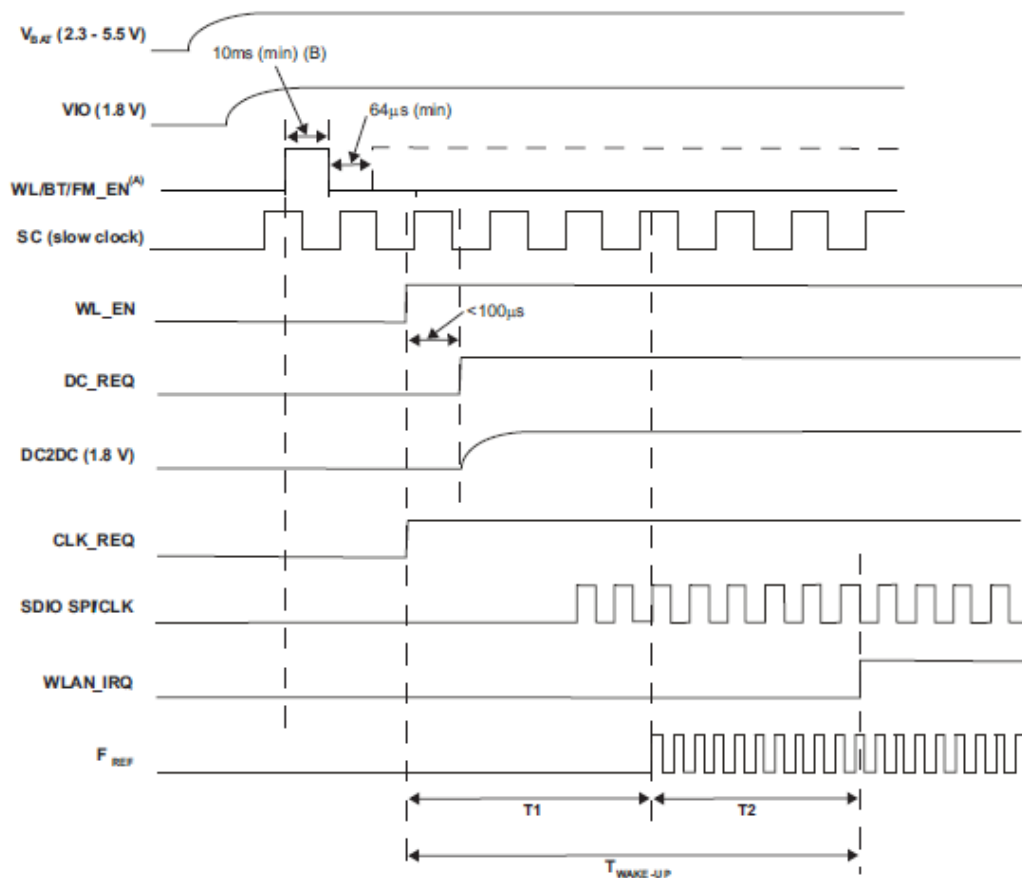


Power Up Requirements



- A. After this sequence is completed, the device is in the low VIO-leakage state while in shutdown.
 B. 10ms enable pulse not required for PG3.0 and higher devices.

WL1271 Powerup Sequence Requirements

The following sequence describes device powerup from shutdown. Only the WLAN Core is enabled; the BT and FM cores are disabled.

- No signals are allowed on the IO pins if no IO power supplied, because the IOs are not 'fail safe'. Exceptions are CLK_REQ_OUT, SLOWCLK, XTALP and AUD_XXX, which are failsafe and can tolerate external voltages with no VDDS and DC2DC.
- V_{BAT} , V_{IO} and SLOWCLK must be available before WL_EN.
- $T_{wake-up} = T_1 + T_2$

The duration of T_1 is defined as the time from WL_EN=high until Fref is valid for the SoC

$T_1 \approx 55ms$

The duration of T_2 depends on:

- Operating system
- Host enumeration for the SDIO/WSPI
- PLL configuration
- Firmware download
- Releasing the core from reset
- Firmware initialization