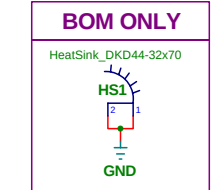
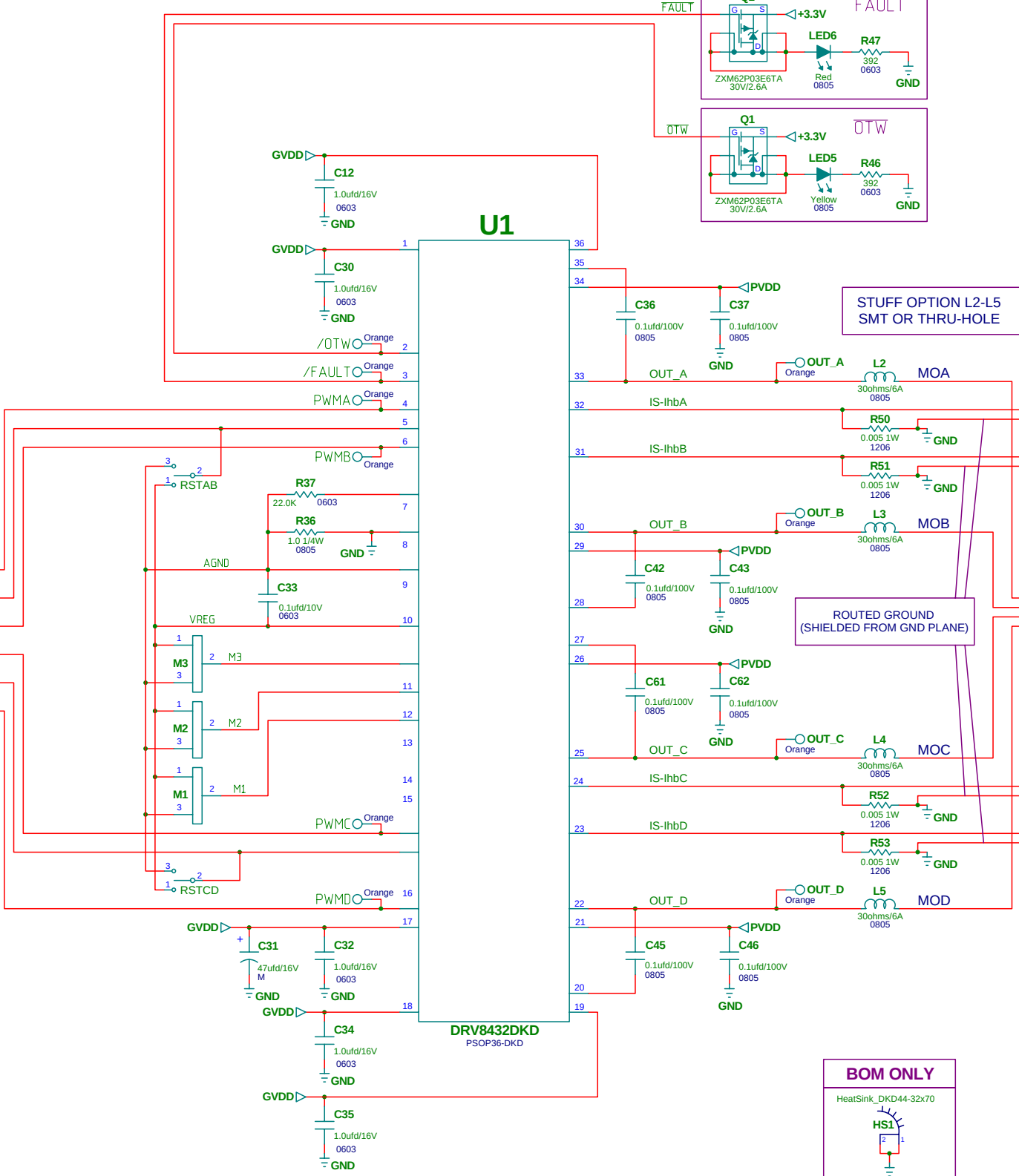
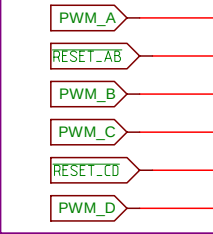


DRV8432DKD EVALUATION BOARD

FROM J1



ROUTED GROUND
(SHIELDED FROM GND PLANE)

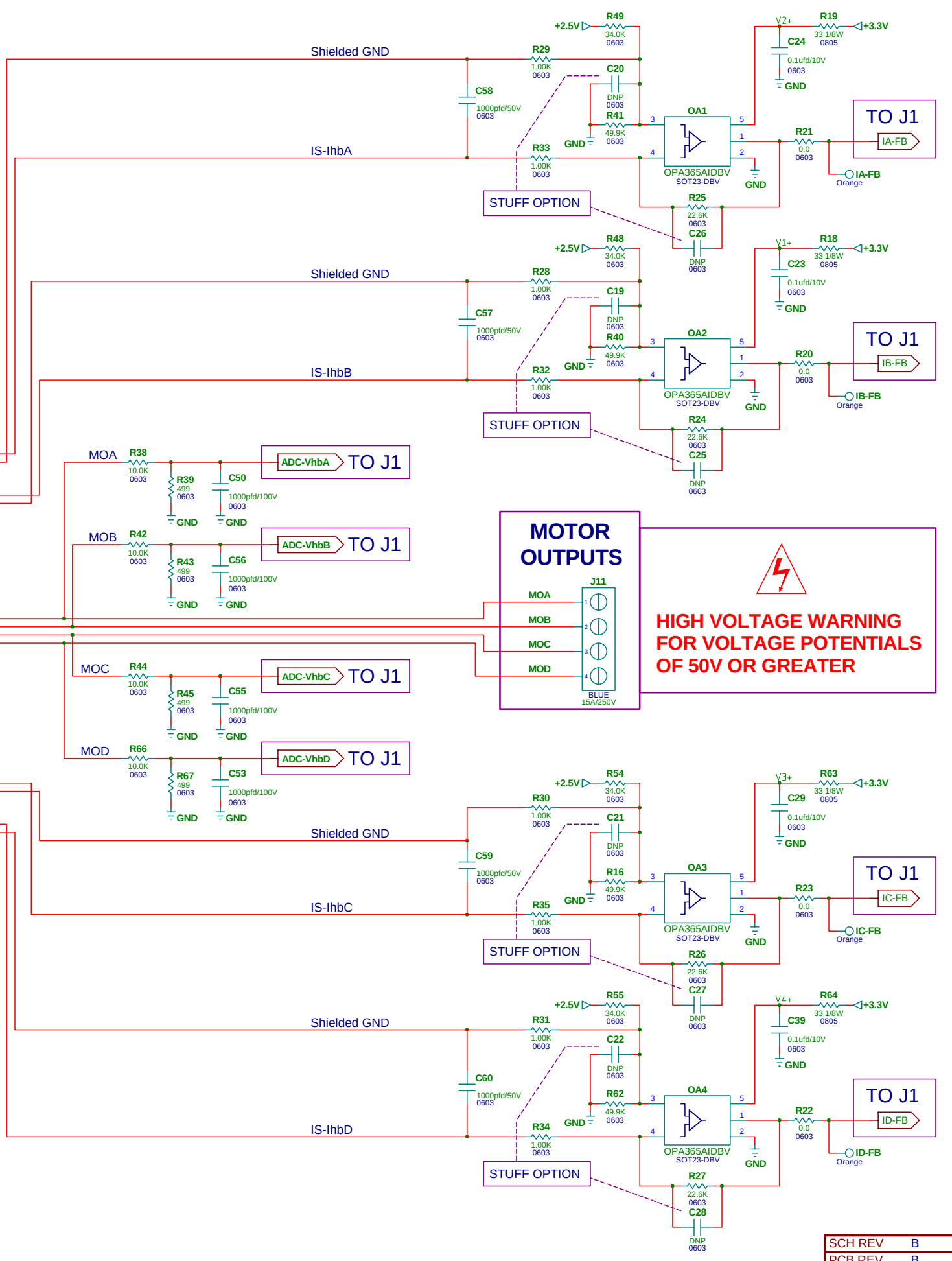
STUFF OPTION L2-L5
SMT OR THRU-HOLE

MOTOR OUTPUTS

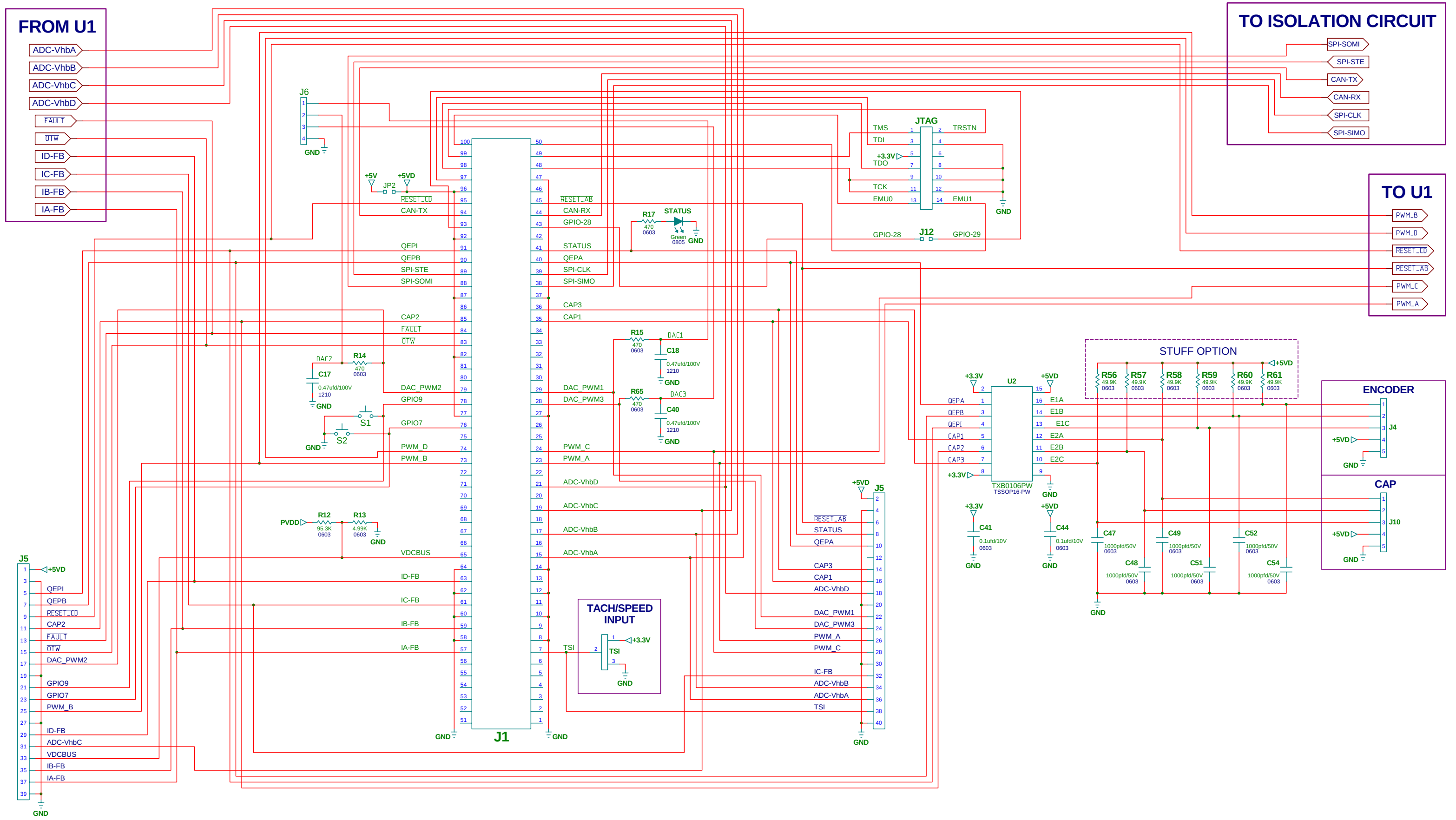
J11

MOA
MOB
MOC
MOD

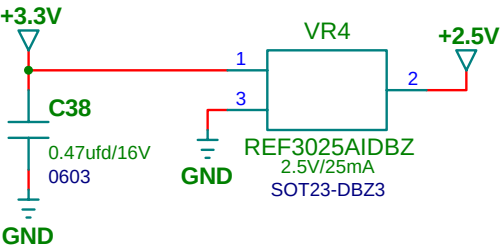
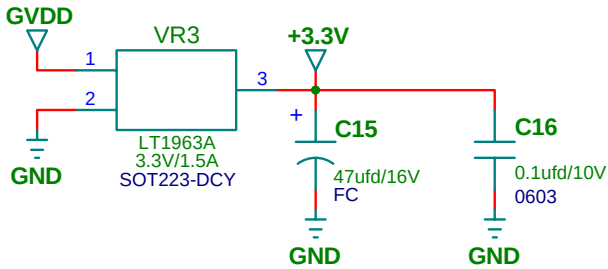
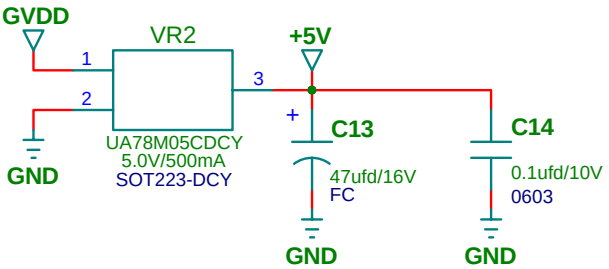
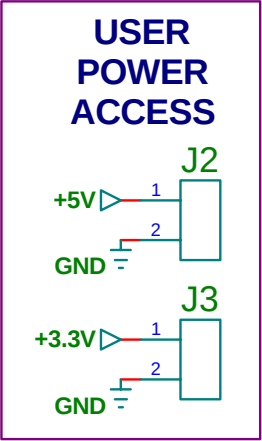
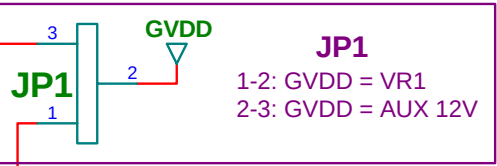
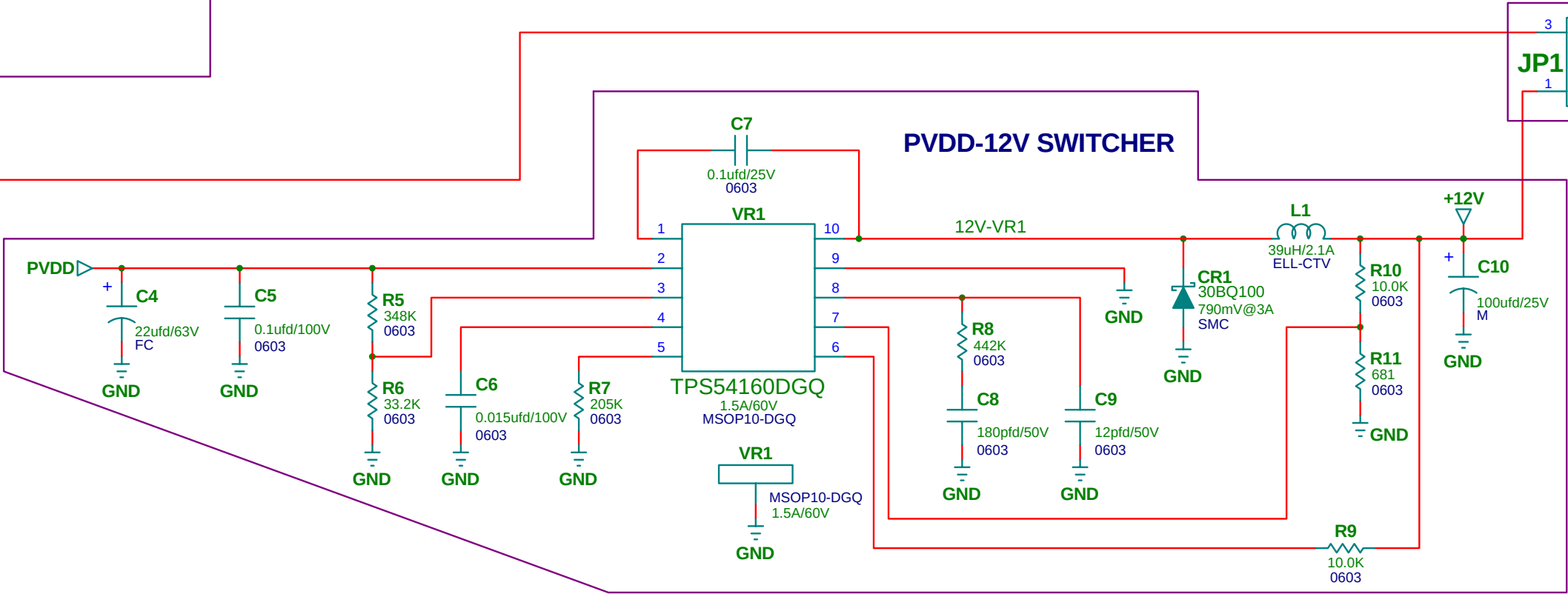
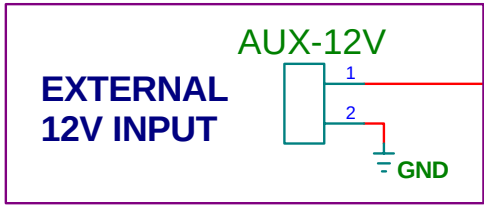
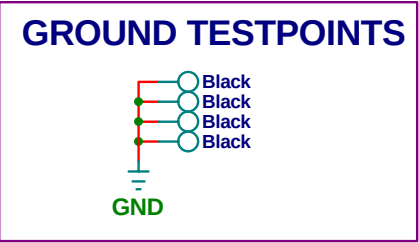
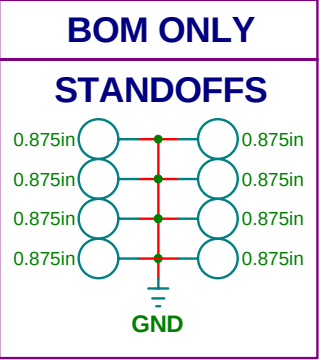
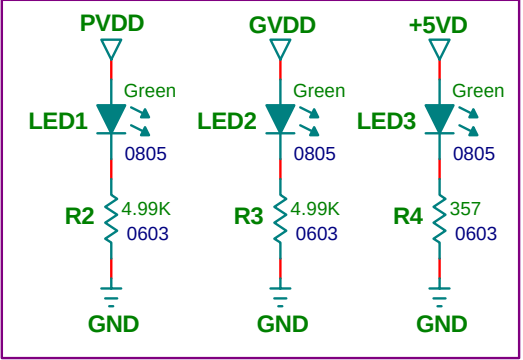
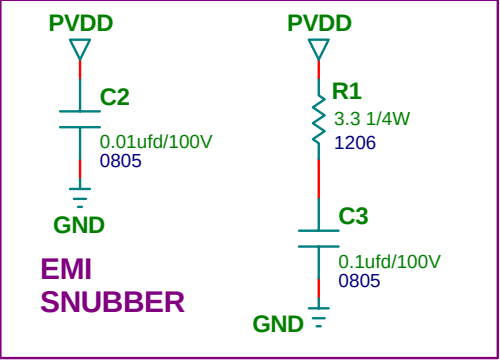
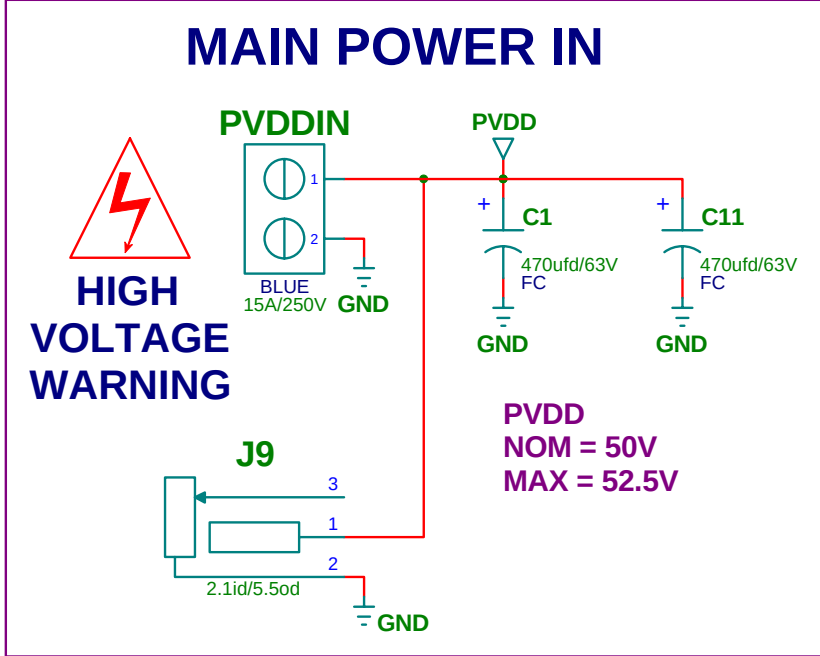
**HIGH VOLTAGE WARNING
FOR VOLTAGE POTENTIALS
OF 50V OR GREATER**



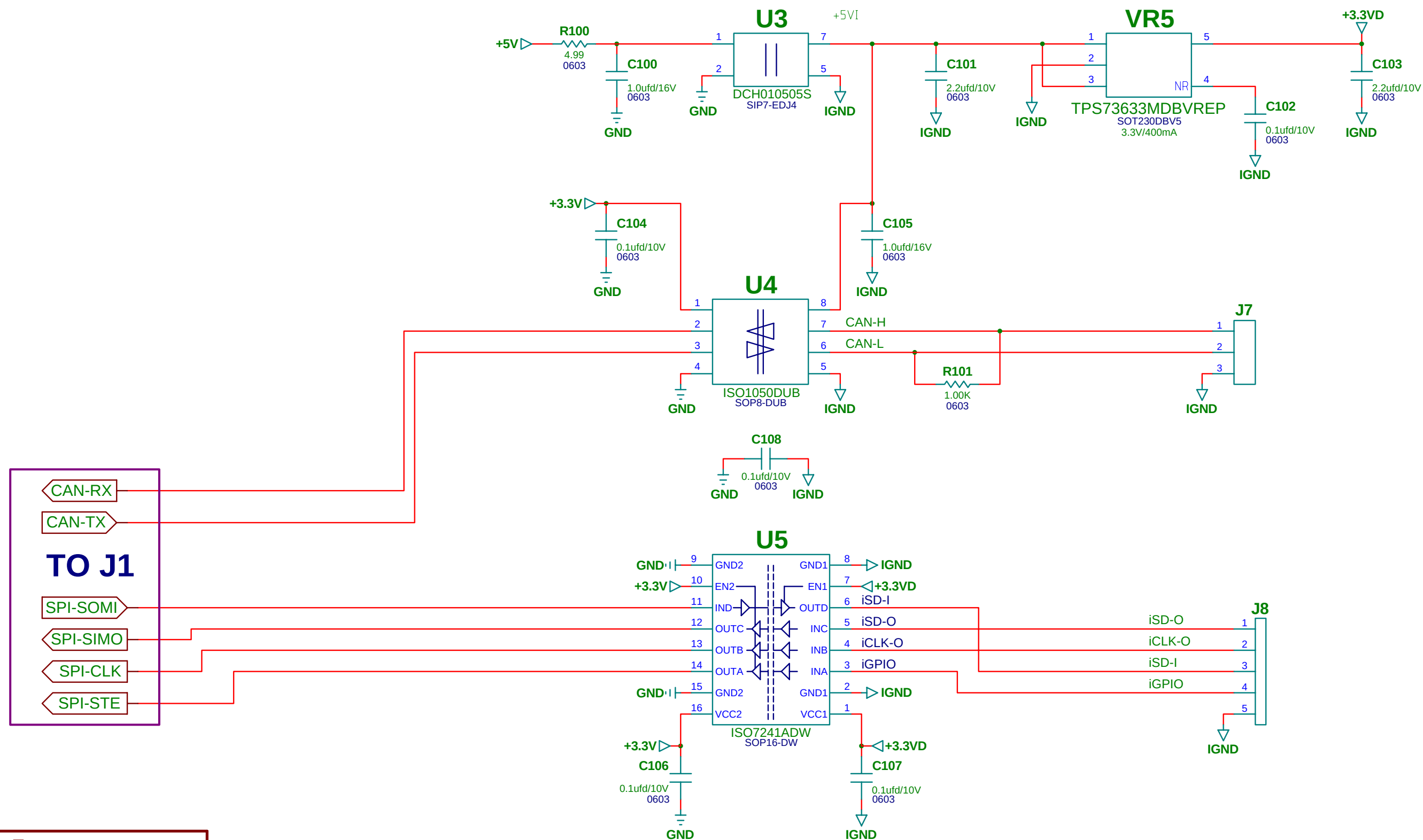
DRV8432DKD EVALUATION BOARD



DRV8432DKD EVALUATION BOARD



DRV8432DKD EVALUATION BOARD



DRV8432DKD EVALUATION BOARD

REVISION HISTORY			
REVISION	DESCRIPTION	DATE	APPROVAL
A	INITIAL RELEASE	OCTOBER 30, 2009	ML
B	CONVERTED BOARD TO MATCH DRV8332EVM LAYOUT	FEBRUARY 3, 2011	RK

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