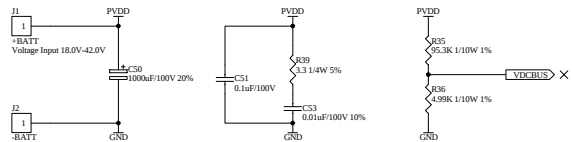
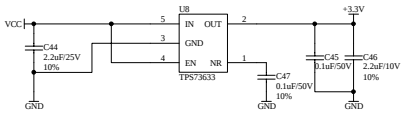


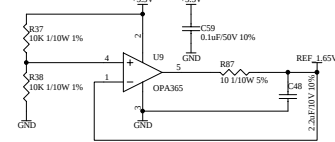
BATTERY TERMINAL



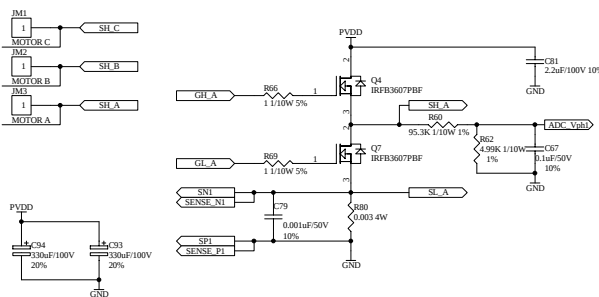
LDO +3.3V 400mA with Digital VDD



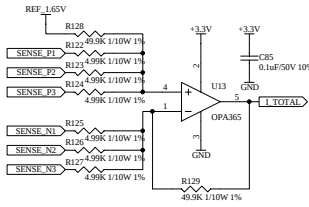
Reference Voltage 1.65V



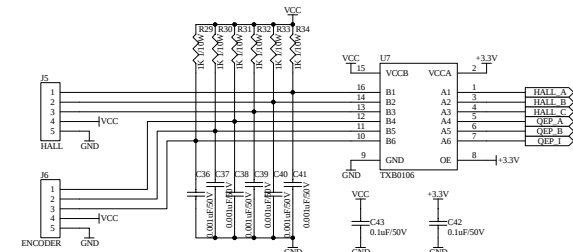
MOSFET & PHASE CURRENT SENSE



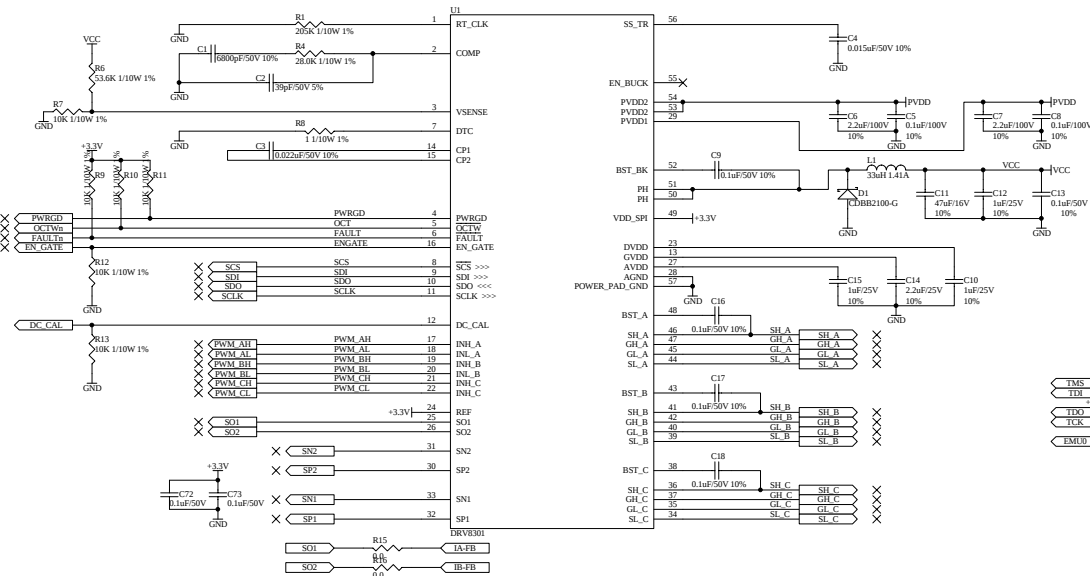
3 PHASE CURRENT SUM



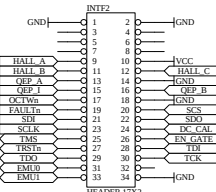
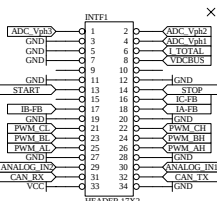
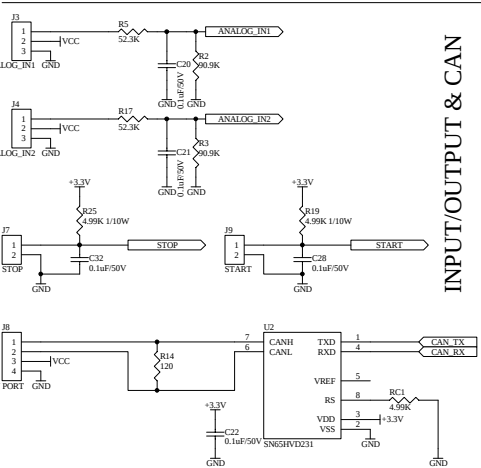
HALL AND ENCODER INPUT



GATE DRIVER with BUCK VCC 5V 1.5A



INPUT/OUTPUT & CAN



Title		
Size	Number	Revision
D	124012011	1
Date	12/01/2011	Sheet of 1
File	D:\FET\Project\LA-BLDC_Controller\2\Buck_Bldc_DC_T1.Ash	