TAS5717PHP EVALUATION BOARD HPR_OUT **HEADPHONE INPUT** HEADPHONE **OUTPUT** C20 AVDD (VIRTUAL OP-AMPS) C15 ___C16 HPL_OUT 10ufd/6.3V 0805 1.0ufd/25V 0603 HPL_IN C23 **FROM** MC57xxPSIA AVDD + C26 FAULT BST_A PDN LRCLK OUT_A LEFT C34 C33 PGND_AB 43 **U1**TAS5717PHP SDA C28 C35 0.33ufd/50 0805 **ANALOG** BST_C **OUTPUTS** C29 C36 **RIGHT** C41 DVDD C10 +__C32 C6 C7 **STANDOFFS** TEXAS INSTRUMENTS PAGE INFO: MAIN BOARD SCHEMATIC DESIGN LEAD TUAN LUU PCB REV B SEPTEMBER 10, 2010 FILENAME TAS5717EVM_RevB.sbk EDGE # 6515928

TAS5717PHP EVALUATION BOARD

REVISION HISTORY				
REVISION	DESCRIPTION	DATE	APPROVAL	
Α	INITIAL RELEASE	MARCH 4, 2010	TL	
В	1. C1/C33 ARE DO NOT POPULATE. 2. ADD C34, C35, C36, C41 - 0.33ufd/16V/0603. 3. CONNECT C34-1 AND C35-1 TO LEFT-2, LEFT-1, RESPECTIVELY. CONNECT C36-1 AND C41-1 TO RIGHT-2, RIGHT-1, RESPECTIVELY. CONNECT C34-2, C35-2, C36-2 AND C41-2 TO PGND.	SEPTEMBER 10, 2010	TL	

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SCH REV	В
PCB REV	В
SHEET 2	OF 3
DRAWN BY	LDN

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