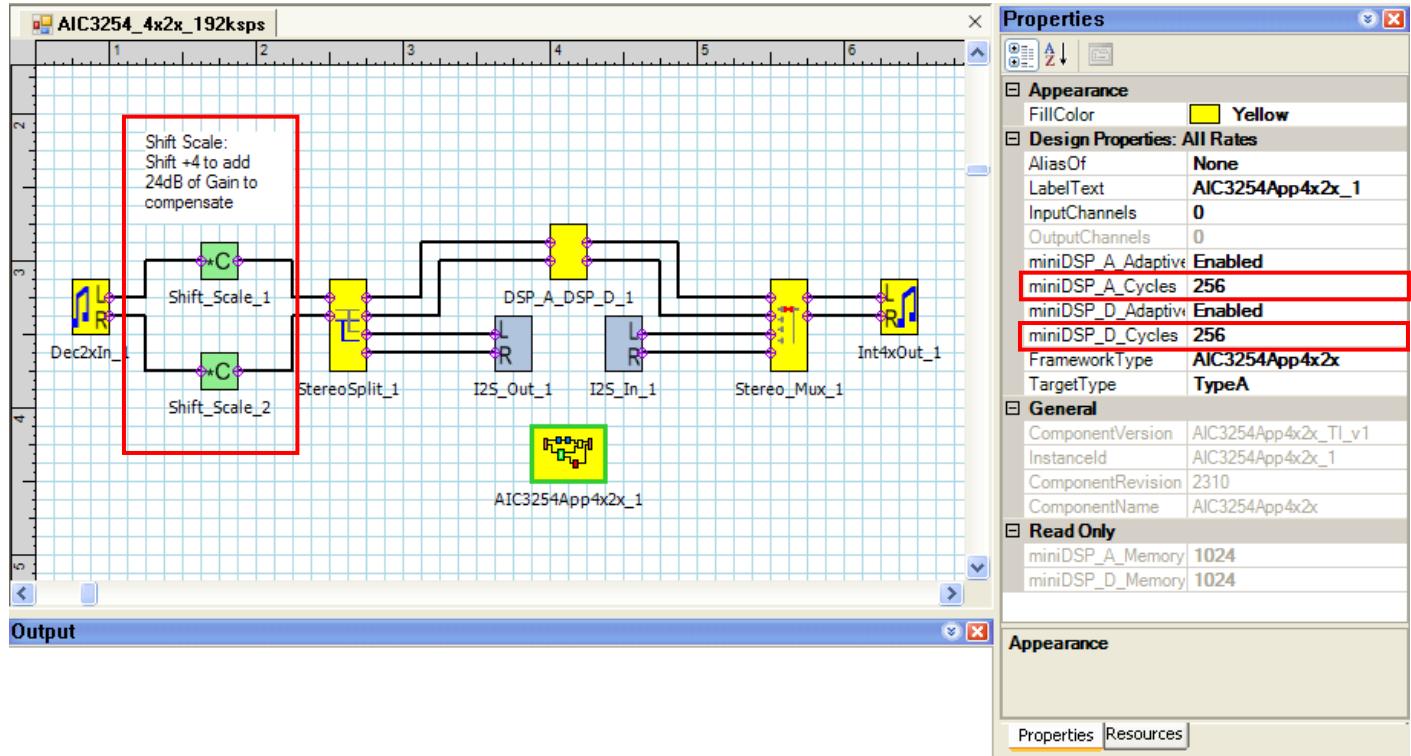


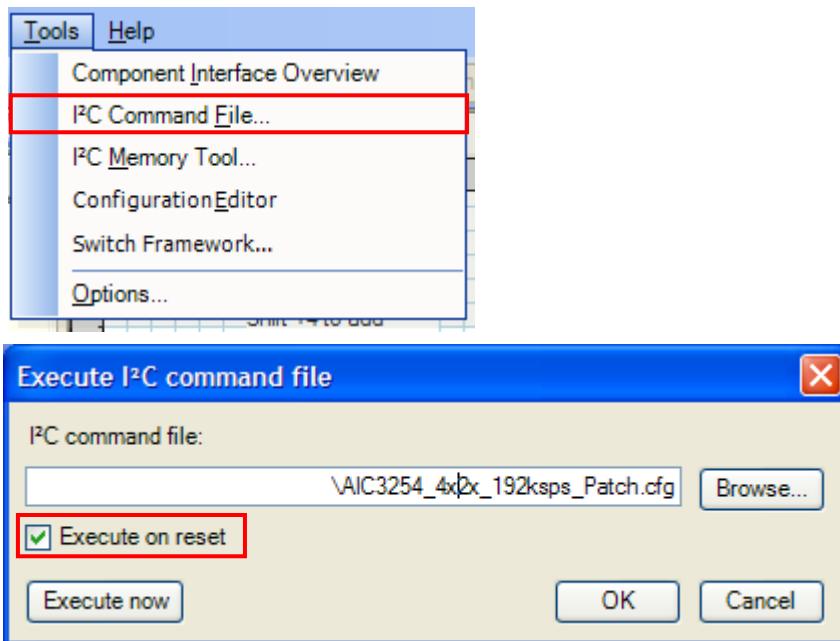
# PurePath Configuration

- A shift scale component can be used to add 24dB of Gain to compensate for the loss.
- miniDSP\_A cycles must be set less than or equal to MADC\*AOSR.
- miniDSP\_D cycles must be set less than or equal to MDAC\*DOSR.
- Audio is sent to the I2S bus.
- Mux selects DAC audio path.



# PurePath Configuration

- This is a case that requires a special configuration. PurePath can be configured to load a script after downloading the miniDSP code to re-configure Page 0 and Page 1 registers.
- AIC3254\_4x2x\_192ksps\_Patch.cfg reconfigures the clocking for an OSR of 32.



# EVM Configuration

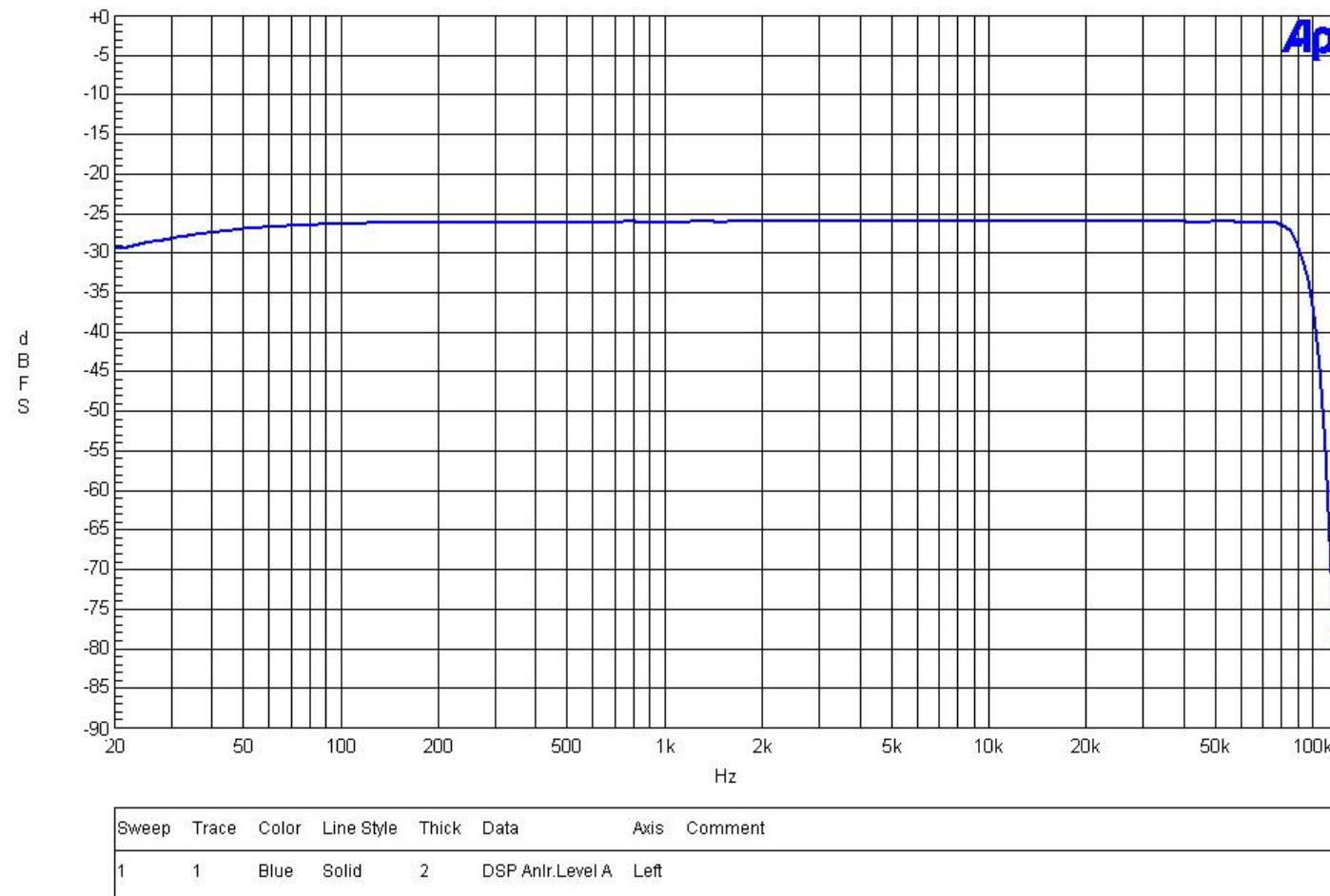
- USB 1.1 bandwidth does not allow 192ksps. An external audio interface must be connected to the USB-MODEVM's J14 and SW2.4-SW2.5 set to OFF.
- Codec is I2S Slave.

# Frequency Response

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 0dB compensation

Texas Instruments

12/04/09 21:38:11

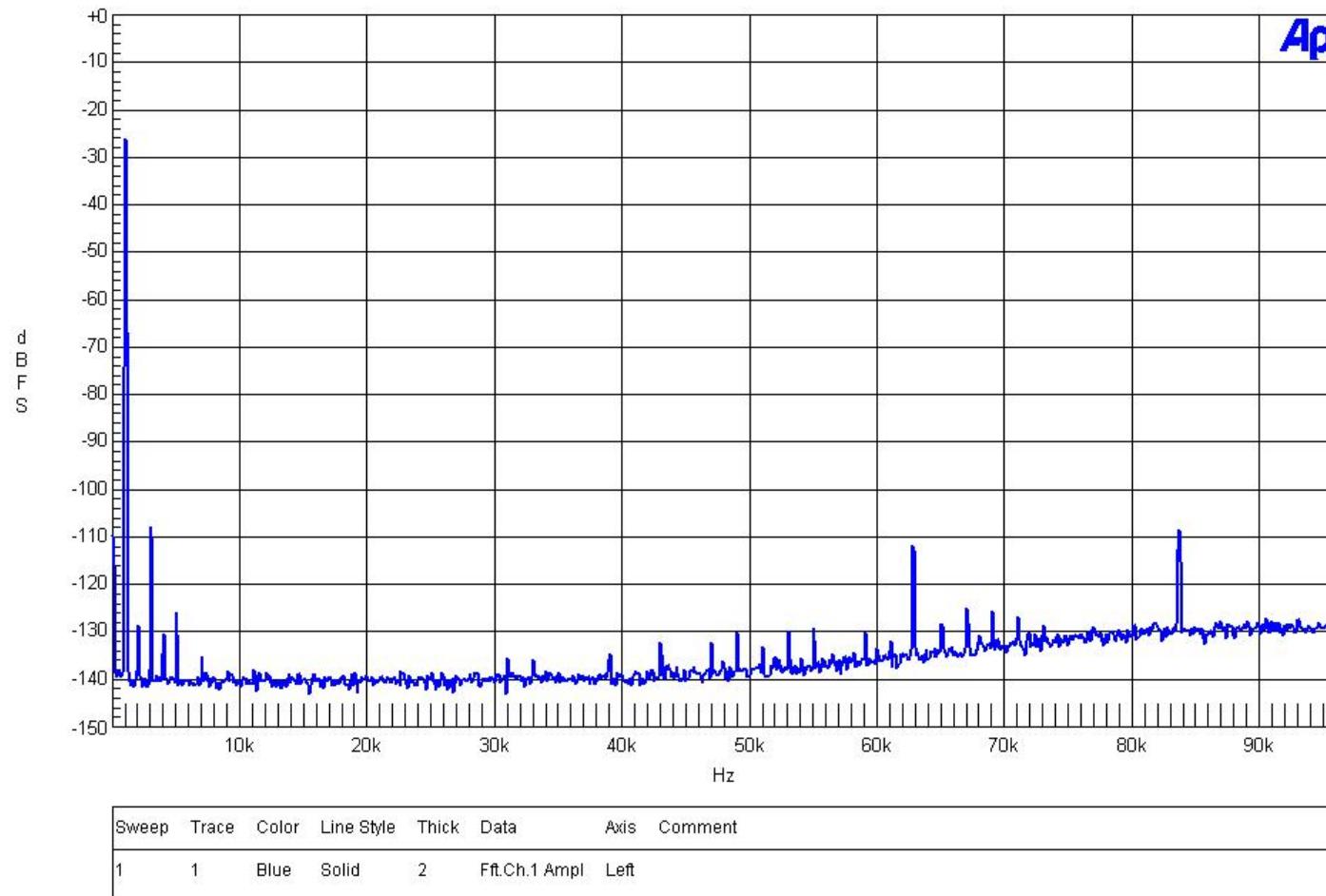


# 1kHz Tone FFT

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 0dB compensation

Texas Instruments

12/04/09 21:56:14

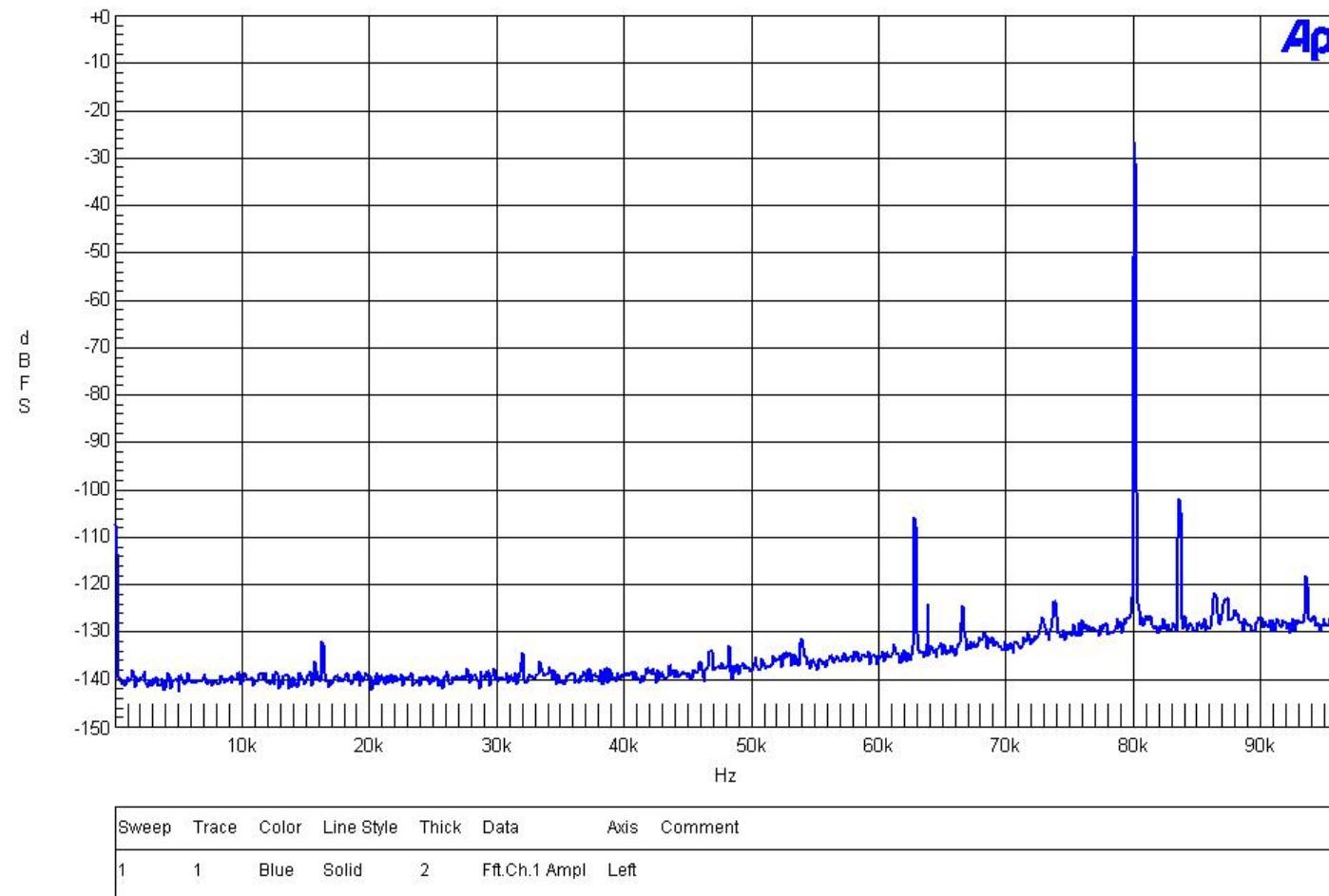


# 80kHz Tone FFT

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 0dB compensation

Texas Instruments

12/04/09 21:55:16

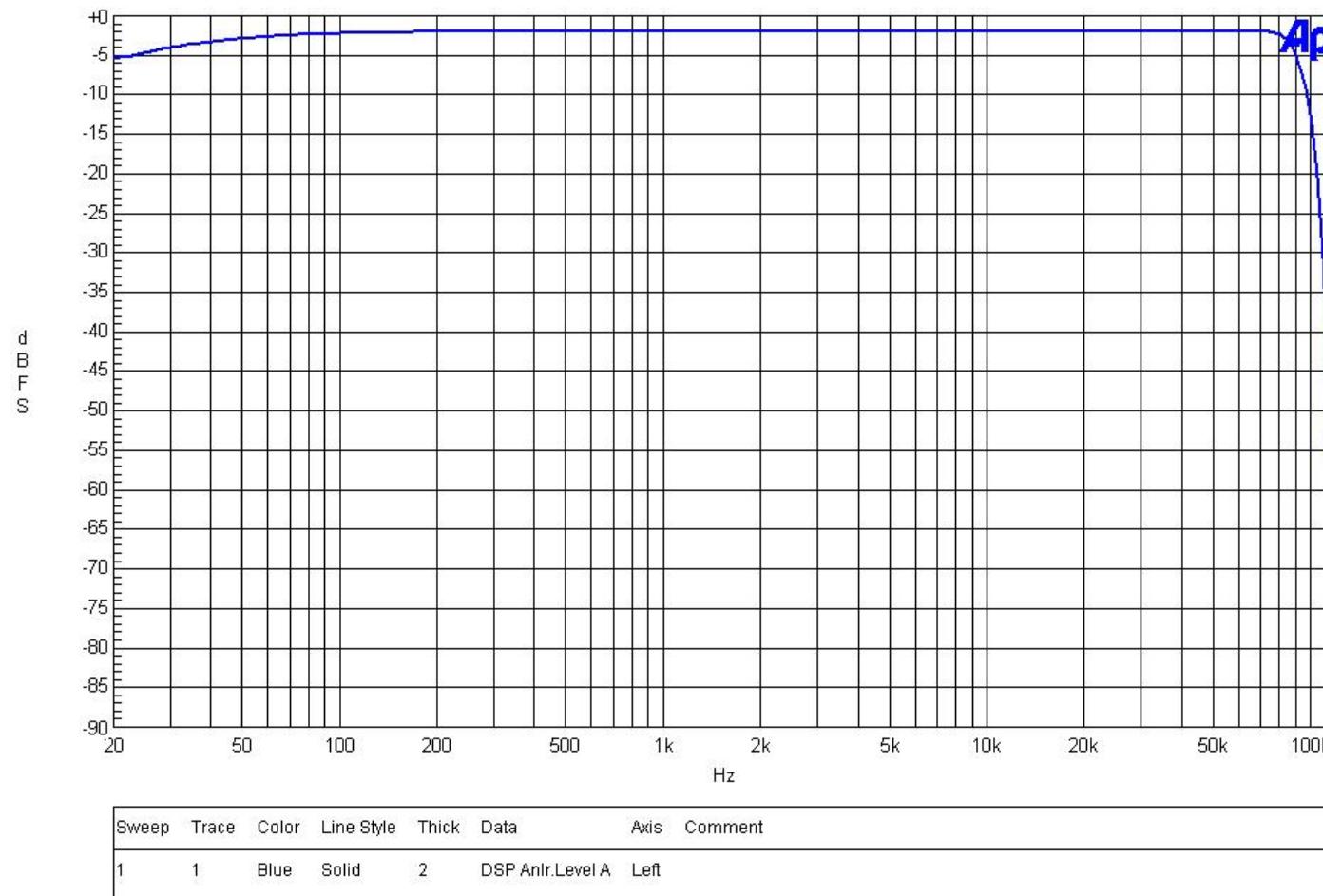


# Frequency Response

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 24dB compensation

Texas Instruments

12/04/09 21:34:18

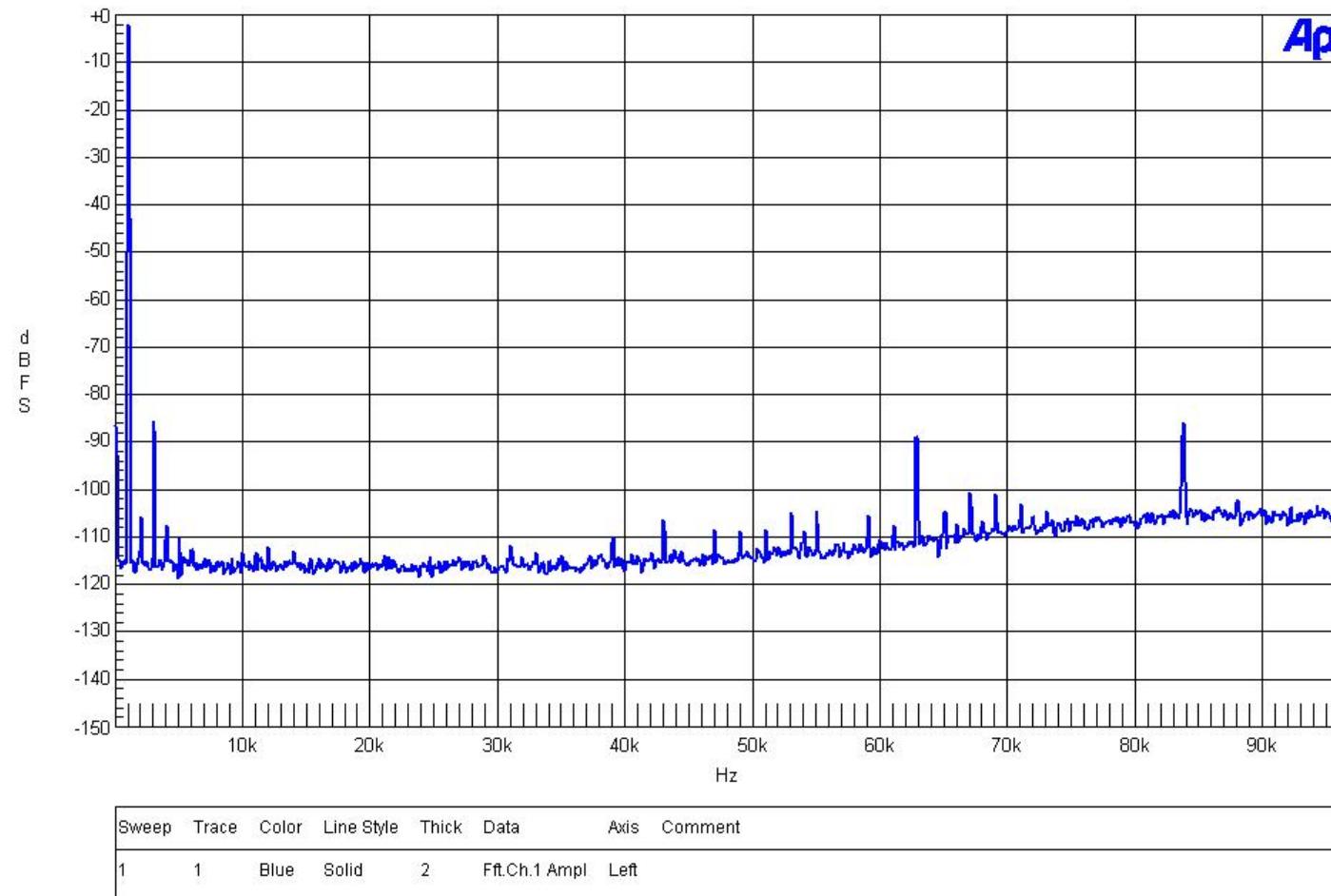


# 1kHz Tone FFT

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 24dB compensation

Texas Instruments

12/04/09 21:57:15



# 80kHz Tone FFT

AIC3254\_4x2x, 192kHz, AOSR=32, -2dB input signal, 24dB compensation

Texas Instruments

12/04/09 21:57:59

