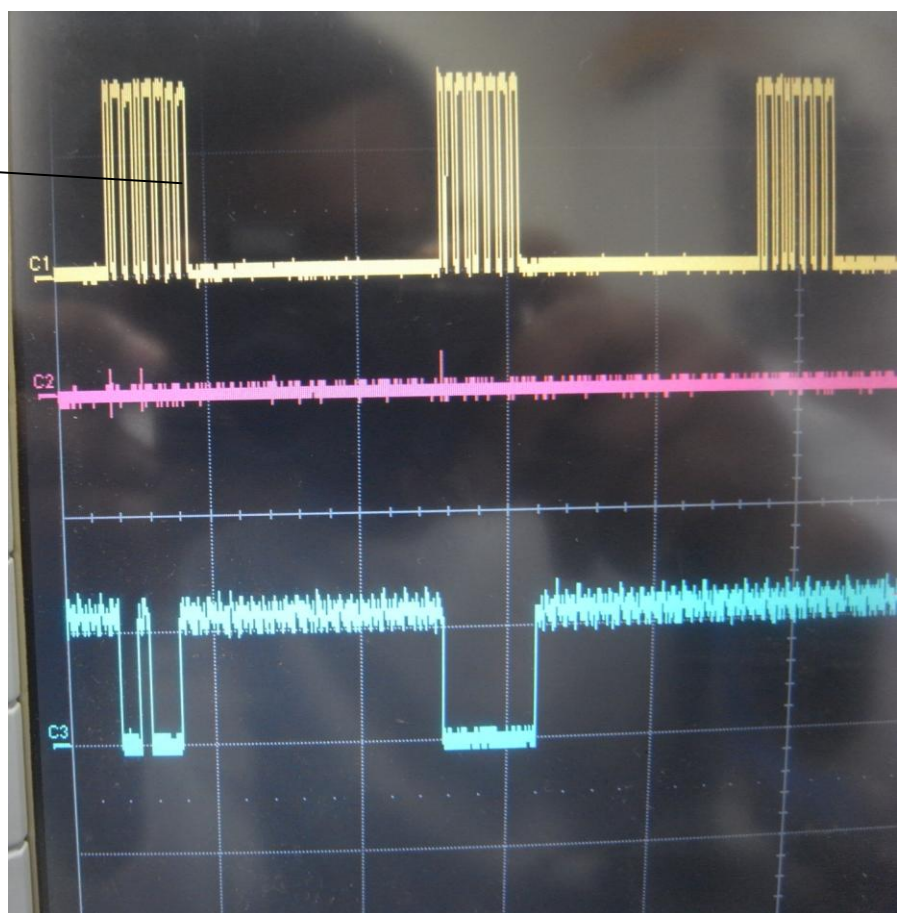
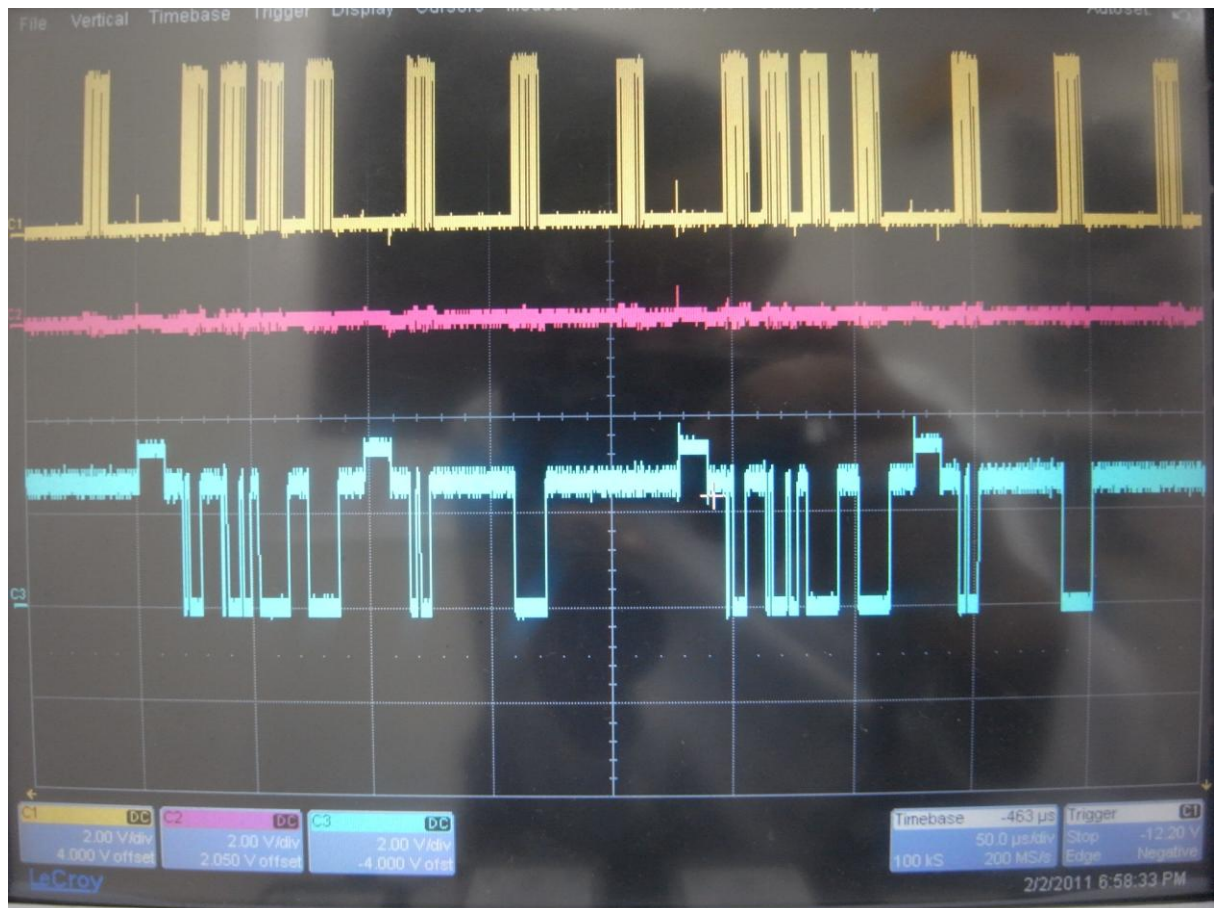


Multiplexer change

ADC initial Setup & Multiplexer change
are listed on figure 80.

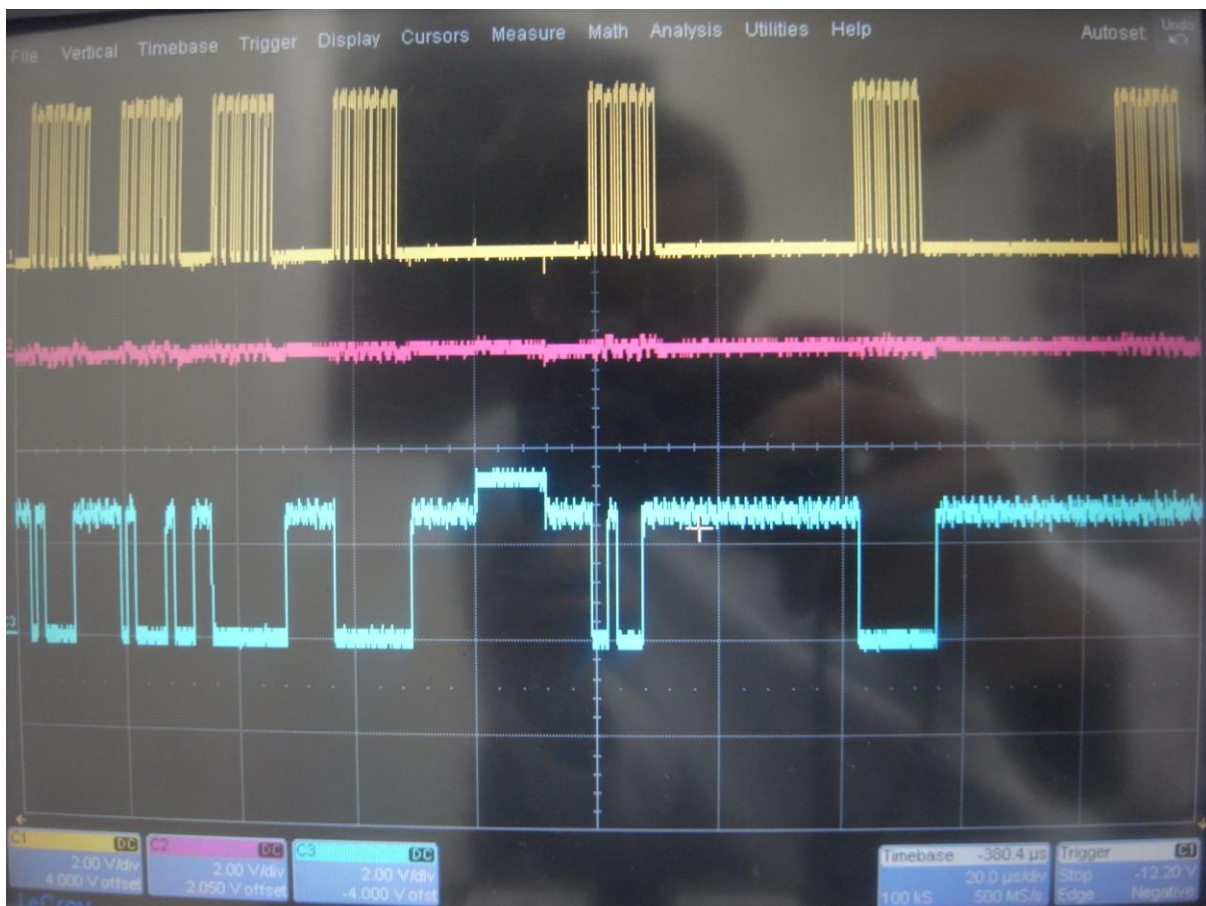
(Datasheet ADS1248)



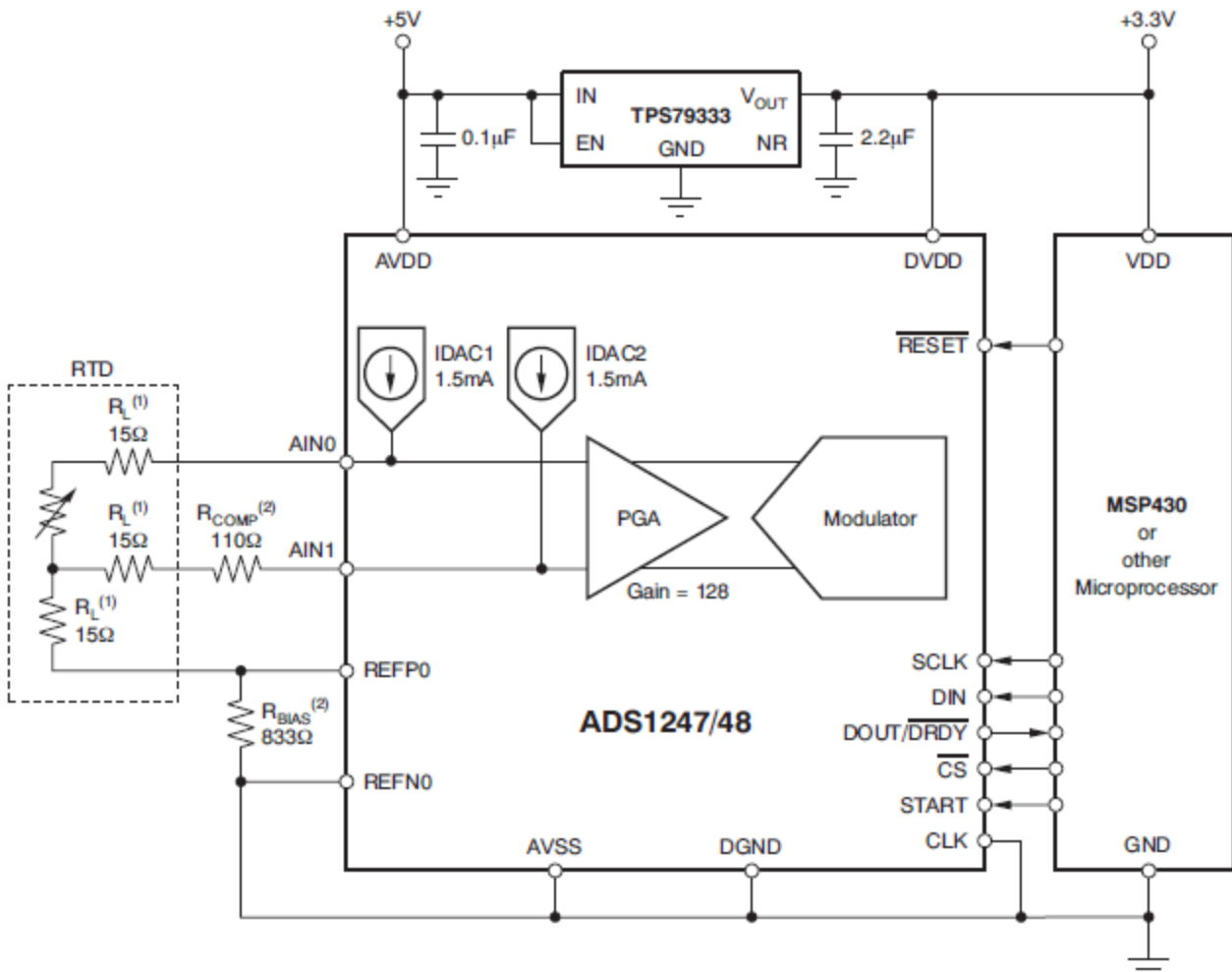


Picture of DIN (Red)/ DOUT(blue)/ SCLK(Yellow) : We can see 2 periode

Picture of DIN (Red)/ DOUT(blue)/ SCLK(Yellow) : We can see 1 periode



a schematic as to how you have the ADS1248 connected to your processor :
MSP430f169



- (1) RTD line resistances.
- (2) R_{BIAS} and R_{COMP} should be as close to the ADC as possible.

Figure 82. Three-Wire RTD Application with Hardware Compensation

My codes. Txt.files