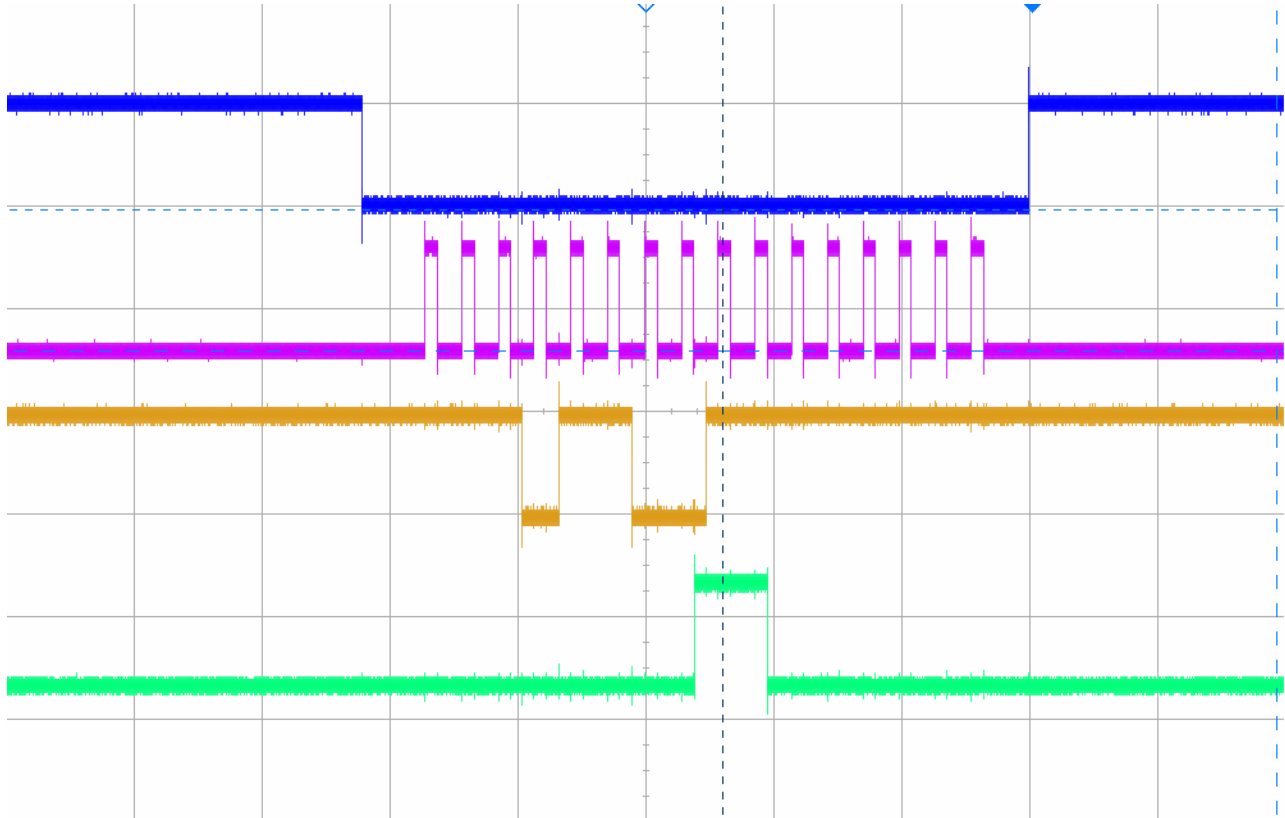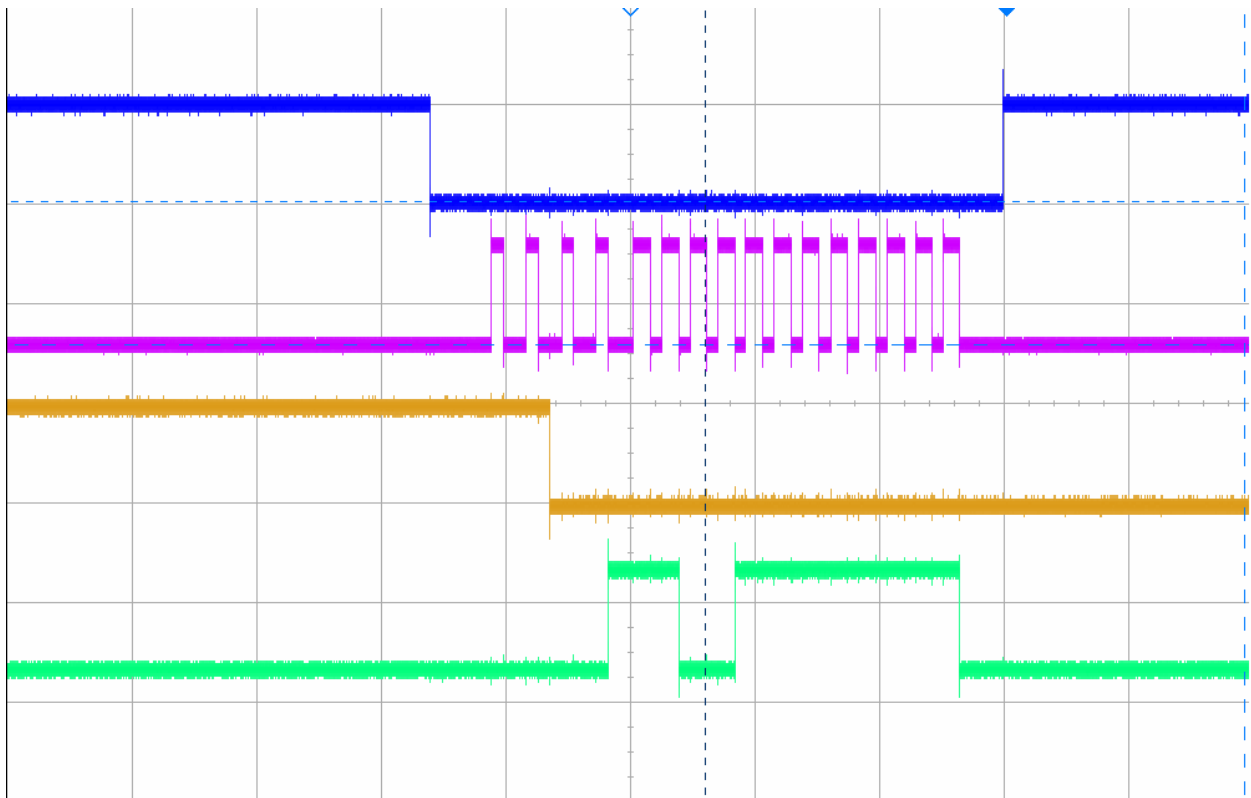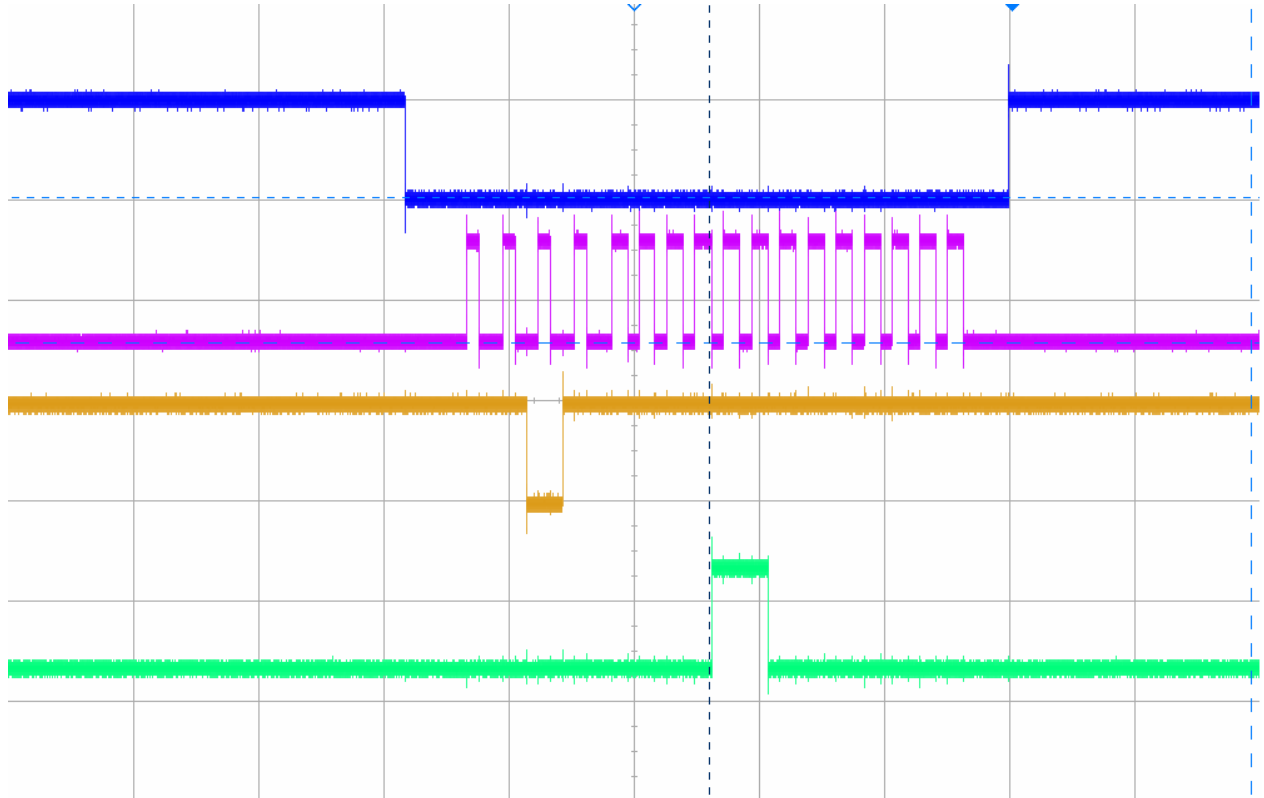# ADC ADS7230

1. Write config 0xECFF for Auto Trigger, Auto Channel, TAG bit enable

2. Read config for verification : 0x0CFF

3. First Read Converted Data with Cmd 0xD and Read with 13 Clocks with Extra Bit for TAG Bit : Data Read = 0x180 (TAG 0 for Channel 0)

4. Second Read Converted Data with Cmd 0xD and Read with 13 Clocks with Extra Bit for TAG Bit : Data Read = 0x180 (TAG 0 for Channel 0) : Same 1st Channel, Not the 2nd One