These scenarios are screenshots taken following a power-up, and release from Reset of the DSP. No PCI accesses are performed.

The 1st screenshot shows the PC at zero, the 2nd does not. Both conditions would cause failures, but because we are not writing to PCI, we do not have a "pass" condition. My belief is that a PASS condition is identical, but the timing would be such that the FFs would be replaced by the Gpp test data. In most (but not all) failure conditions, the PC is non-zero.

The 3rd screenshot shows a scenario where not ALL of the 1st 1k contains FFs, but contains some other data.





