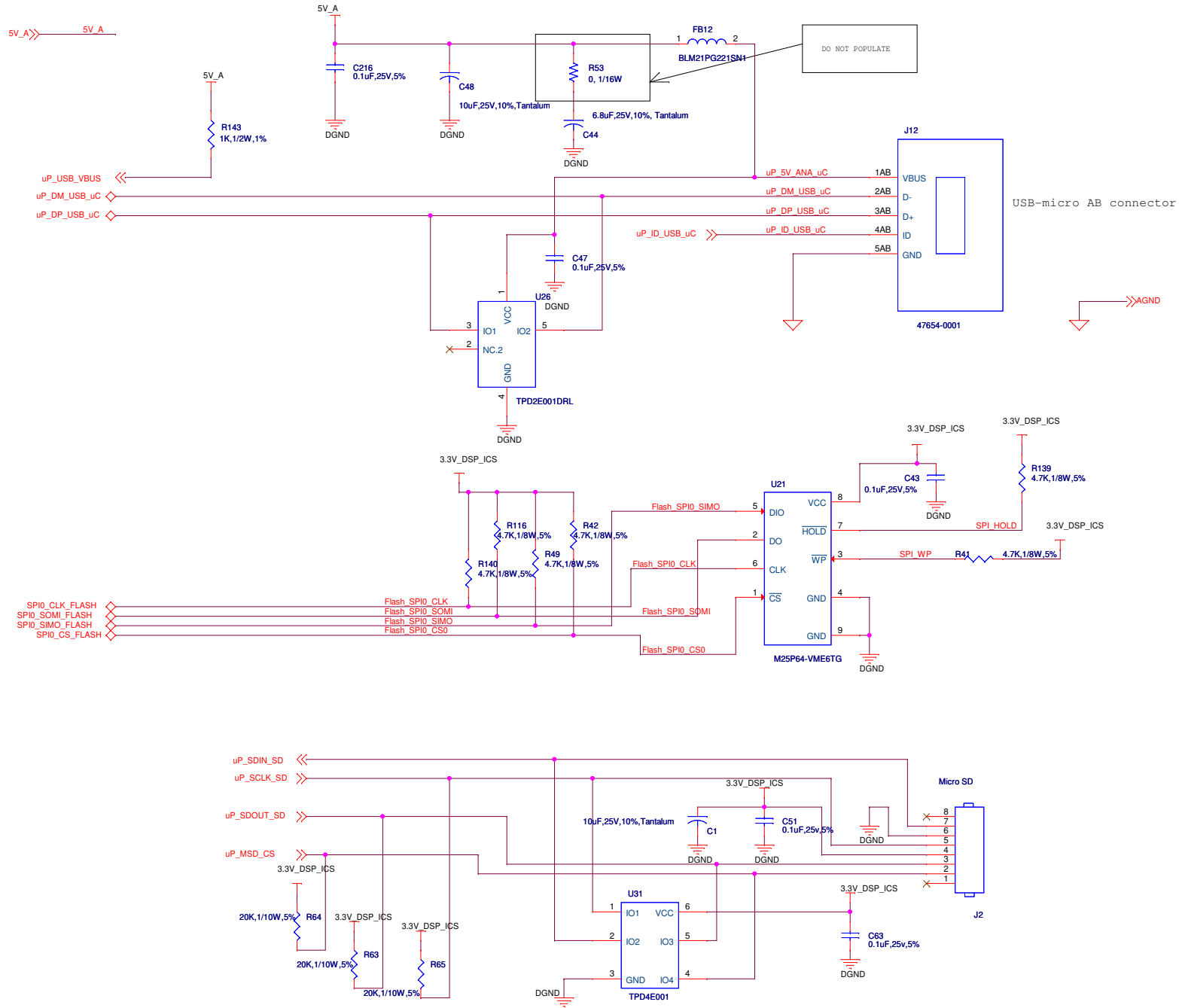
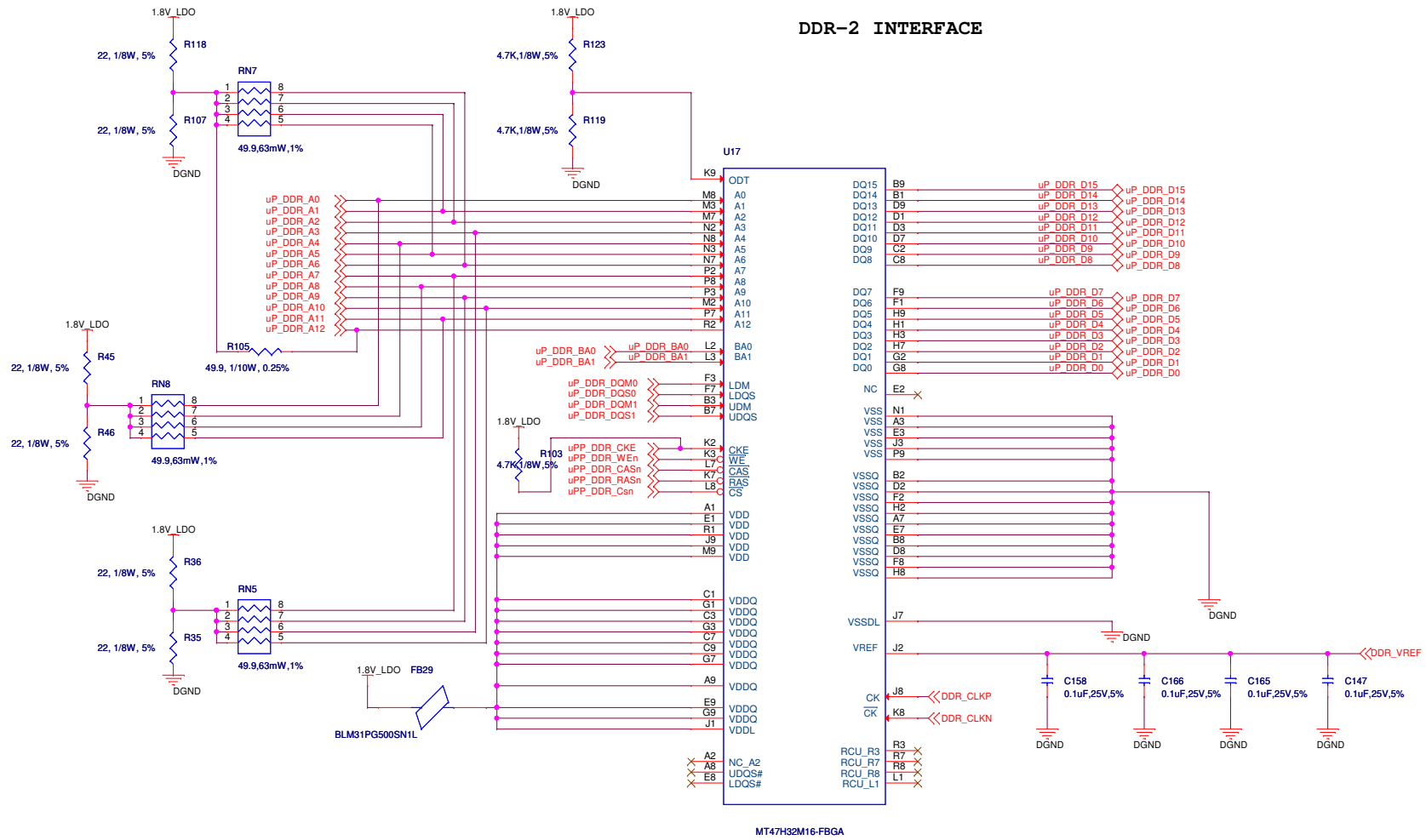


USB2.0 INTERFACE & SPI-0 NAND FLASH FOR BOOTING OF DSP BIOS



DDR-2 INTERFACE



DDR2 Signal Terminations

	Min	Typ	Max	Unit	Kindly Note
CK Net Class	0		10	ohm	Only series termination is permitted, Parallel or SST specifically disallowed. Terminator values larger than typical only recommended to address EMI issues. Termination value should be uniform across net class.
ADDR_CTRL Net Class	0	22	Zo	ohm	
Data Byte Net Classes (DQS[0], DQS[1], D0, D1)	0	22	Zo	ohm	
DQGATE Net Class (DQGATE)	0	10	Zo	ohm	

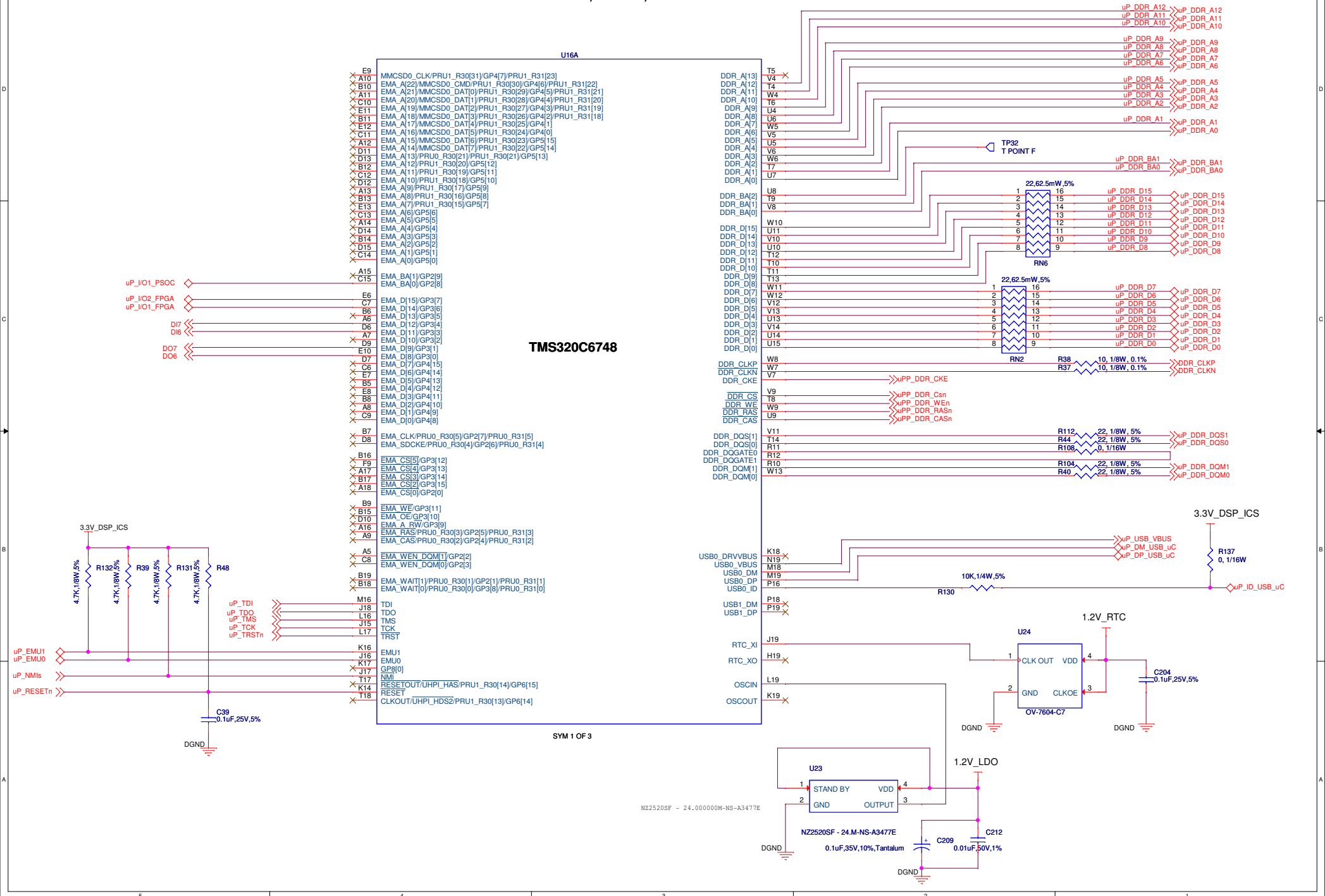
DSP TO - SPI0, UART-1 AND UART-2



SYM 3 OF 3

BOOT BITS[7:0]	BOOT DEVICES
0001 0100	SPI0
0001 1110	Emulation Debug

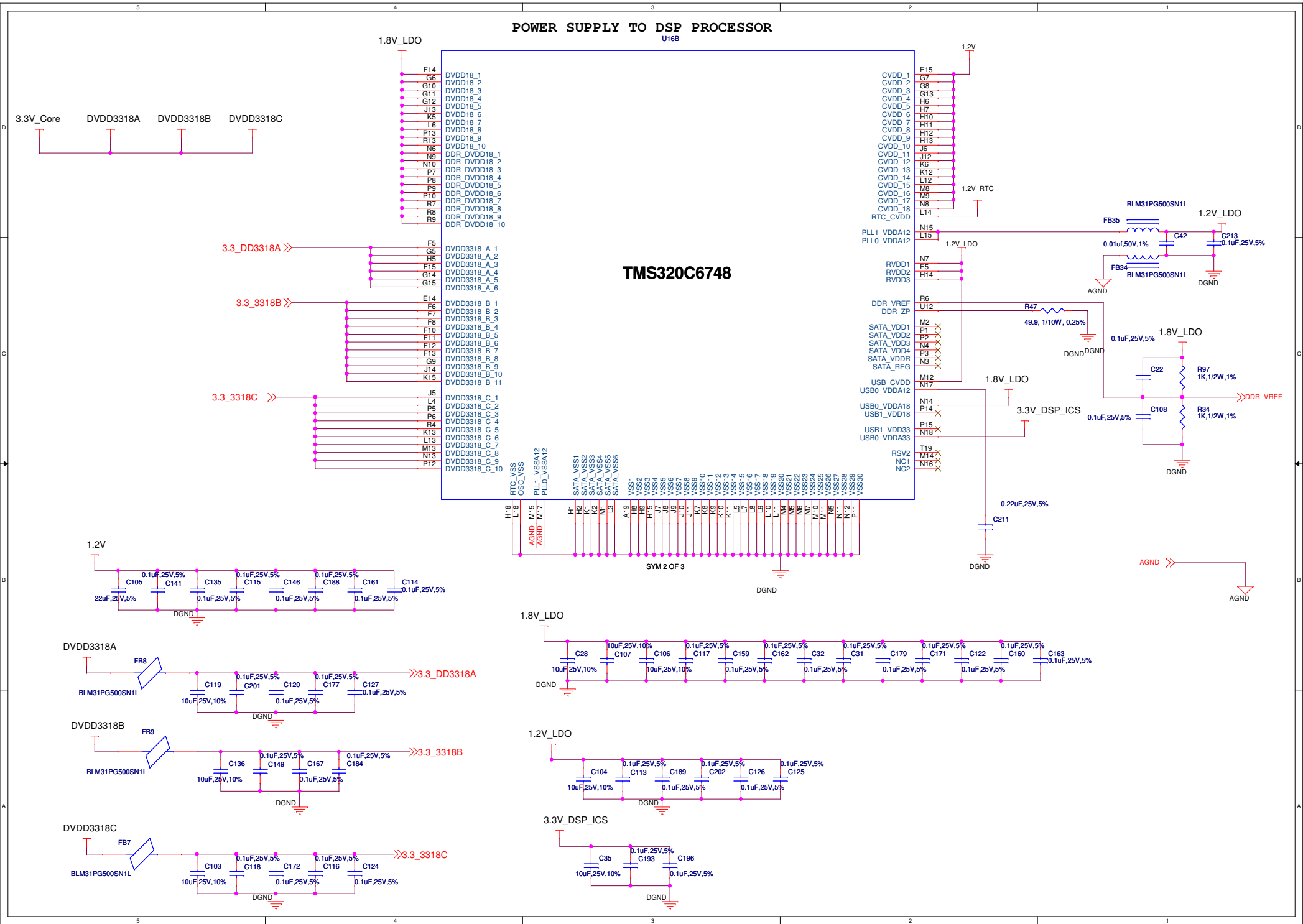
DSP TO USB, JTAG, DDR2



POWER SUPPLY TO DSP PROCESSOR

U16B

TMS320C6748



POWER REGULATION FOR DSP BOARD

