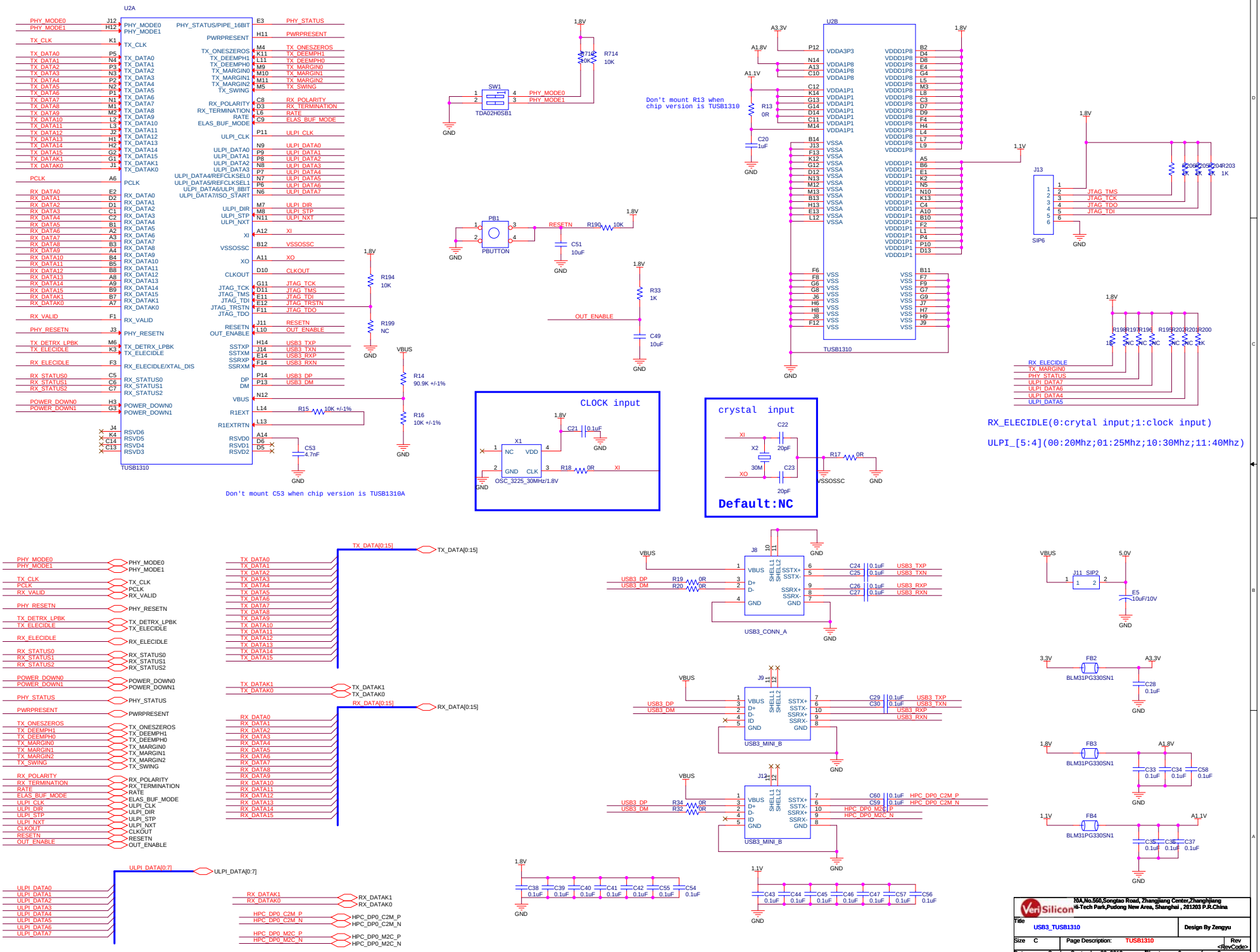
U2A
TUSB1310
Don't mount C53 when chip version is TUSB1310A
SW1
TDA02H0SB1
PB1
PBUTTON
U2B
Don't mount R13 when chip version is TUSB1310
J13
SIP6
JTAG_TMS
JTAG_TCK
JTAG_TDO
JTAG_TDI
RX_ELECIDLE(0:crytal input;1:clock input)
ULPI_[5:4](00:20Mhz;01:25Mhz;10:30Mhz;11:40Mhz)
CLOCK input
X1
OSC_3225_30MHz/1.8V
crystal input
Default:NC
J8
USB3_CONN_A
J9
USB3_MINI_B
J12
USB3_MINI_B
J11_SIP2
FB2
BLM31PG330SN1
FB3
FB4
TX_DATA[0:15]
RX_DATA[0:15]
ULPI_DATA[0:7]
HPC_DP0_C2M_P
HPC_DP0_C2M_N
HPC_DP0_M2C_P
HPC_DP0_M2C_N
VerSilicon
10A,No.560,Songtao Road, Zhangjiang Center,Zhanghjiang Hi-Tech Park,Pudong New Area, Shanghai , 201203 P.R.China
Title
USB3_TUSB1310
Design By Zengyu
Size C
Page Description: TUSB1310
Rev <RevCode>
Date: Sunday, September 29, 2013
Sheet 2 of 3