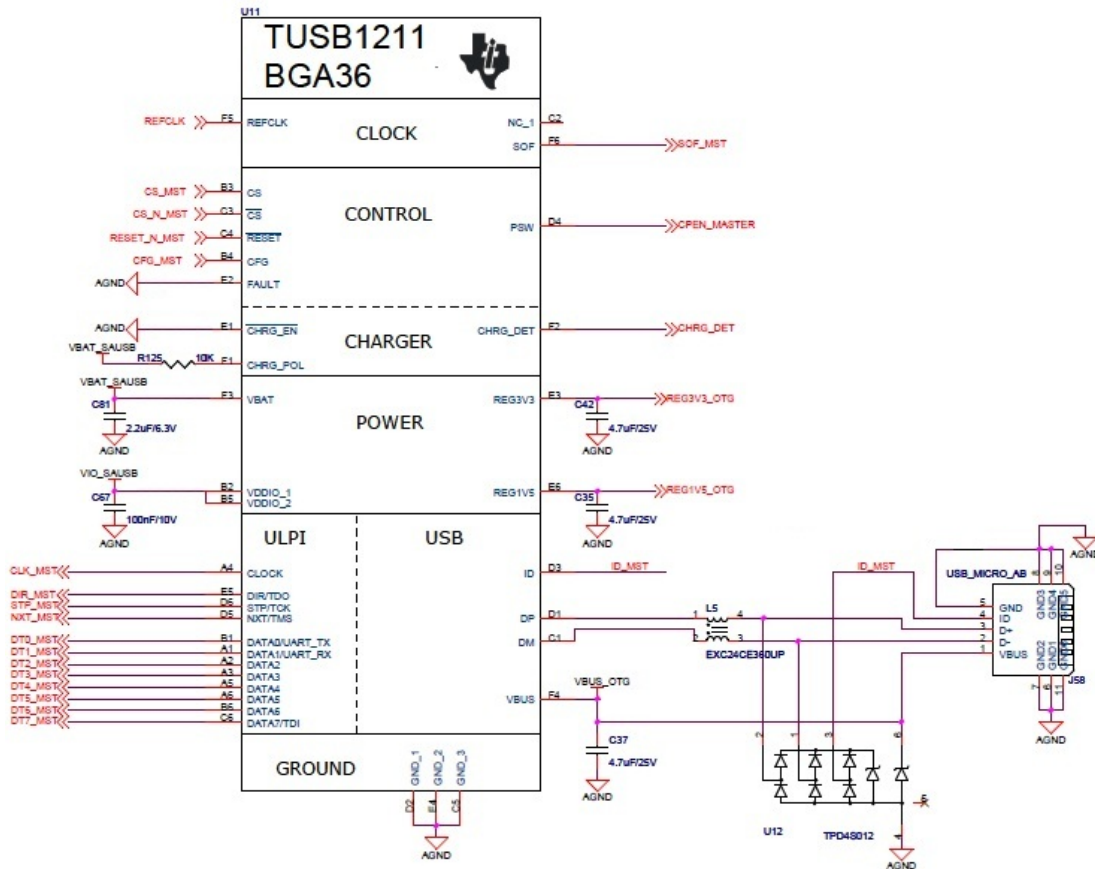


## TUSB1211 in ULPI output clock mode

In this configuration the TUSB1211 PHY provides the 60MHz ULPI Clock to the link controller as shown in the figure below.

In this case the TUSB1211 PHY need a reference clock applied on REFCLK pin to generate the 60Mhz ULPI clock Output. Two frequencies are supported: 19.2MHz (when CFG = 0), and 26MHz (when CFG = 1). TUSB1211 supports square-wave reference clock input only.



## TUSB1211 in ULPI output clock mode

- Note: 60MHz clock is output on ULPI CLOCK pin
- Note: 19.2MHz or 26MHz reference clock must be provided on REFCLK pin
- Note: TUSB1211 REFCLK is 1.8V square-wave only.