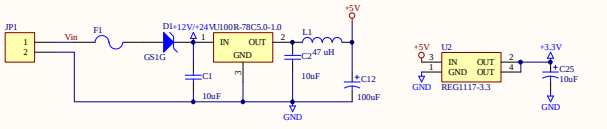
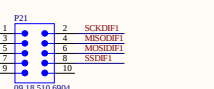
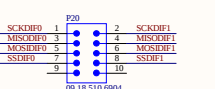
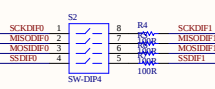
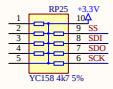
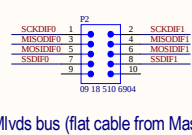
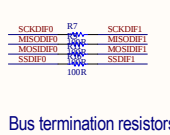
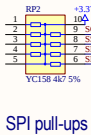
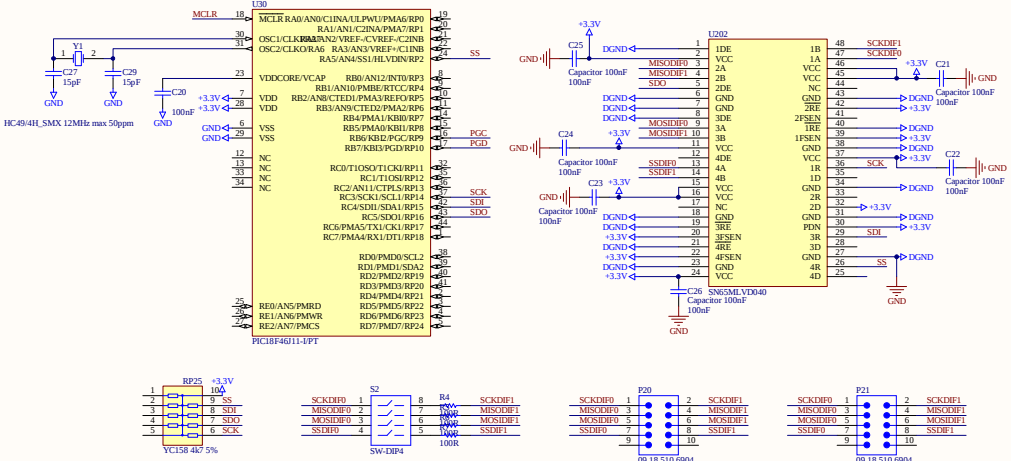
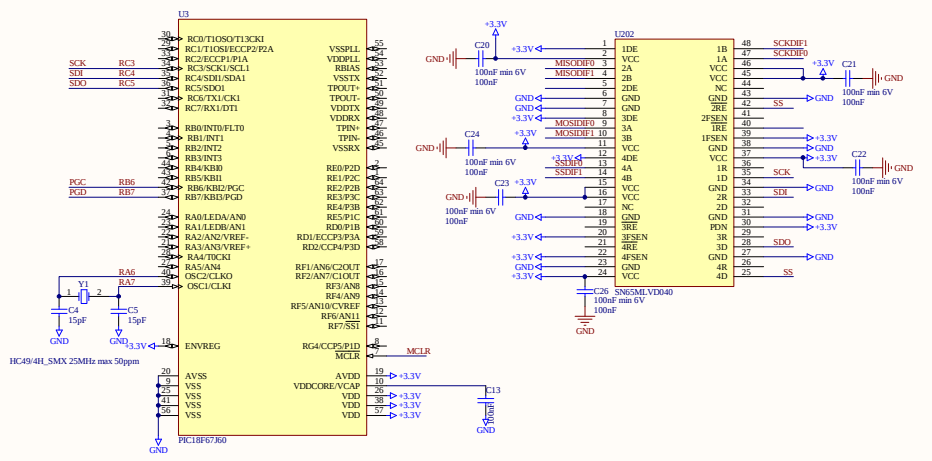
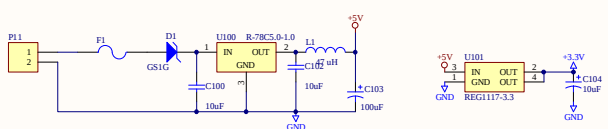


MASTER



SLAVES



SPI pull-ups

Bus termination resistors

Mlvds bus (flat cable from Master to 1st slave)

SPI pull-ups

bus termination resistors by switch (last Slave)

Mlvds bus (short flat cables from board to board)

Mlvds bus (short flat cables from board to board)

Title			Revision	
Size	A2	Number		
Date	09/01/2013	Sheet	of	
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