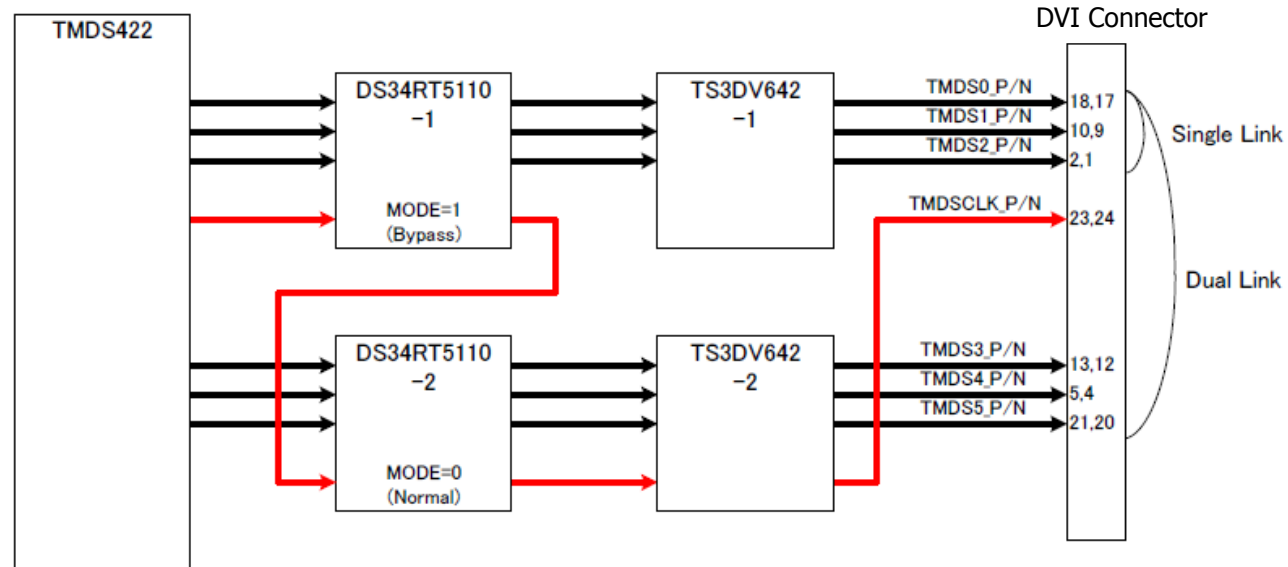
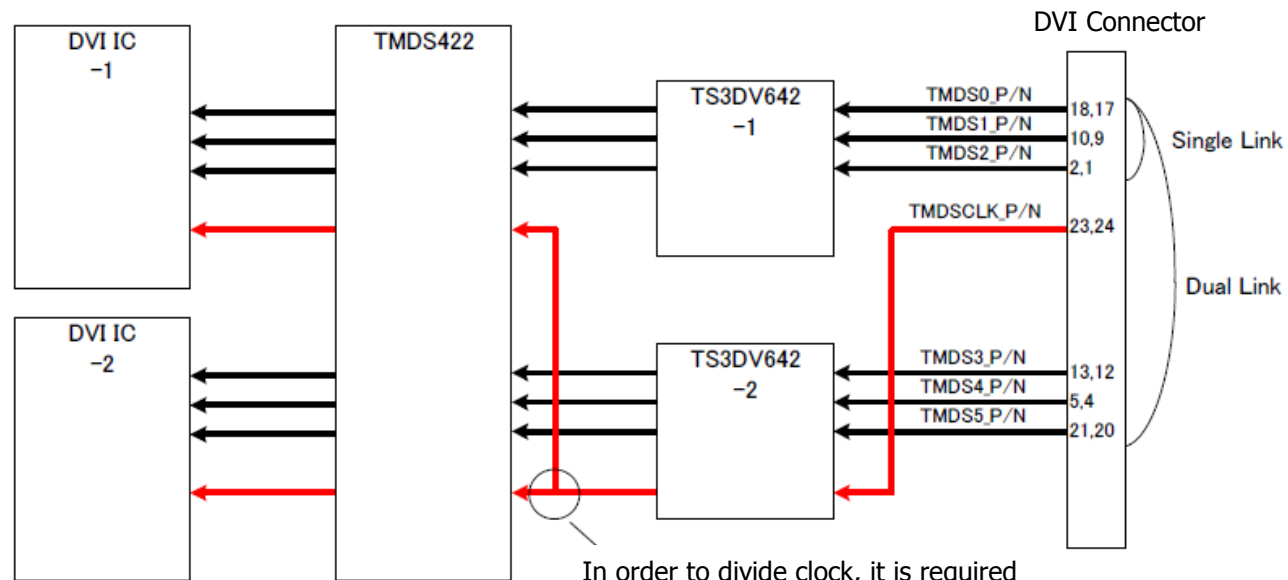


DVI Output



DVI Input



In order to divide clock, it is required to use signal Clock buffer, isn't it?