

LMH1983 Recommended SMBus Timing Parameters

Objective:

The table and figure below is the timing parameters for the LMH1983 SMBus interface.

TABLE: SERIAL MANAGEMENT BUS INTERFACE AC TIMING SPECIFICATIONS						
Symbol	Parameter	Condition	Min	Typ	Max	Units
FSMB	Bus Operating Frequency	ENSMB = VDD (Slave Mode)	10		400	kHz
TBUF	Bus Free Time Between Stop and Start Condition		4.7			μ s
THD:STA	Hold time after (Repeated) Start Condition. After this period, the first clock is generated.		4.0			μ s
TSU:STA	Repeated Start Condition Setup Time		4.7			μ s
TSU:STO	Stop Condition Setup Time		4.0			μ s
THD:DAT	Data Hold Time		300			ns
TSU:DAT	Data Setup Time		250			ns
TLOW	Clock Low Period		1.3			μ s
THIGH	Clock High Period		0.6		50	μ s
t _F	Clock/Data Fall Time				300	ns
t _R	Clock/Data Rise Time				300	ns

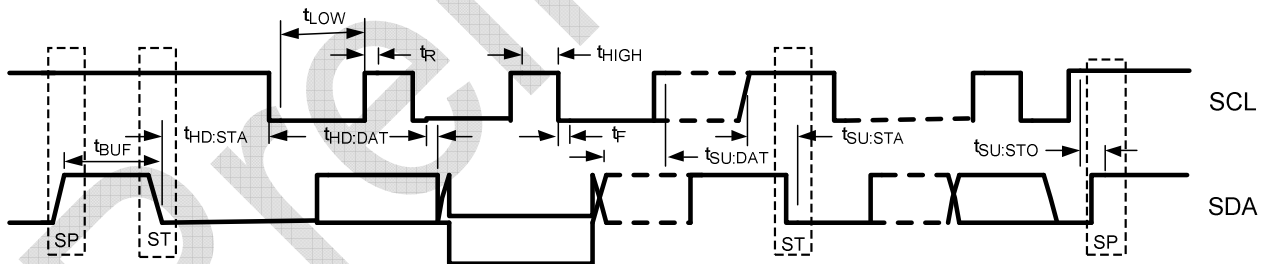


Figure: SMBUS Timing Parameters