◆32kHz Word clock input
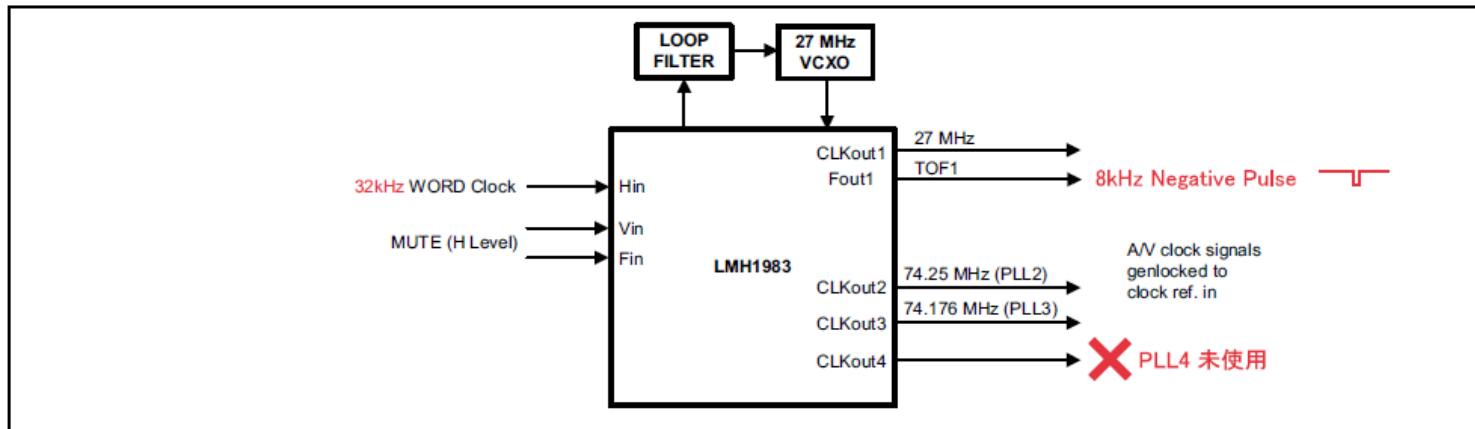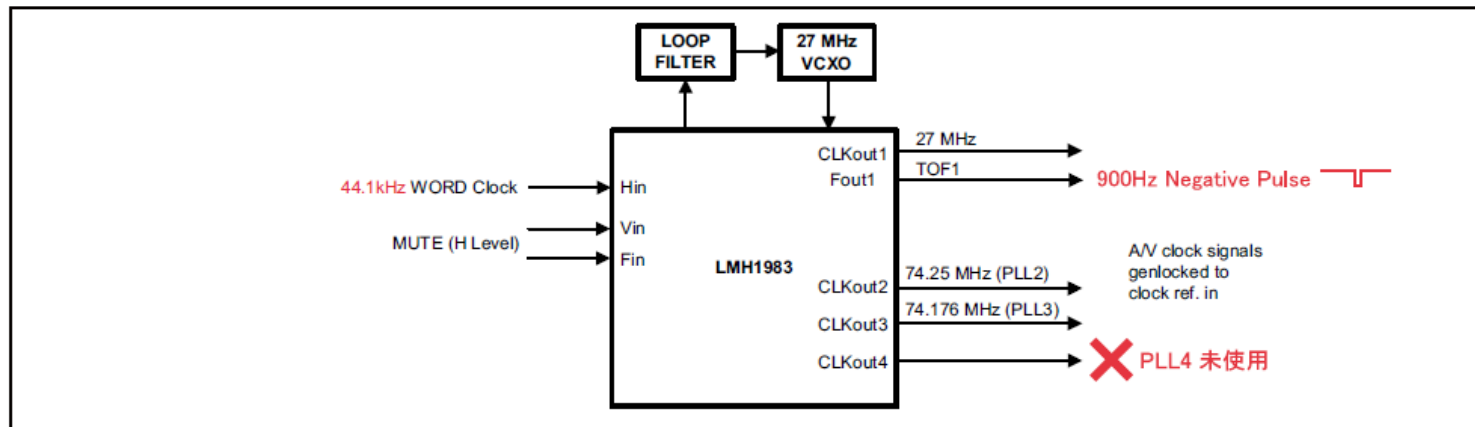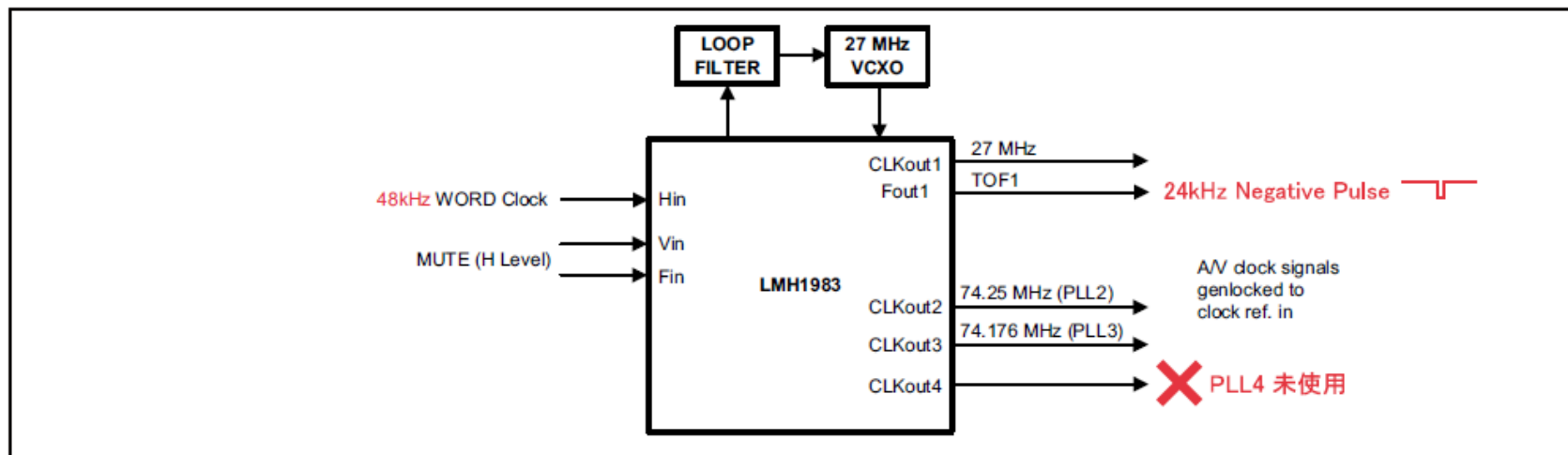

Negative Pulse of 8Khz cycle is outputted from Fout1 terminal.
common denominator (27 MHz and 32 kHz), it is guessed that the phase
comparison frequency of PLL1 is 8 kHz. Reference lock time: About 6 seconds

◆44.1kHz Word clock input

Negative Pulse of 900Hz cycle is outputted from Fout1 terminal.
common denominator (27 MHz and 44.1 kHz), it is guessed that the phase
comparison frequency of PLL1 is 900Hz. Reference lock time: About 30 seconds

◆48kHz Word clock input

Negative Pulse of 24Khz cycle is outputted from Fout1 terminal.
common denominator (27 MHz and 48 kHz), it is guessed that the phase
comparison frequency of PLL1 is 24 kHz. Reference lock time: About 6 seconds