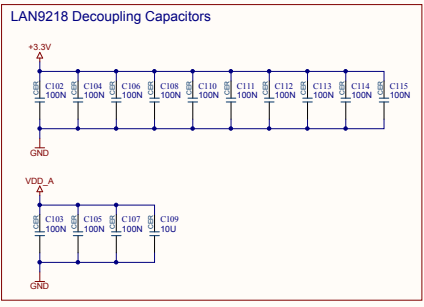


Configuration:

- PHY address 16
- Auto-Negotiation enabled
- 10BASE-T, Half/Full-Duplex
- 100BASE-TX, Half/Full-Duplex
- CLK_OUT disabled
- FX disabled
- LED Config, Mode 1
- MII Mode selected
- Phy Control Frame disabled



Project		Version	Drawn by	Reviewed by	Checked by
File					
Size					
AD					
Date					
Source					