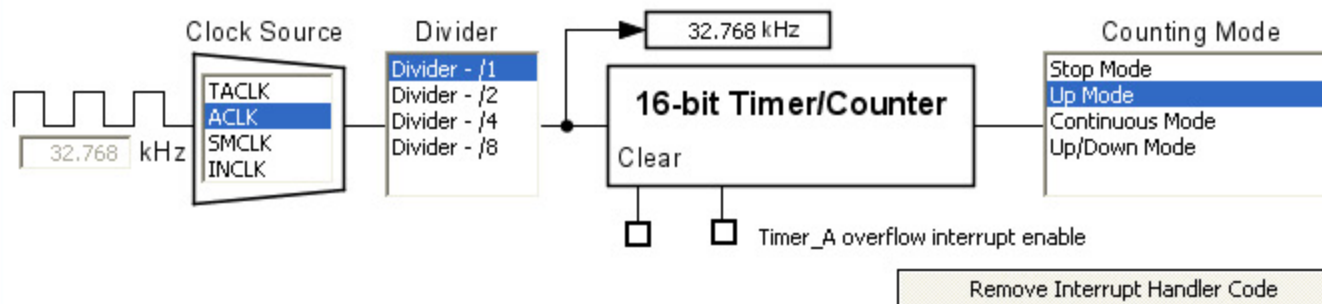


Grace (MSP430) ▶ Timer_A2 - Power User Mode - CCR0

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[View All Interrupt Handlers](#)

Timer Capture/Compare Block #0

Desired Timer Period: 500.0 ms Time(r) Period 500 ms

Capture Register: 16383 Clock Ticks Time(r) Frequency 2 Hz

Input Selection

CC Input OFF
P1.1/Timer_A2.CCI0A
GND
VCC

Capture Mode

No Capture
Rising Edge
Falling Edge
Both Edges

Mode

Timer OFF
Output Compare/Period
Input Capture

Output Pins

TA0 Output OFF
P1.1/Timer_A2.TA0
P1.5/Timer_A2.TA0

Output Mode: PWM output mode: 0 - OUT bit value ☐ Set OUT bit High/Low

Interrupt Enables

☒ Capture/compare interrupt enable 0

Remove Interrupt Handler Code

[View All Interrupt Handlers](#)