

- Texas Instruments
- C28x C/C++ Compiler Version 6.1.1
- These are the ClearQuest defects fixed in compiler release 6.1.1. This compiler was released in September 2012.
- Details on these ClearQuest Software Issue Reports can be found at:
 - [SDO-Web](#)
- For more information contact:
 - http://e2e.ti.com/support/development_tools/compiler/default.aspx
- ClearQuest list created: 2012-9-12

1. ClearQuest ID: [SDSCM00043944](#)
 - Headline: --display_error_number (-pden) not working during lnk cmd file parsing and in shell
 - Fixed in Version: 6.1.1
2. ClearQuest ID: [SDSCM00043197](#)
 - Headline: Compiler may not issue DP loads in some cases when compiling using -o4
 - Fixed in Version: 6.1.1
3. ClearQuest ID: [SDSCM00045105](#)
 - Headline: Empty struct as field of parent struct may cause optimiser abort
 - Fixed in Version: 6.1.1
4. ClearQuest ID: [SDSCM00041434](#)
 - Headline: Compiler optimizes away certain calls to assert()
 - Fixed in Version: 6.1.1
5. ClearQuest ID: [SDSCM00044559](#)
 - Headline: Compiler incorrectly places MOV between MAX and MAXCUL which causes incorrect result
 - Fixed in Version: 6.1.1
6. ClearQuest ID: [SDSCM00043868](#)
 - Headline: Linker cannot find include file specified with relative path
 - Fixed in Version: 6.1.1
7. ClearQuest ID: [SDSCM00044468](#)
 - Headline: The __norm32 intrinsic does not generate CSB instruction resulting in incorrect result
 - Fixed in Version: 6.1.1
8. ClearQuest ID: [SDSCM00043720](#)
 - Headline: Prologue code rescheduling causes C2000 register allocation failure
 - Fixed in Version: 6.1.1
9. ClearQuest ID: [SDSCM00043713](#)
 - Headline: Linker fails with internal error
 - Fixed in Version: 6.1.1
10. ClearQuest ID: [SDSCM00043174](#)
 - Headline: Linker fails to honor specific placement for function from RTS library
 - Fixed in Version: 6.1.1

11. ClearQuest ID: [SDSCM00044456](#)
 - Headline: C2000 match error: MPYB XARn, #imm
 - Fixed in Version: 6.1.1
12. ClearQuest ID: [SDSCM00034341](#)
 - Headline: __byte Intrinsic usage giving internal error
 - Fixed in Version: 6.1.1
13. ClearQuest ID: [SDSCM00045170](#)
 - Headline: C2000 disassembler does not decode XCALL instruction when file is compiled with --c2xlp_src_compatible
 - Fixed in Version: 6.1.1
14. ClearQuest ID: [SDSCM00042948](#)
 - Headline: Writing to wrong memory location
 - Fixed in Version: 6.1.1
15. ClearQuest ID: [SDSCM00044216](#)
 - Headline: Link time optimization produces error about option --optimize_with_debug missing its argument
 - Fixed in Version: 6.1.1
16. ClearQuest ID: [SDSCM00043062](#)
 - Headline: Structure in .cdecls which contains bit fields does not match layout of C compiler
 - Fixed in Version: 6.1.1
17. ClearQuest ID: [SDSCM00043996](#)
 - Headline: Linker changes EALLOW instruction to ITRAP 0 instruction, which encodes as all 0's
 - Fixed in Version: 6.1.1
18. ClearQuest ID: [SDSCM00044736](#)
 - Headline: Loop nest over two-dimensional array compiles wrong if inner loop has early exit
 - Fixed in Version: 6.1.1
19. ClearQuest ID: [SDSCM00044048](#)
 - Headline: Enabling vectorization produces incorrect code
 - Fixed in Version: 6.1.1
20. ClearQuest ID: [SDSCM00045141](#)
 - Headline: C2000 generates incorrect code for __norm64() when compiling with optimization
 - Fixed in Version: 6.1.1
21. ClearQuest ID: [SDSCM00043325](#)
 - Headline: Compiler is incorrectly calculating RPTB block size and generating assembler error "Block size out of range"
 - Fixed in Version: 6.1.1
22. ClearQuest ID: [SDSCM00044472](#)
 - Headline: c2xlp_src_compatible causes error: label defined differently in each pass
 - Fixed in Version: 6.1.1
23. ClearQuest ID: [SDSCM00044969](#)
 - Headline: CLA compiler does not allow initialization of constant variables

- Fixed in Version: 6.1.1
24. ClearQuest ID: [SDSCM00045200](#)
- Headline: C2000 register allocation failure with unsigned int array index variable also used as multiply operand.
 - Fixed in Version: 6.1.1