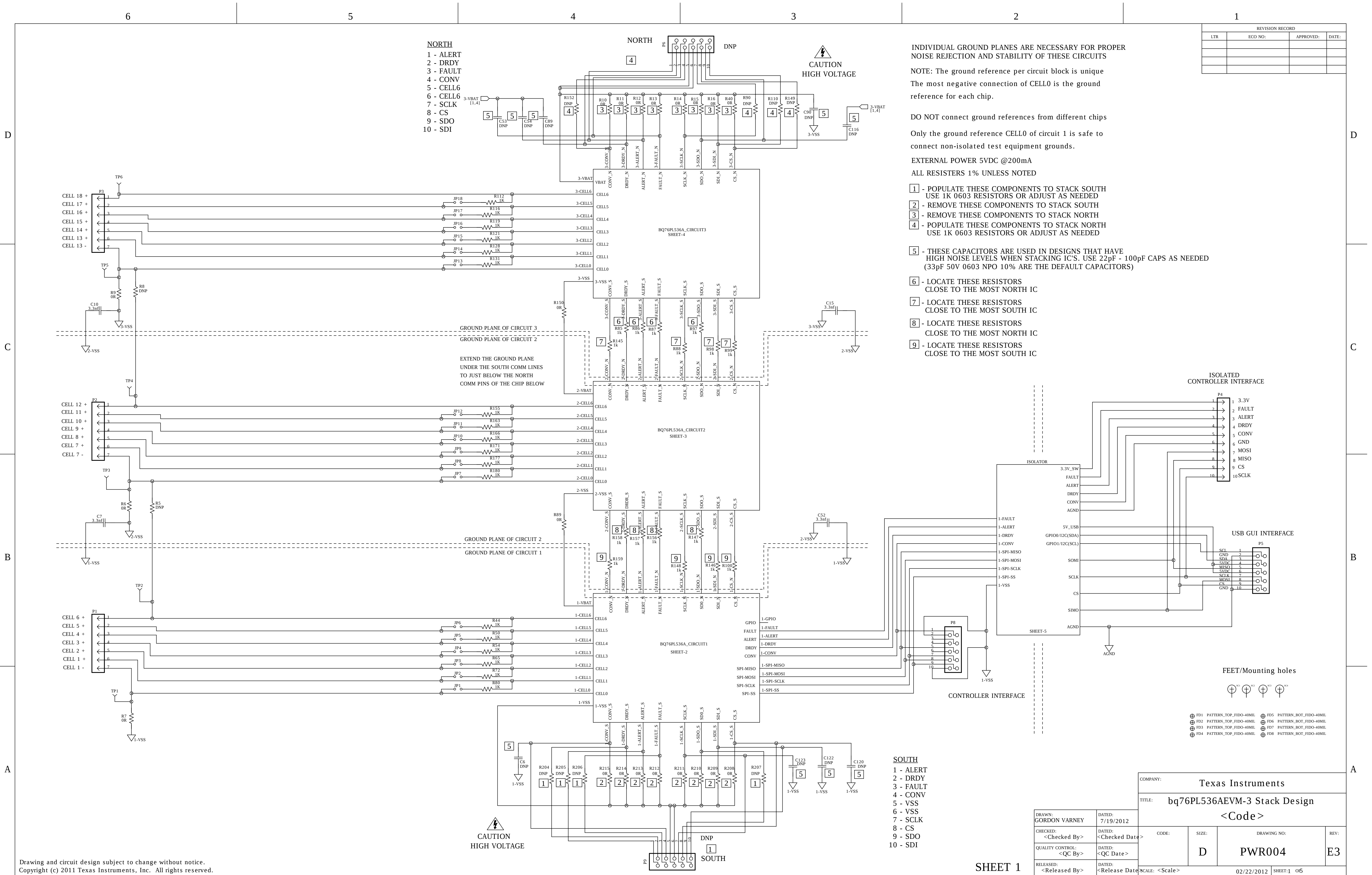




D

C

B

A

D

C

B

A

ALL RESISTERS 1% UNLESS NOTED
** - Locate these components
very close to U1.

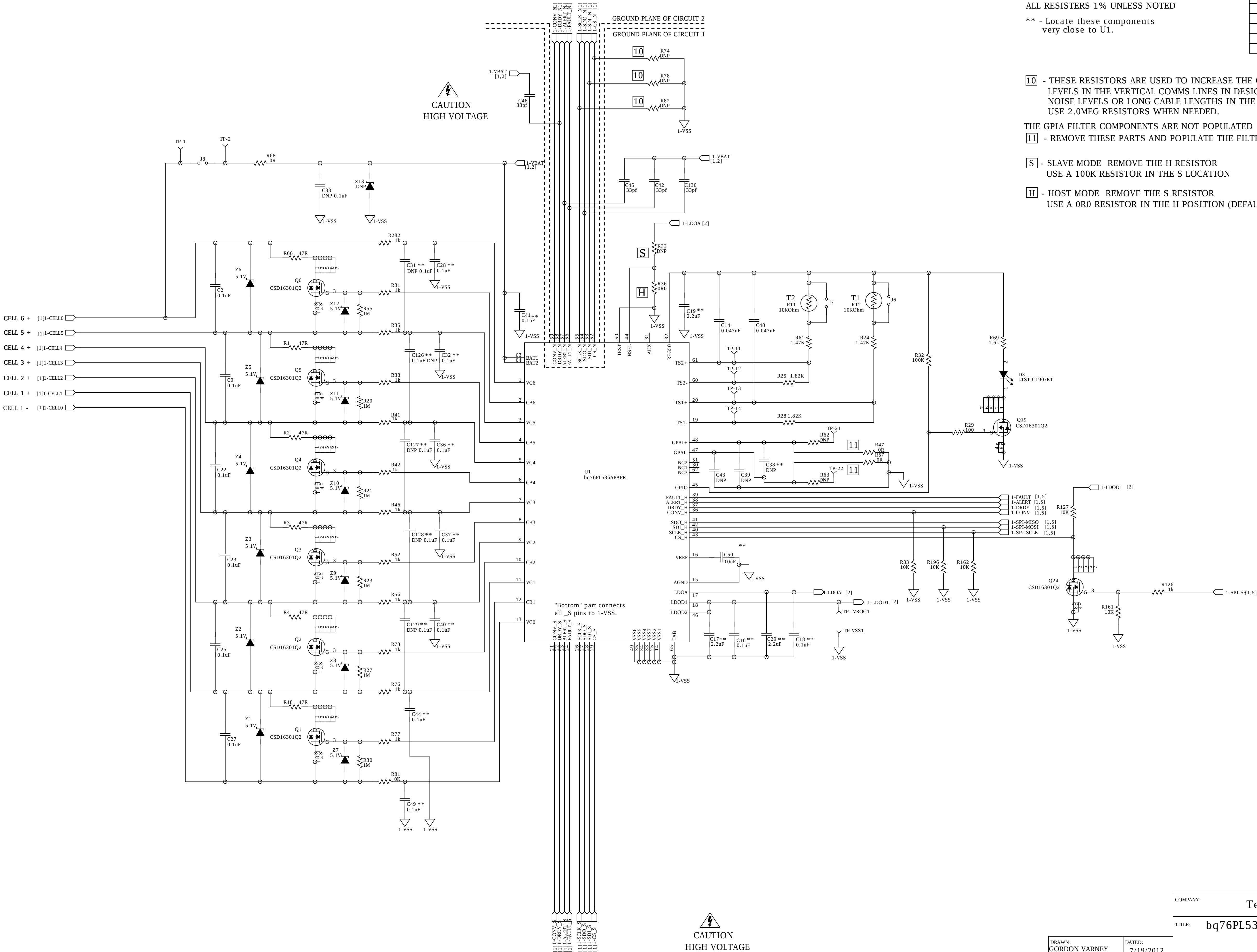
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

[10] - THESE RESISTORS ARE USED TO INCREASE THE CURRENT
LEVELS IN THE VERTICAL COMMS LINES IN DESIGNS THAT HAVE HIGH
NOISE LEVELS OR LONG CABLE LENGTHS IN THE VERTICAL COMMS
USE 2.0MEG RESISTORS WHEN NEEDED.

THE GPIA FILTER COMPONENTS ARE NOT POPULATED
[11] - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED

[S] - SLAVE MODE REMOVE THE H RESISTOR
USE A 100K RESISTOR IN THE S LOCATION

[H] - HOST MODE REMOVE THE S RESISTOR
USE A 0R0 RESISTOR IN THE H POSITION (DEFAULT)



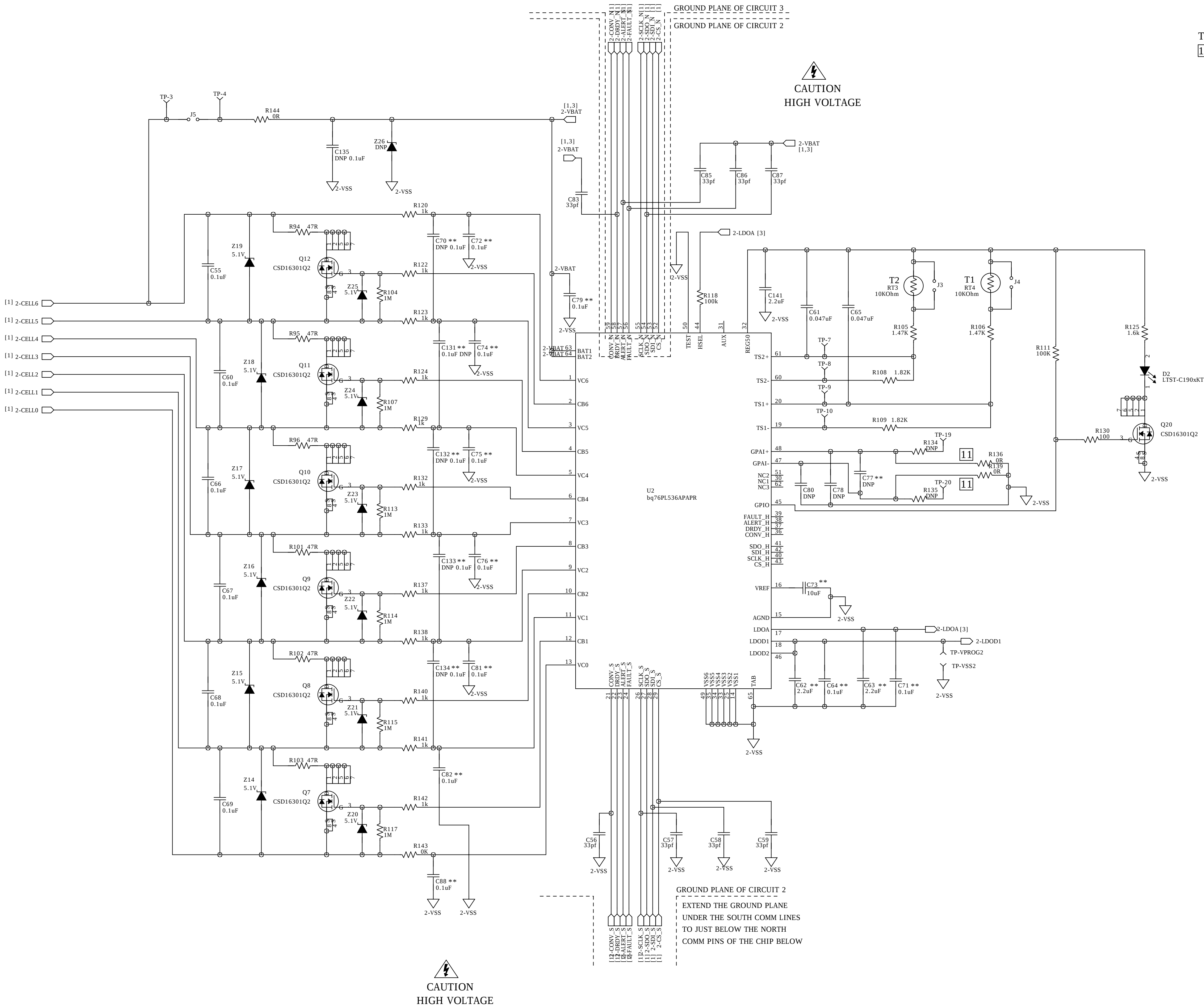
DRAWN: GORDON VARNEY		DATED: 7/19/2012		<Code>			
CHECKED: <Checked By>		DATED: <Checked Date>		CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>		DATED: <QC Date>			D	CIRCUIT 1 PWR004	E3
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>			SHEET: 2 OF 5

ALL RESISTERS 1% UNLESS NOTED

** - Locate these components
very close to U2.

THE GPIA FILTER COMPONENTS ARE NOT POPULATED
[12] - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



CAUTION
HIGH VOLTAGE

CAUTION
HIGH VOLTAGE

GROUND PLANE OF CIRCUIT 2
EXTEND THE GROUND PLANE
UNDER THE SOUTH COMM LINES
TO JUST BELOW THE NORTH
COMM PINS OF THE CHIP BELOW

DRAWN: GORDON VARNEY		DATED: 7/19/2012		<Code>			
CHECKED: <Checked By>		DATED: <Checked Date>		CODE:	SIZE: D	DRAWING NO: CIRCUIT 2 PWR004	REV: E3
QUALITY CONTROL: <QC By>		<QC Date>					
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>		SHEET: 3 OF 5	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

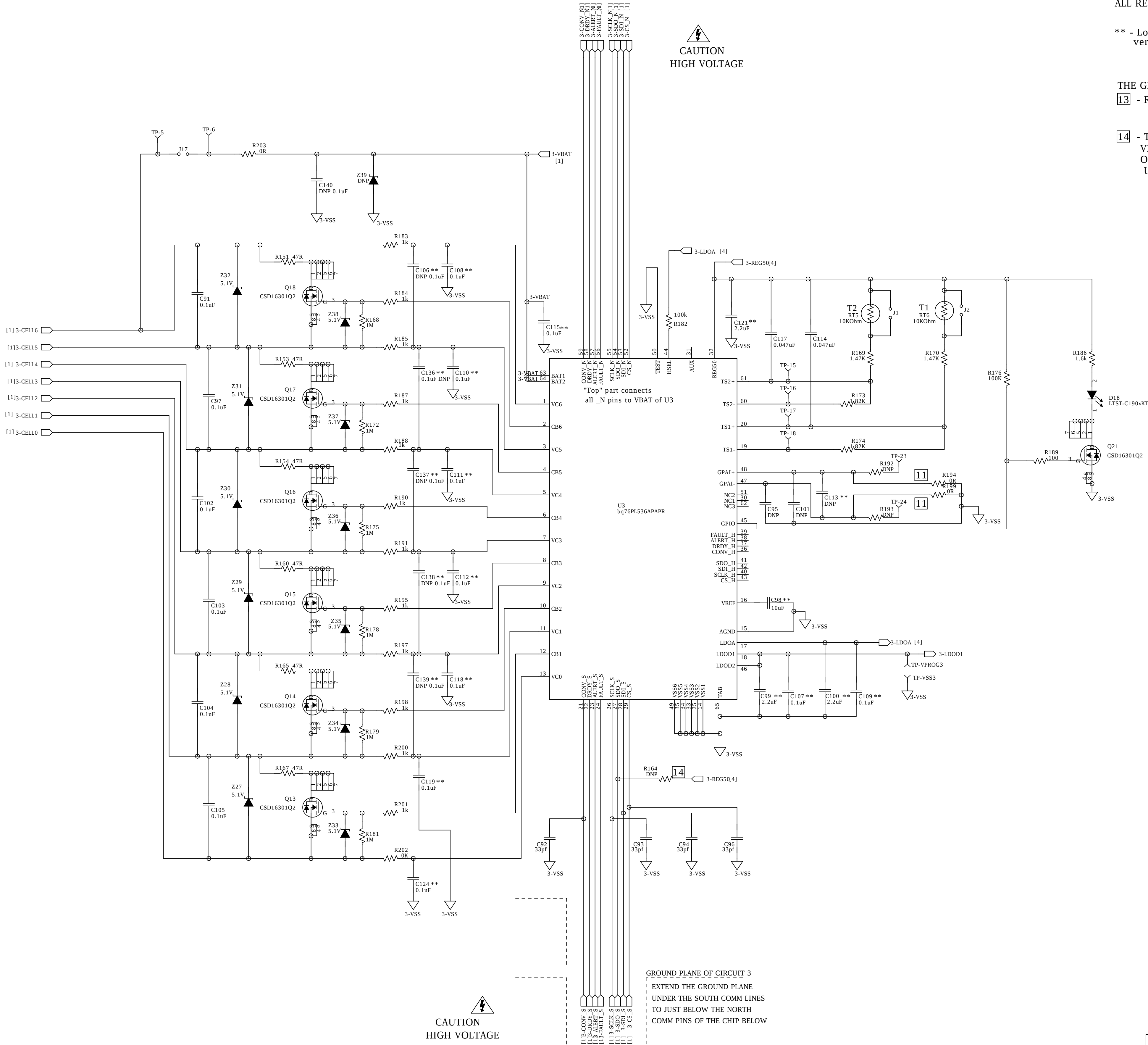
ALL RESISTERS 1% UNLESS NOTED

** - Locate these components
very close to U3.

THE GP1A FILTER COMPONENTS ARE NOT POPULATED

[13] - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED

[14] - THIS RESISTOR IS USED TO INCREASE THE CURRENT LEVELS IN THE
VERTICAL COMMS LINES IN DESIGNS THAT HAVE HIGH NOISE LEVELS
OR LONG CABLE LENGTHS IN THE VERTICAL COMM PATHS.
USE A 512K RESISTOR WHEN NEEDED



COMPANY: Texas Instruments				
TITLE: bq76PL536AEVM-3 Stack Design <Code>				
>	CODE:	SIZE: D	DRAWING NO: CIRCUIT 3 PWR004	REV: E3
SCALE: <Scale>			SHEET: 4 OF 5	

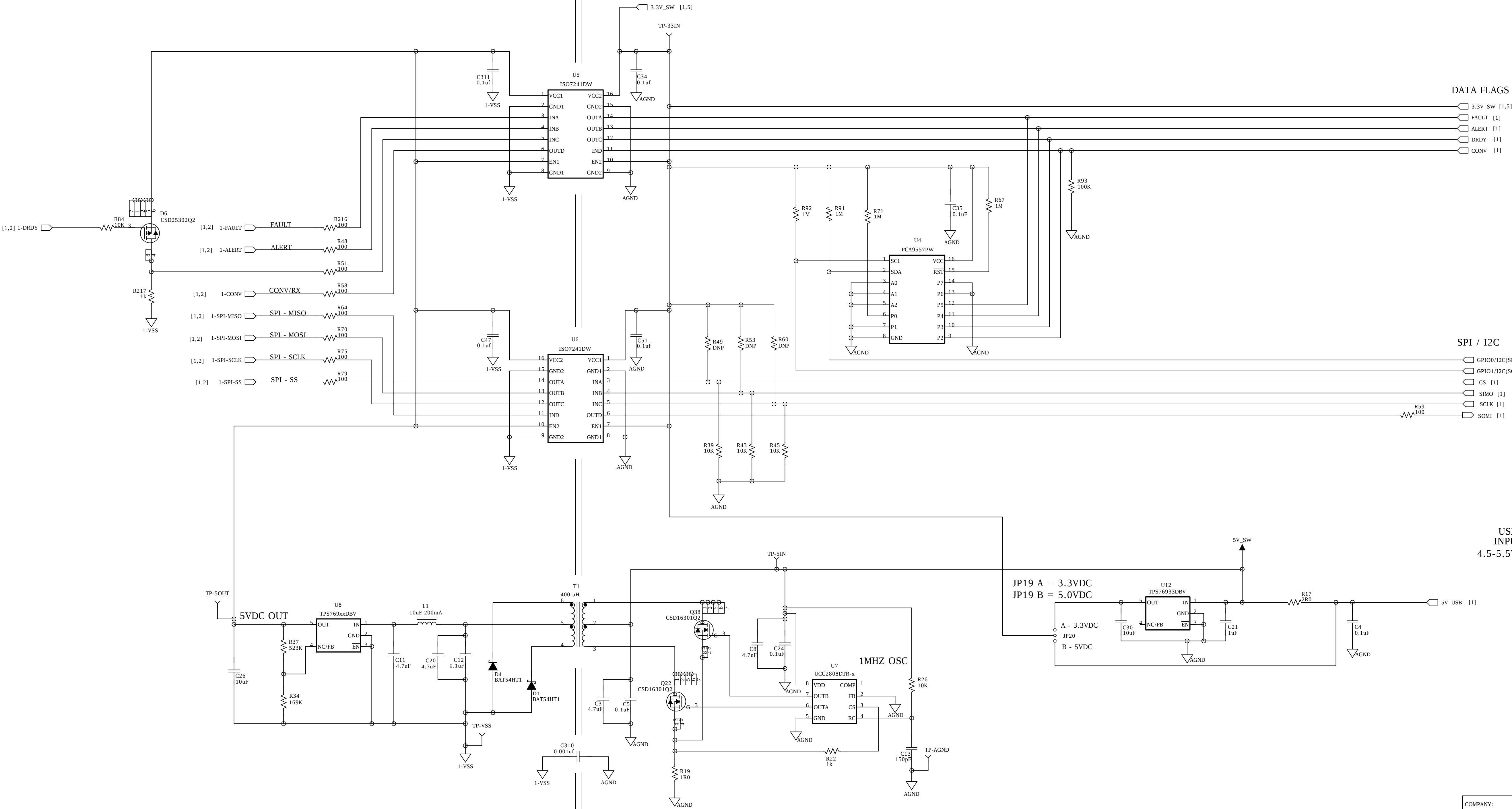
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

ALL RESISTERS 1% UNLESS NOTED

CAUTION: WHEN USING AN EXTERNAL MICROCONTROLLER
PULL UP RESISTORS CAN APPLY VOLTAGE TO THE MICRO WHEN
THE MICRO IS POWERED DOWN.

CAUTION
HIGH VOLTAGE

ISOLATION BOUNDRY
48 MIL ISOLATION REQUIRED



USB
INPUT
4.5-5.5VDC

JP19 A = 3.3VDC
JP19 B = 5.0VDC

CAUTION
HIGH VOLTAGE

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DRAWN: GORDON VARNEY	DATED: 7/19/2012
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: Texas Instruments			
TITLE: bq76PL536AEVM-3 Stack Design <Code>			
CODE:	SIZE: D	DRAWING NO: ISOLATOR PWR004	REV: E3
SCALE: <Scale>			SHEET: 5 OF 5