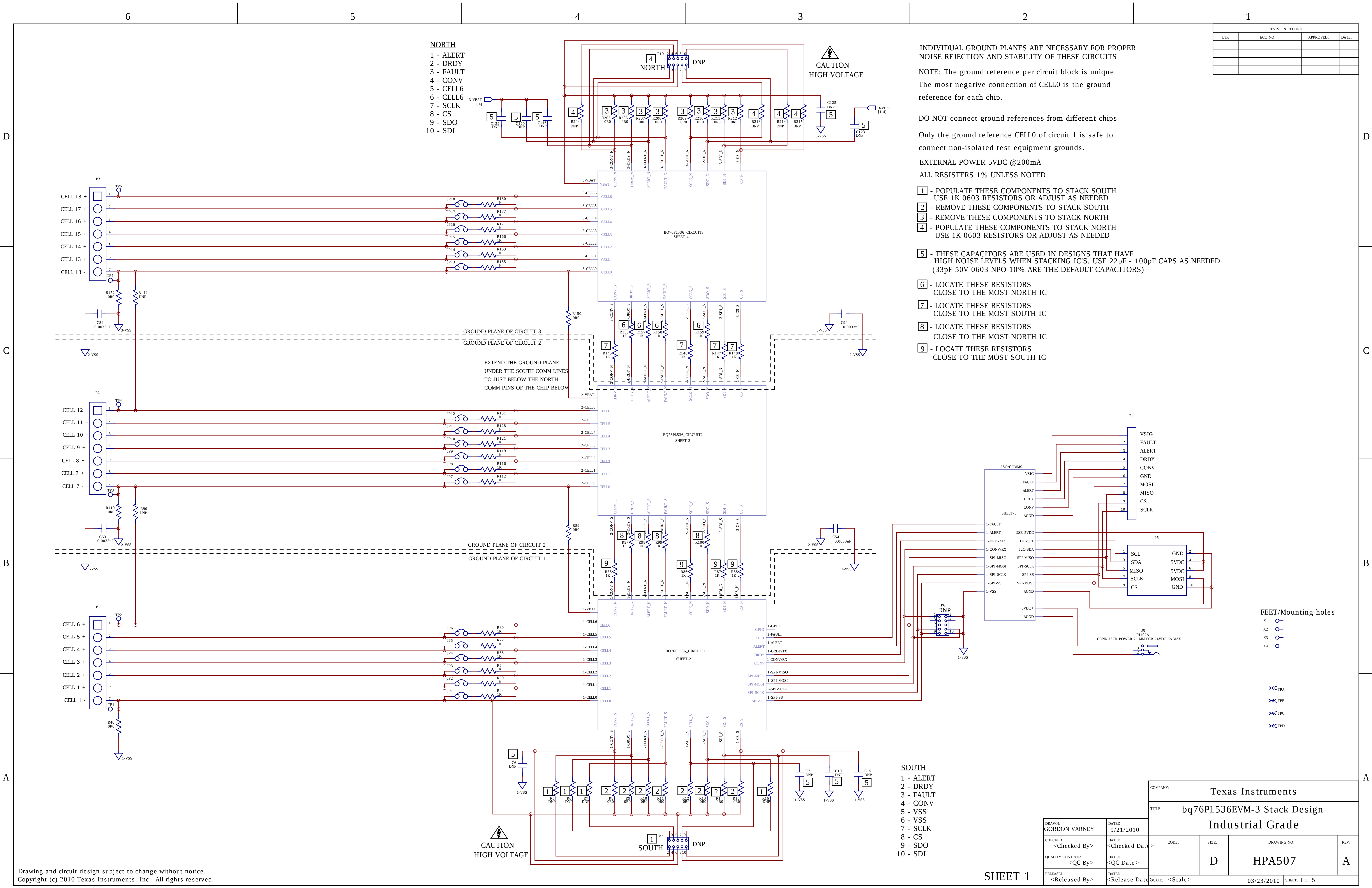




DRAWN: GORDON VARNEY		DATED: 9/21/2010		COMPANY: Texas Instruments	
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: bq76PL536EVM-3 Stack Design Industrial Grade	
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE:	SIZE:
RELEASED: <Released By>		DATED: <Release Date>		D	HPA507
				REV:	A
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

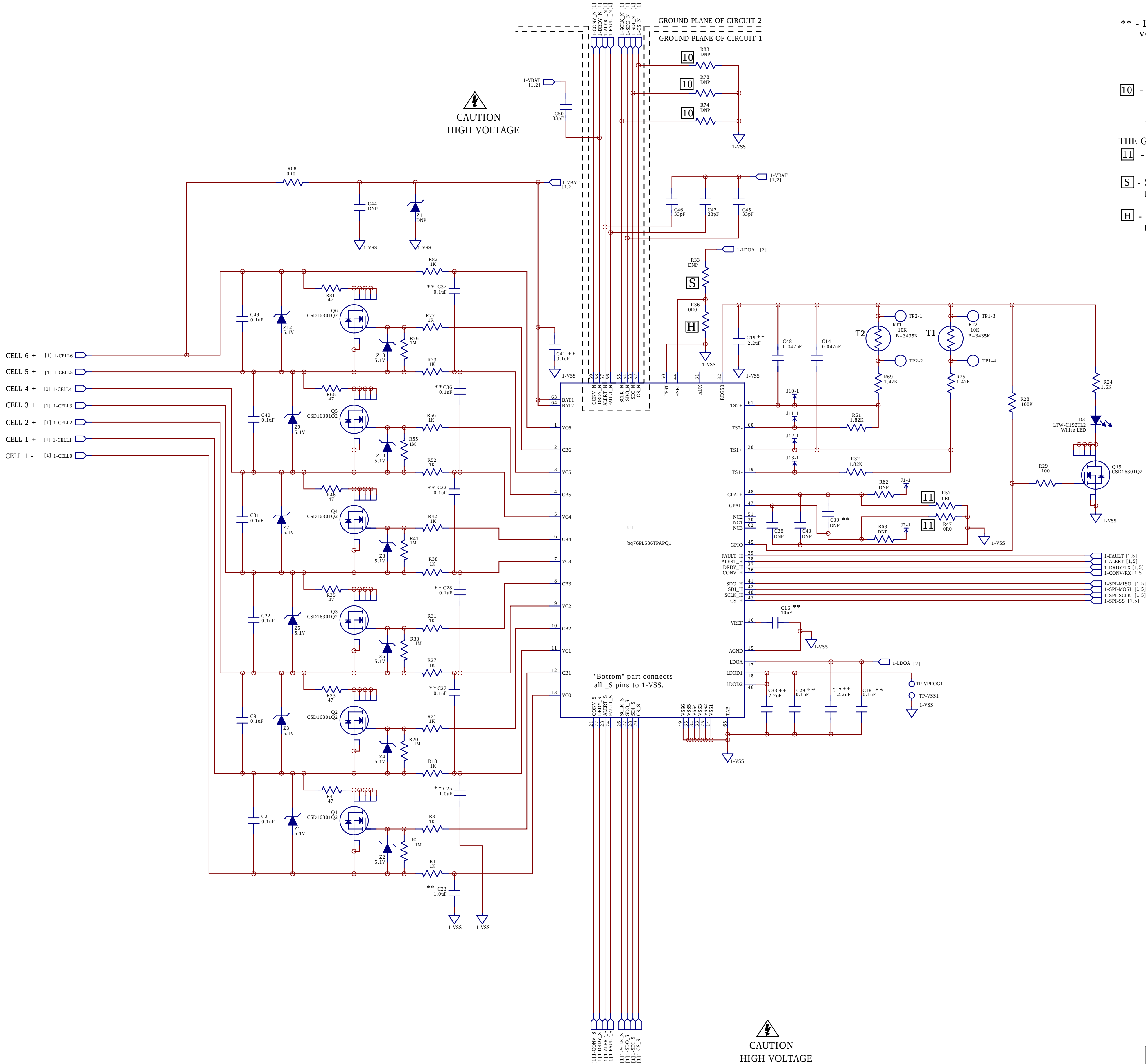
** - Locate these components very close to U1.

[10] - THESE RESISTORS ARE USED TO INCREASE THE CURRENT LEVELS IN THE VERTICAL COMMS LINES IN DESIGNS THAT HAVE HIGH NOISE LEVELS OR LONG CABLE LENGTHS IN THE VERTICAL COMMS USE 2.0MEG RESISTORS WHEN NEEDED.

THE GP1A FILTER COMPONENTS ARE NOT POPULATED
[11] - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED

[S] - SLAVE MODE REMOVE THE H RESISTOR
USE A 100K RESISTOR IN THE S LOCATION

[H] - HOST MODE REMOVE THE S RESISTOR
USE A 0R0 RESISTOR IN THE H POSITION (DEFAULT)



ALL RESISTERS 5% UNLESS NOTED

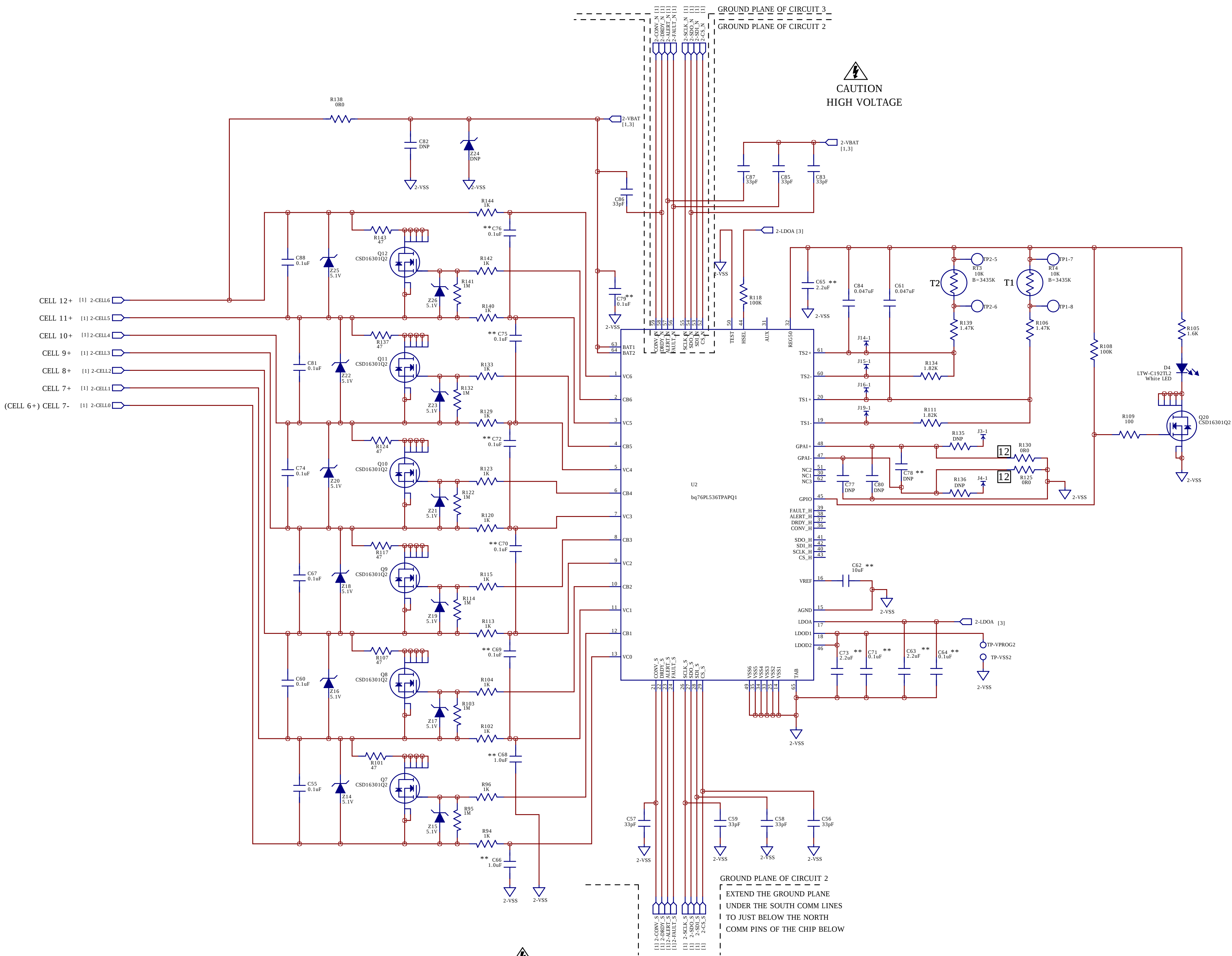
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COMPANY:		Texas Instruments		
TITLE:		bq76PL536EVM-3 Stack Design Industrial Grade		
>	CODE:	SIZE: D	DRAWING NO: CIRCUIT 1 HPA507	REV: A
SCALE: <Scale>			SHEET: 2 OF 5	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

** - Locate these components very close to U2.

THE GPIA FILTER COMPONENTS ARE NOT POPULATED
[12] - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED



CAUTION
HIGH VOLTAGE

ALL RESISTERS 5% UNLESS NOTED

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CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: bq76PL536EVM-3 Stack Design Industrial Grade	
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE:	SIZE:
RELEASED: <Released By>		DATED: <Release Date>		DRAWING NO: CIRCUIT 2 HPA507	
				SCALE: <Scale>	REV: A
				SHEET: 30F 5	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

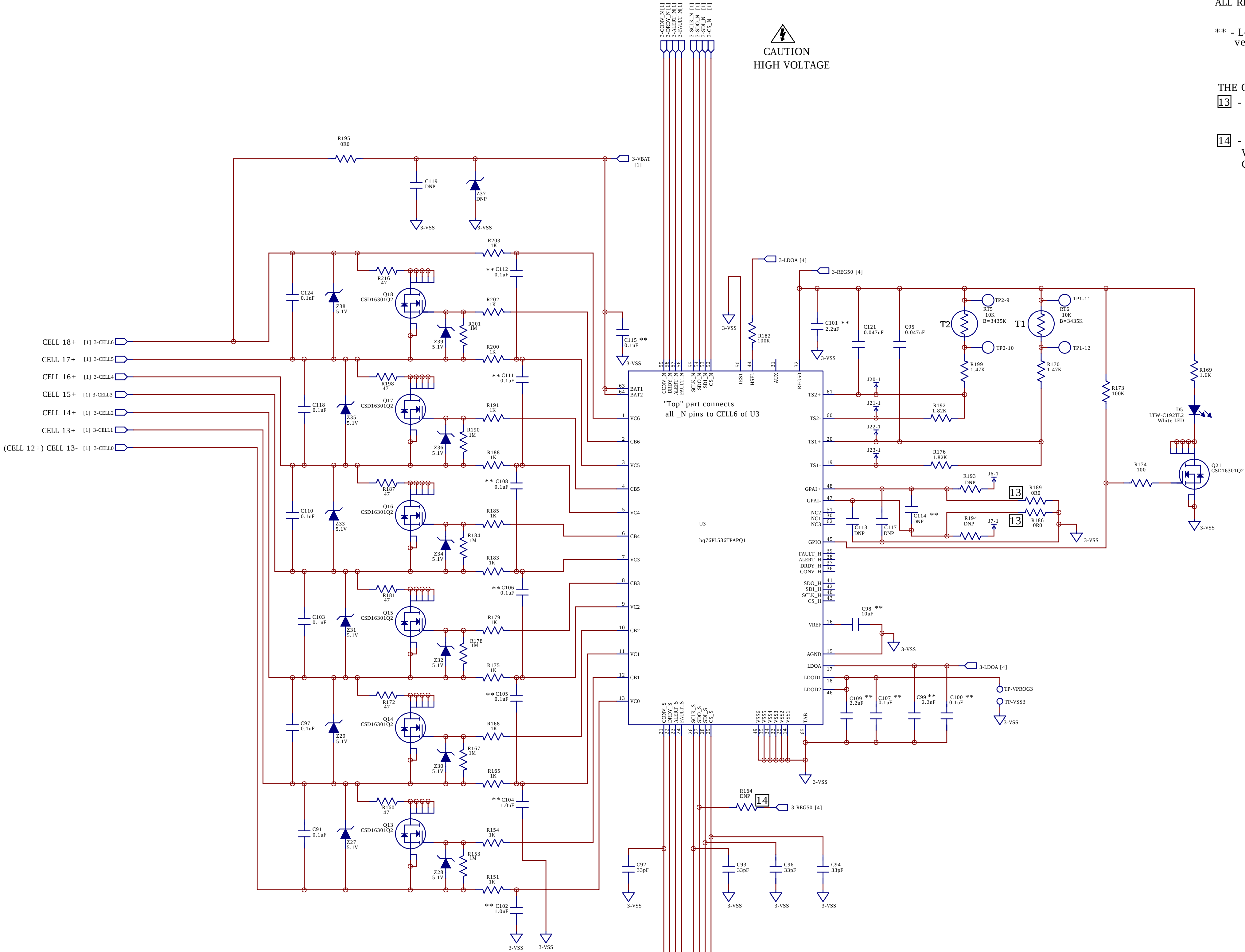
ALL RESISTERS 1% UNLESS NOTED

** - Locate these components
very close to U3.

THE GPIA FILTER COMPONENTS ARE NOT POPULATED

13 - REMOVE THESE PARTS AND POPULATE THE FILTER AS NEEDED

14 - THIS RESISTOR IS USED TO INCREASE THE CURRENT LEVELS IN THE
VERTICAL COMMS LINES IN DESIGNS THAT HAVE HIGH NOISE LEVELS
OR LONG CABLE LENGTHS IN THE VERTICAL COMM PATHS.
USE A 512K RESISTOR WHEN NEEDED



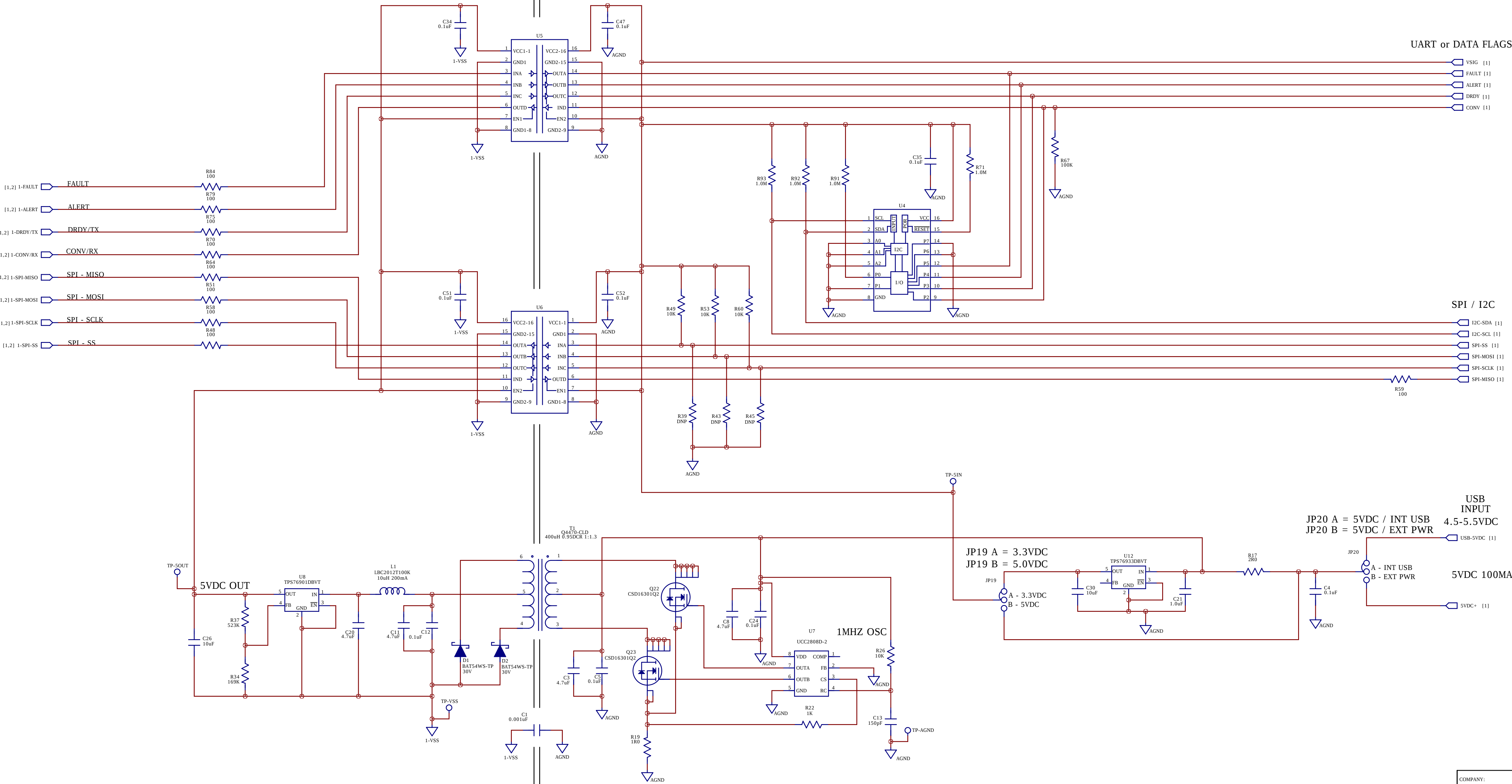
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



ISOLATION BOUNDRY
48 MIL ISOLATION REQUIRED

ALL RESISTERS 1% UNLESS NOTED

CAUTION: WHEN USING AN EXTERNAL MICROCONTROLLER
PULL UP RESISTORS CAN APPLY VOLTAGE TO THE MICRO WHEN
THE MICRO IS POWERED DOWN.



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COMPANY: Texas Instruments			
TITLE: bq76PL536EVM-3 Stack Design Industrial Grade			
DRAWN: GORDON VARNEY	DATED: 9/21/2010	DRAWING NO: ISO/COMMS HPA507	
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: D	REV: A
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
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