1.Icc vs number of active output

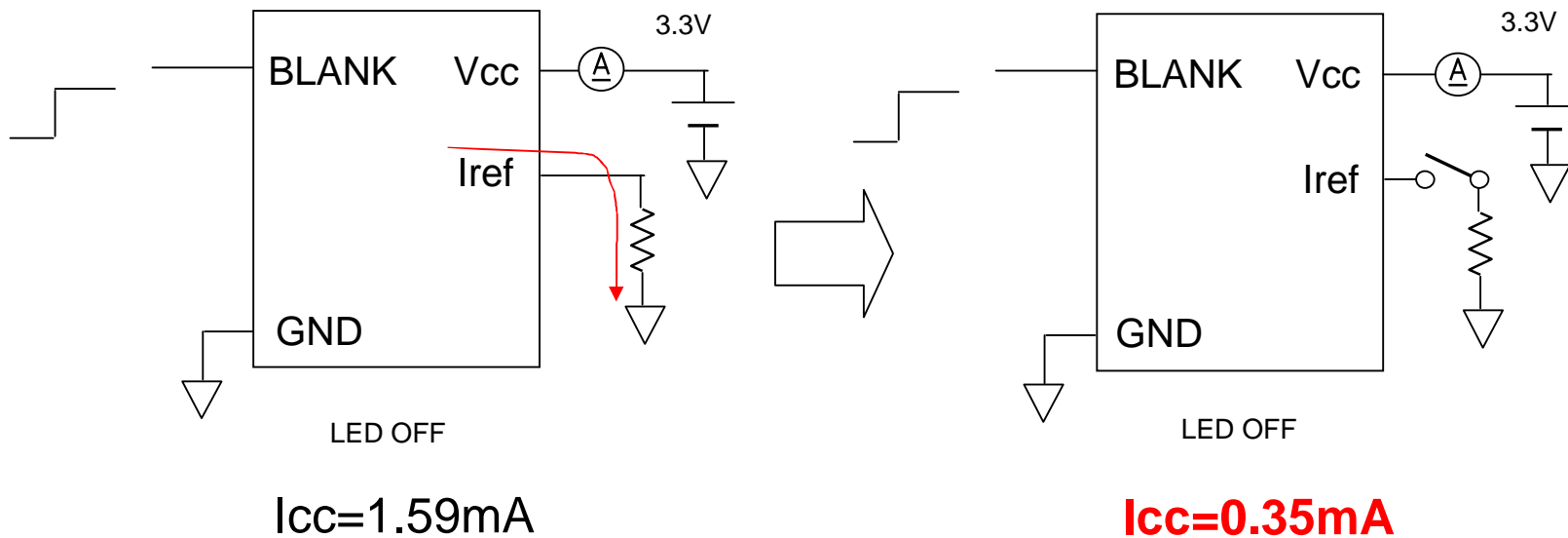| Output [ch]	Icc [mA]
0	1.59
1	1.66
4	1.85
8	2.10
16	2.61

Icc@Vcc=3.3V,Riref=5kohm [mA]
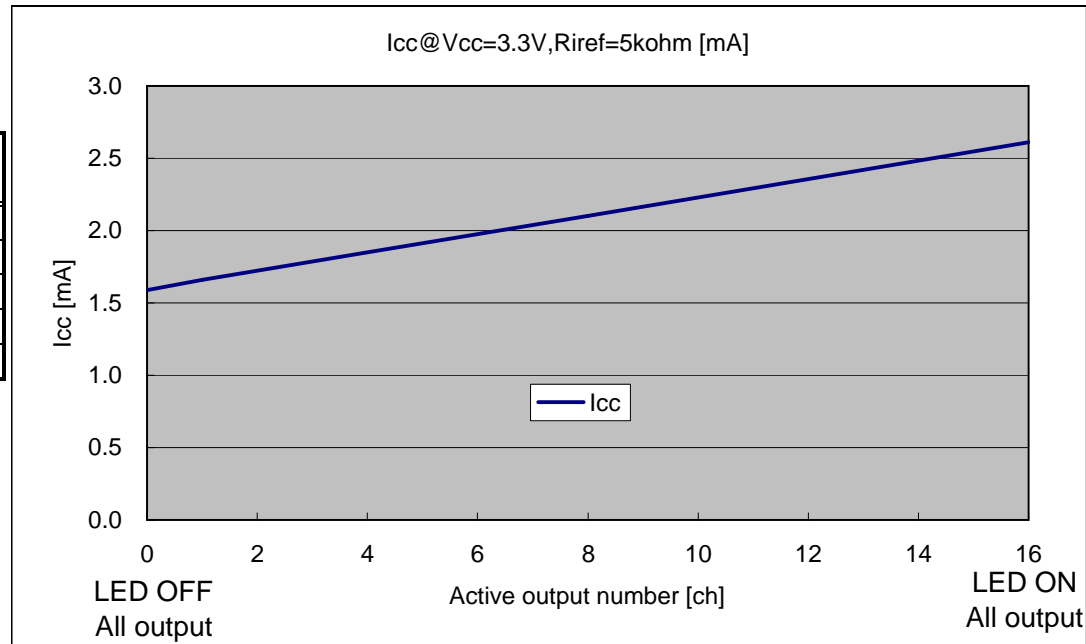
GSCLK is stopped at #1GSCLK to turn on.
This is not included a consumption of GSCLK.
*off output must input 000h(GS)data.If output is opened,Icc is increased.

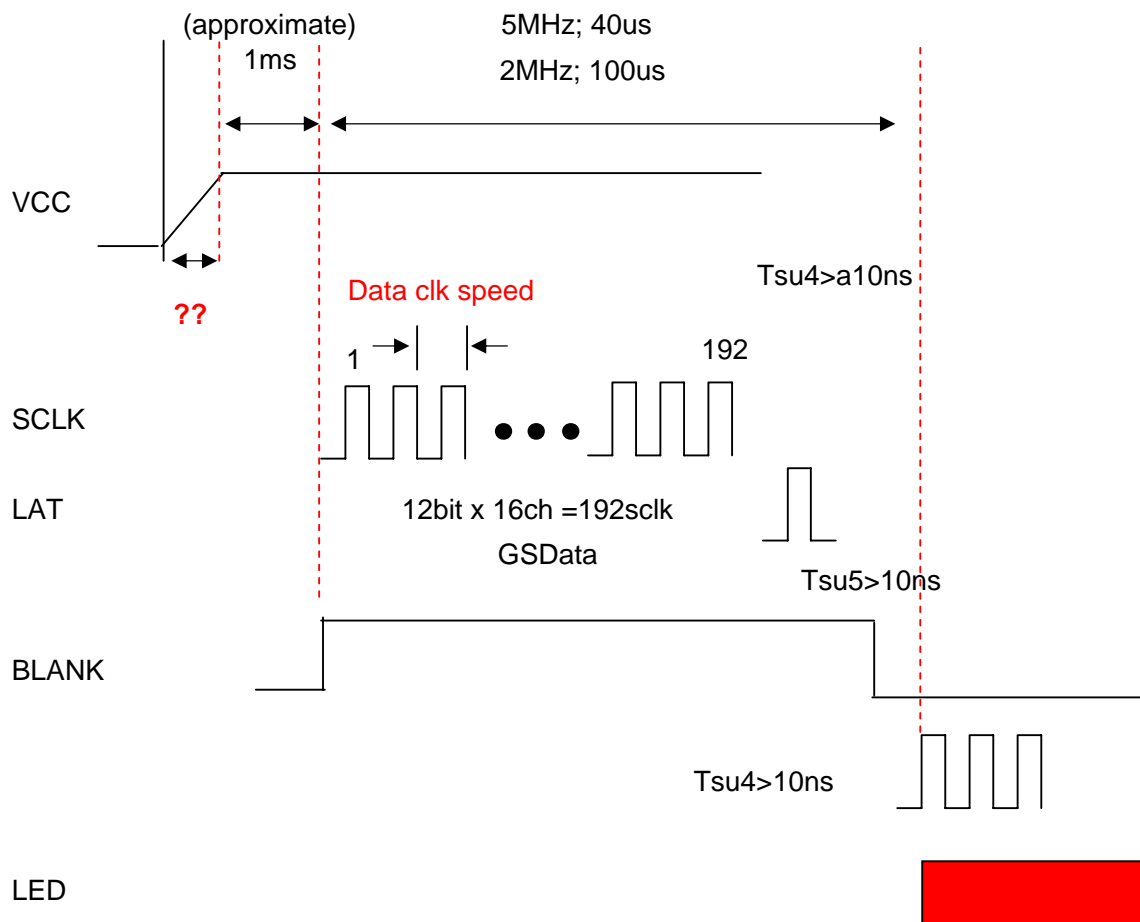## 2.Suggestion to save Icc at sleep mode

Icc=1.59mA

Icc=0.35mA

Icc goes through from Iref to ground during power on even if LED is turned off.
When this root is cut off, Icc can be saved and also can keep register data during power on.

3.Startup Time from power on to turn on.

In case of data transfer speed for GS data

33MHz; 6us

10MHz; 20us

5MHz; 40us

2MHz; 100us

(approximate)
1ms

VCC

Tsu4>a10ns

??

Data clk speed

1   192

SCLK

LAT

12bit x 16ch =192sclk

GSData

Tsu5>10ns

BLANK

Tsu4>10ns

LED

It takes 1ms approximately to startup internal circuit after stable Vcc voltage.
And it depends on the data transfer speed to be able to turn on.
In case of 33MHz transfer rate, it takes 1ms +6us. In case of 2MHz, it's 1ms +100us.