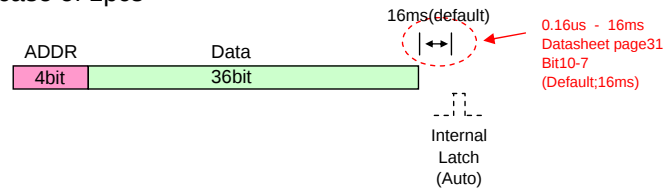
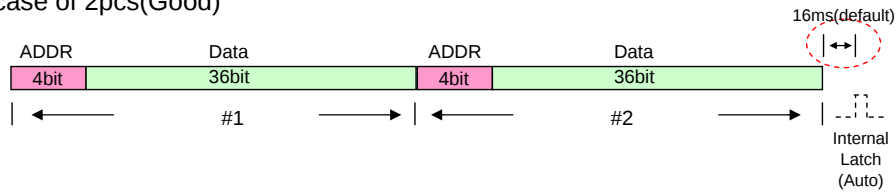


2.Cascading

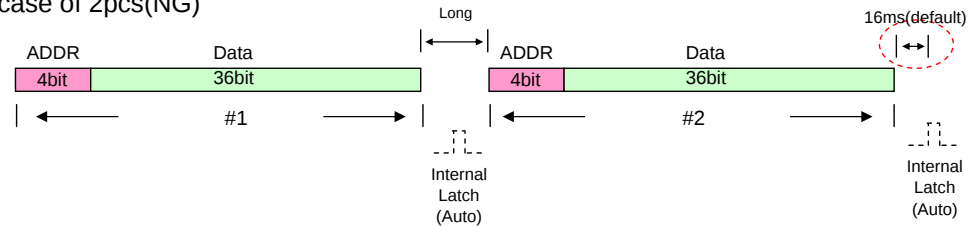
In case of 1pcs



In case of 2pcs(Good)



In case of 2pcs(NG)



Internal Latch is automatic after last CLK, so if between data and data has long wait time, some case is latched. Latch timing is setted. But after power on, data is 16ms as default value. Please see page 31 Bit 10-7.