

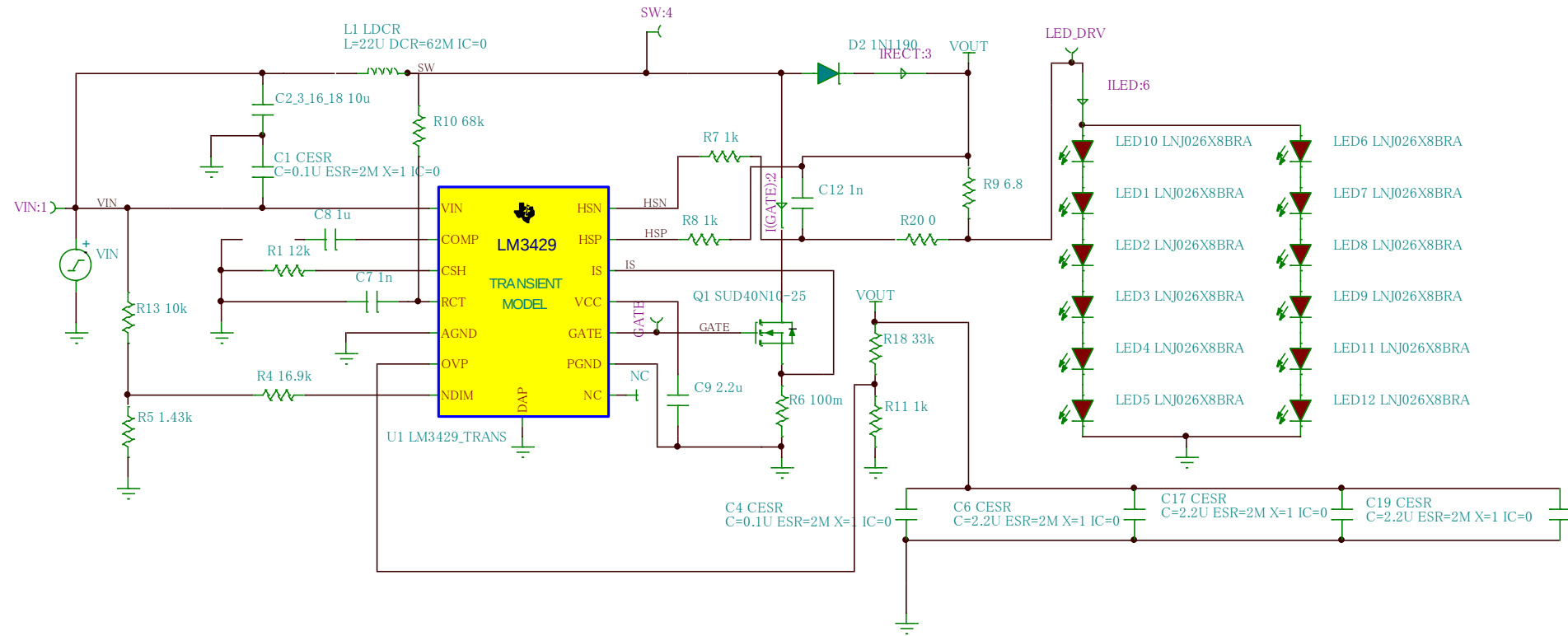
# **LM3429 gate signal simulation**

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**2014/10/31**

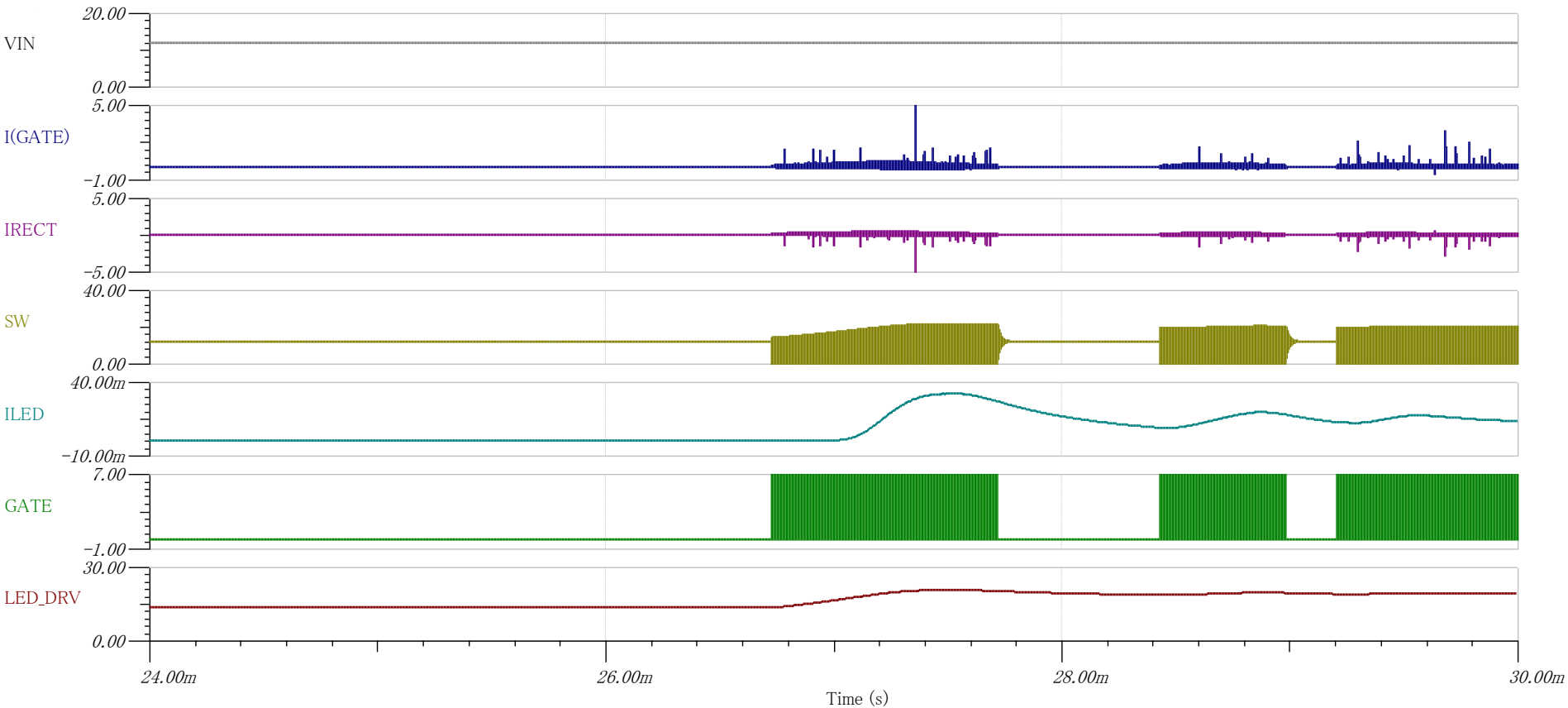
# LM3429

## gate signal simulation(customer circuit )



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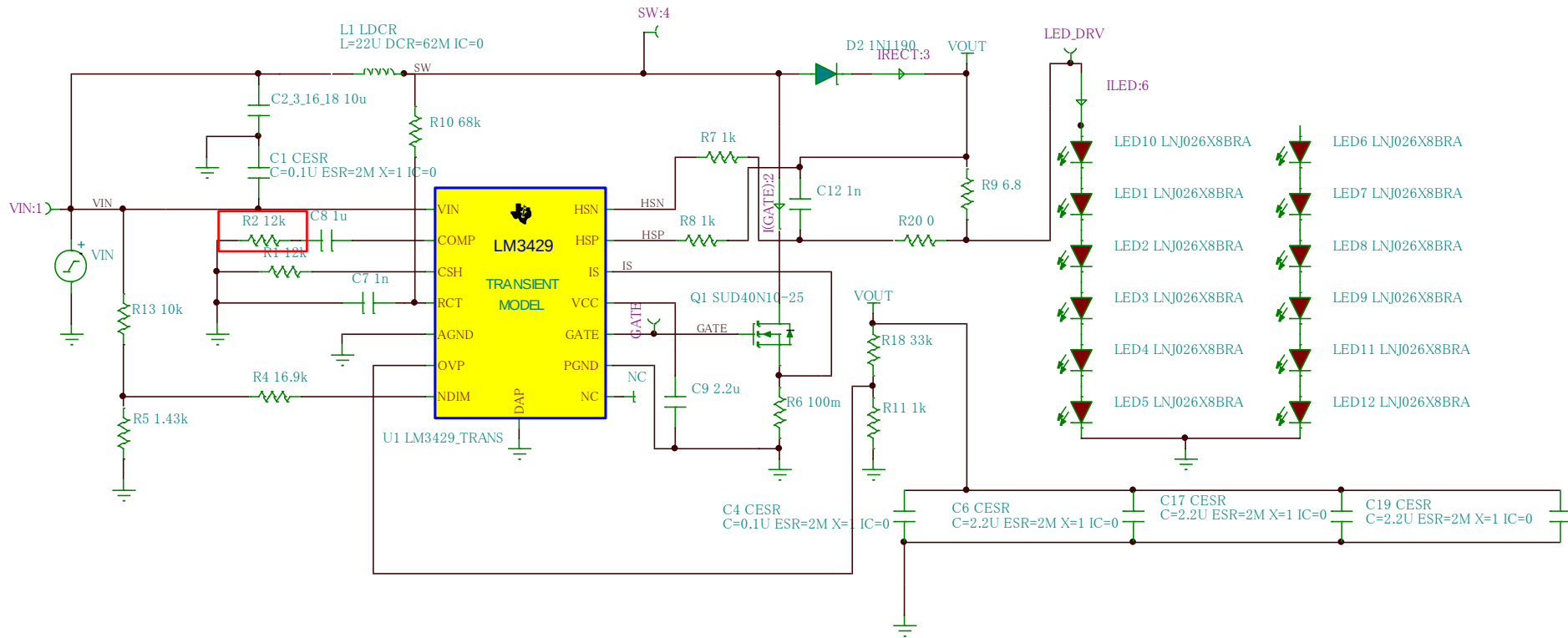


Test condition

- 0s 0V
- 10us 8V
- 110us 12V
- 20ms 12V

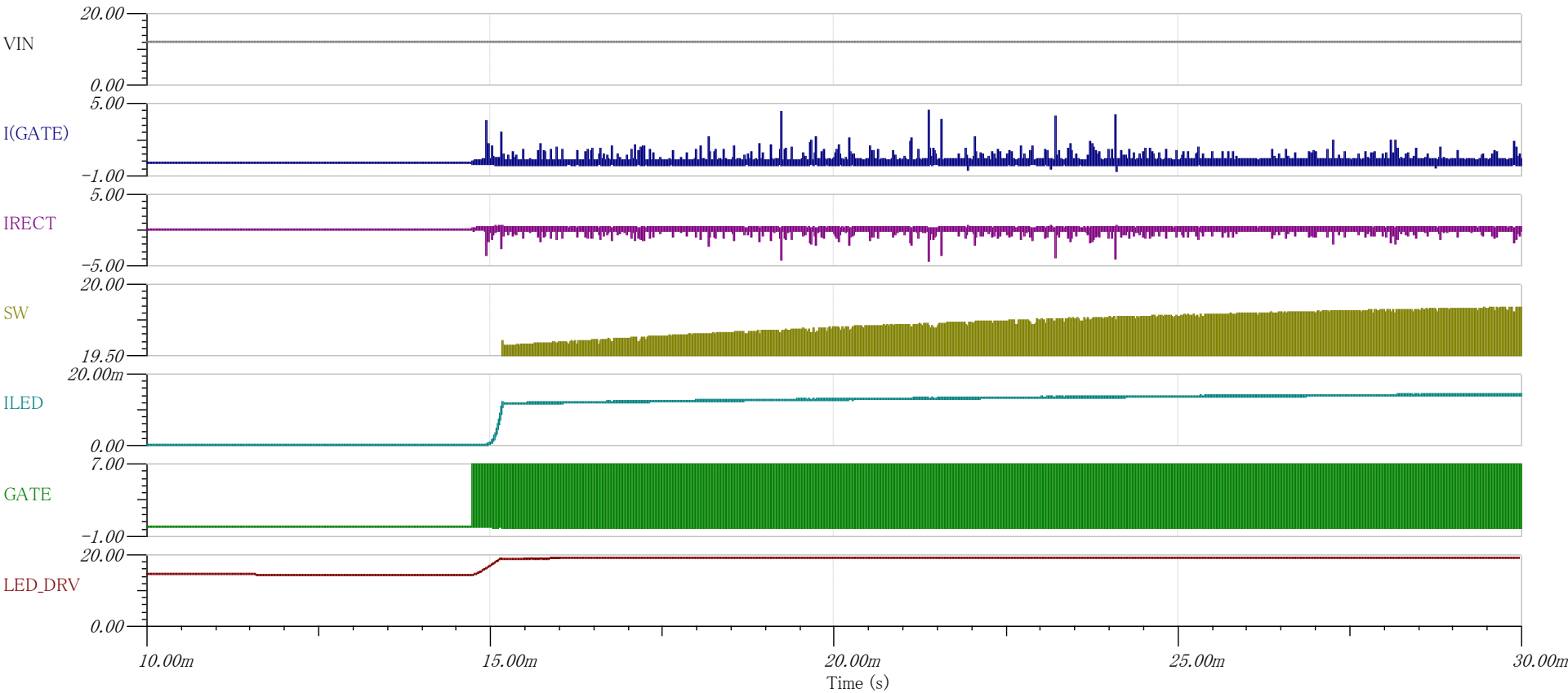
# LM3429

## gate signal simulation(COMP⇒R2 mount ver)



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