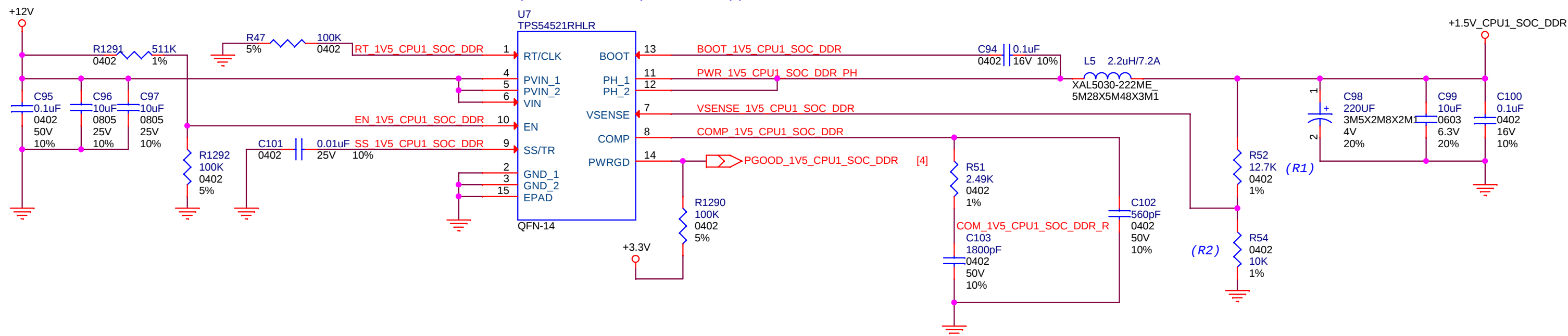


(Place all components within this block close together)

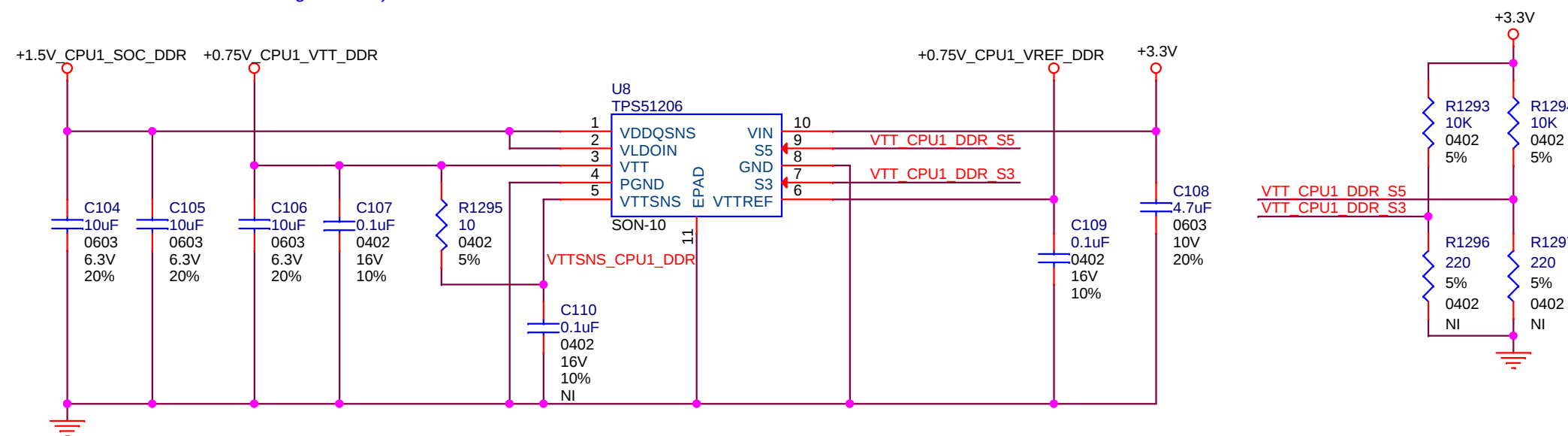
(+1.5V @ 5A)

$$(VOUT = 0.8 \times (1 + R1/R2))$$



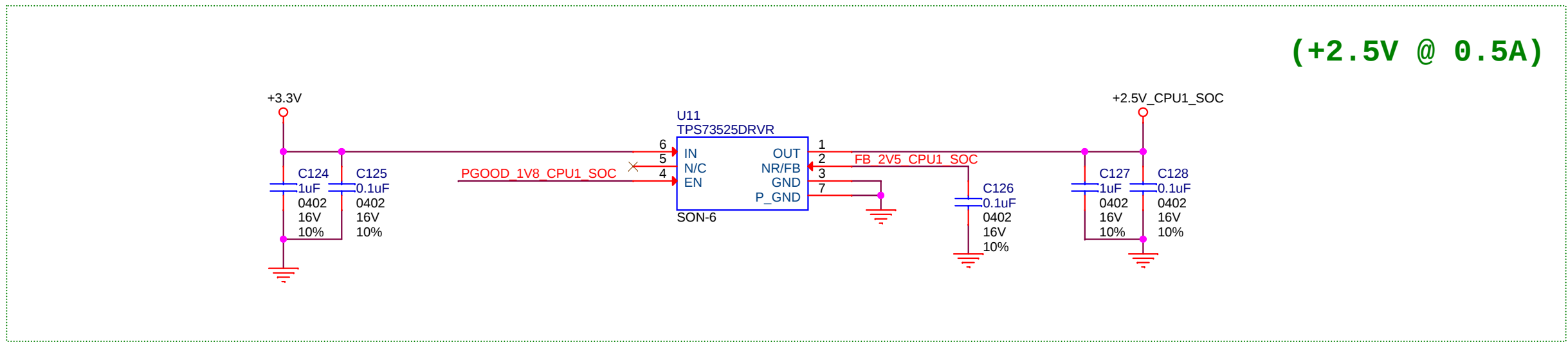
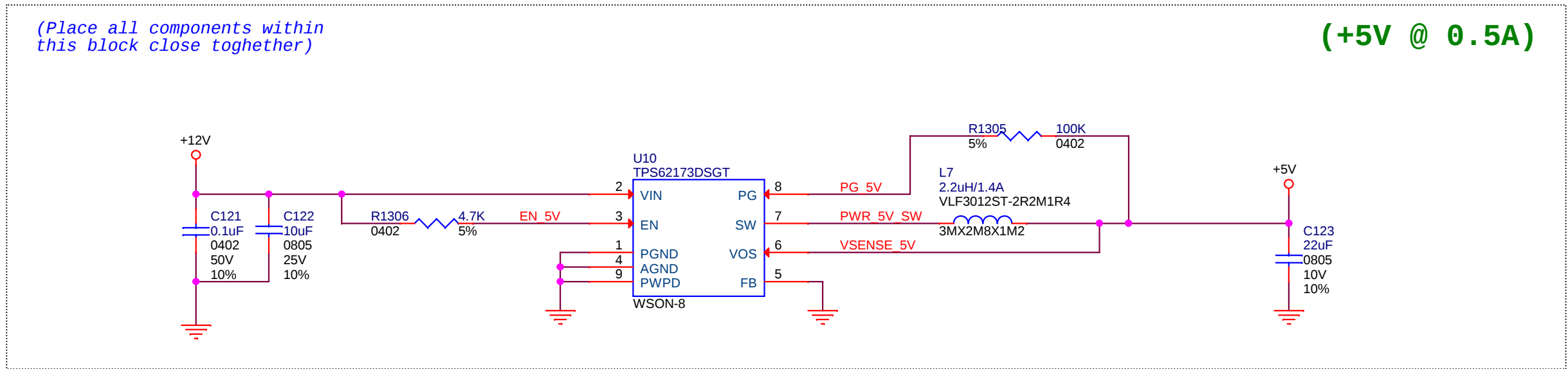
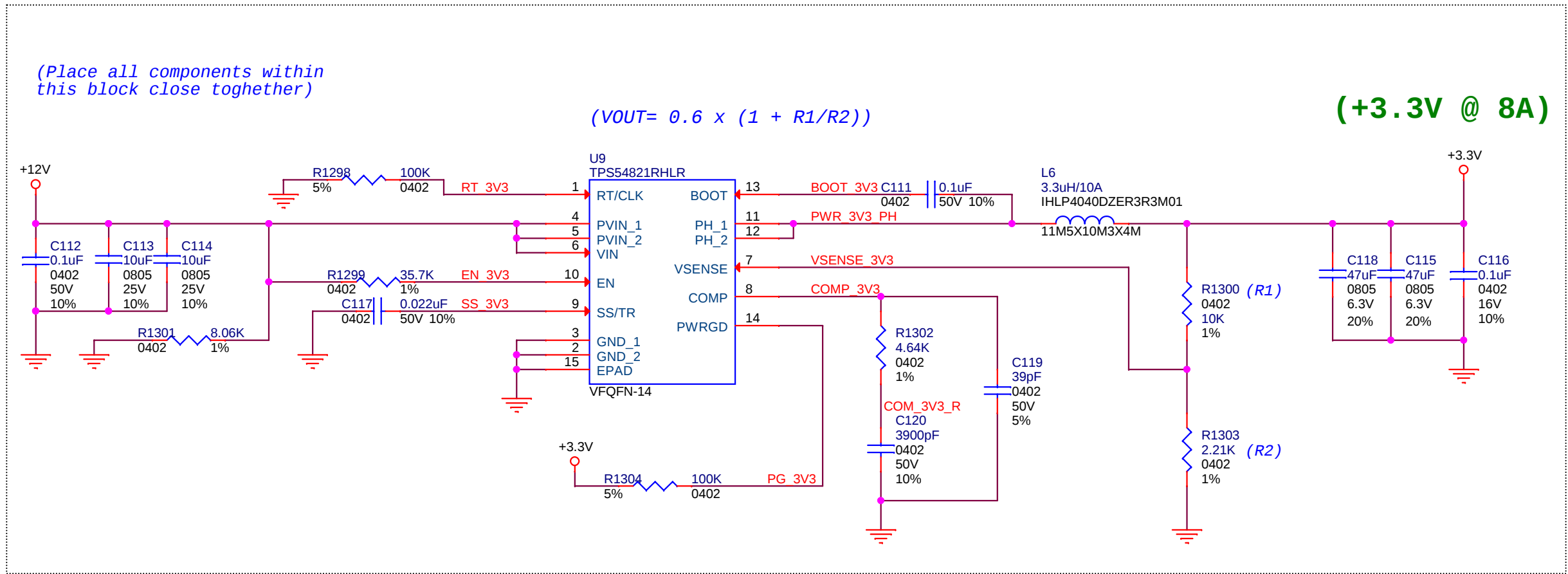
(Place all components within this block close together)

+0.75V_CPU1_VTT @ 2A Max



NOTE:
Refer to TPS51206 for specific routing the VTTSENS signal

Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCoc>
Date:	Wednesday, August 15, 2012	Sheet 1 of 2



Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCoc>
Date:	Wednesday, August 15, 2012	Sheet 2 of 2