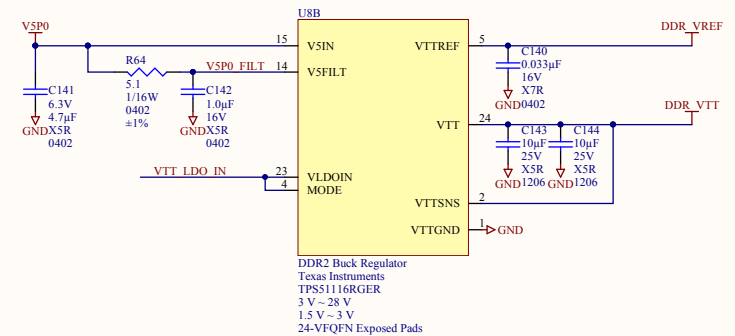
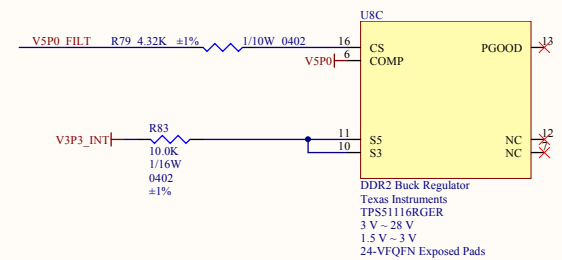
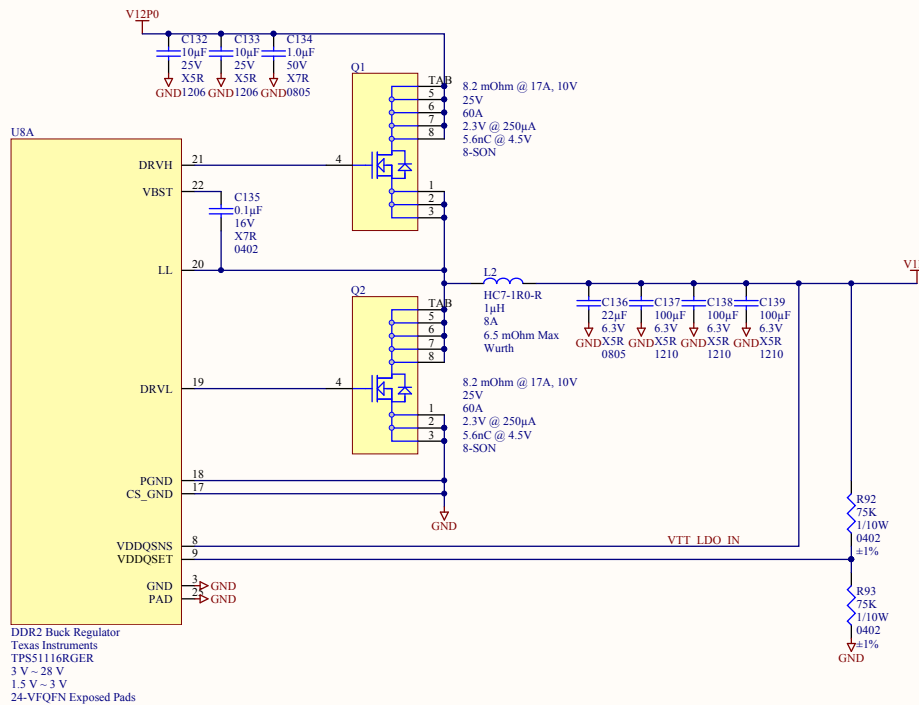
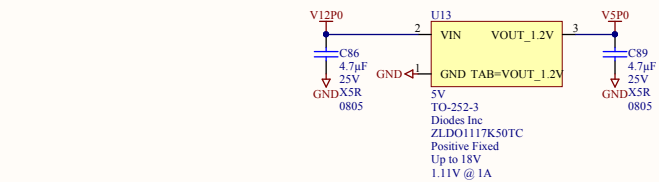




- Enable all regulators for the DDR2
- Enable the S3 for the DDR3 for the FPGA.
- Put the sense voltage for the VTT close to the DDR3 FPGA

Pin Functions, and uses

COMP: Output of the transconductance amplifier, since I'm not using a current feedback loop this is attached to VSIN which puts the device in D-CAP mode (no current sense)

MODE: Discharge Mode setting pin (Connected to VLDIO). This discharges the VTT through the internal LDO. This is a moot point because there is no way to turn off the device because the S3, and S5 pins are permanently enabled