

R5 removed, and R6, C8 updated in Test to set Voltage control loop to ~90kHz BW

R40 optional for applications needing droop

Set R12 at 34.8k for 0.9Vout and at 46.4k for 0.85Vout

Targeting 300kHz per phase

for 1.0Vout R12=23.2k

for 1.2Vout R12=14k

Vramp of TPS40140 is 500mV nominal

Vref for TPS40140 = 700mV +/- 0.5%

COMP = 1.8V + Vramp*(Vout/Vin) + Rchoke*(Iout/phases)* Current gain in TPS40140

Based on PCB PM5423 rev A by R Taylor

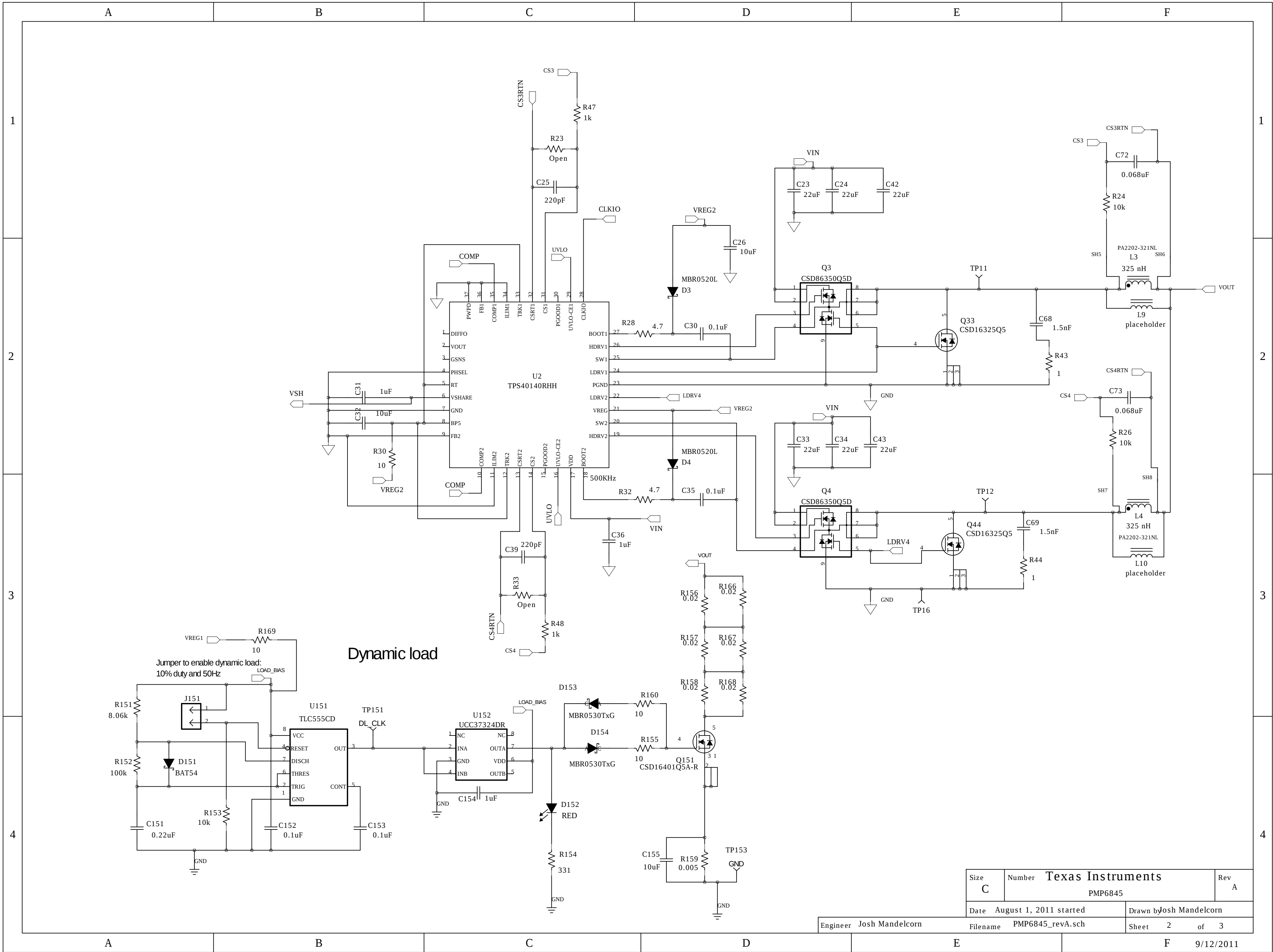
Tested; See Test & Thermal Reports

Texas Instruments

Title			TPS40140 & CSDxxx6 Phases 120A out		
Size	C	Number	PMP6845	Rev	A
Date	August 1, 2011 started		Drawn by	Josh Mandelcorn	
Filename	PMP6845_revA.sch		Sheet	1	of 3

Engineer Josh Mandelcorn

9/12/2011



Size C	Number PMP6845	Texas Instruments		Rev A
Date	August 1, 2011 started	Drawn by		Josh Mandelcorn
Engineer	Josh Mandelcorn	Filename	PMP6845_revA.sch	Sheet 2 of 3

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