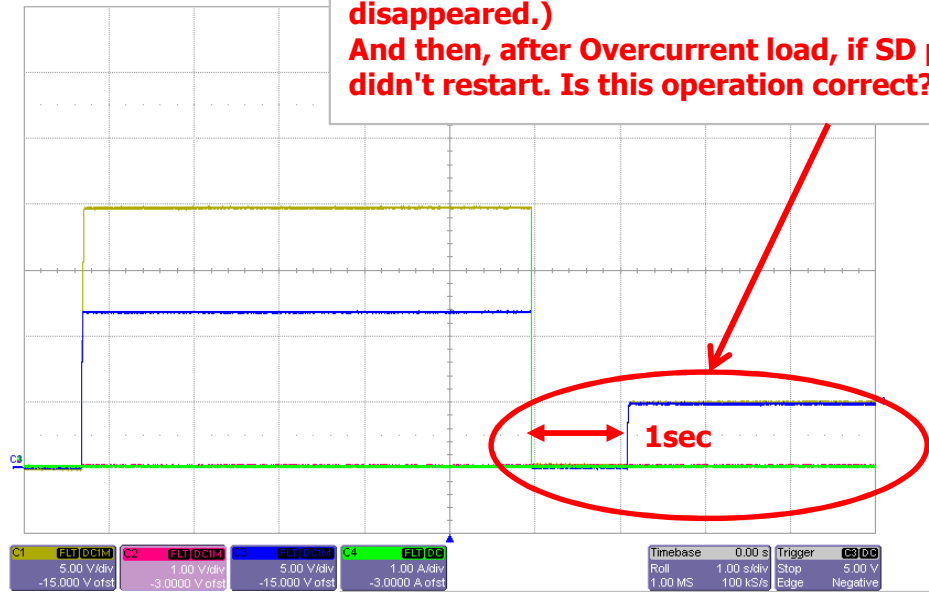


C1: Gate-SG
C2: CT-SG
C3: S-SG
C4: Load current

<Q1>
After Overcurrent load, the block diagram on the datasheet shows "output Latch-off".
However, after 1sec, the output restarted.
(this power was a little, if it was connected to the load, it disappeared.)
And then, after Overcurrent load, if SD pin set to low, the output didn't restart. Is this operation correct?



<Q2>
If SD pin set to low, is "Output Latch-off" released?
Furthermore, LR pin is added 6V.