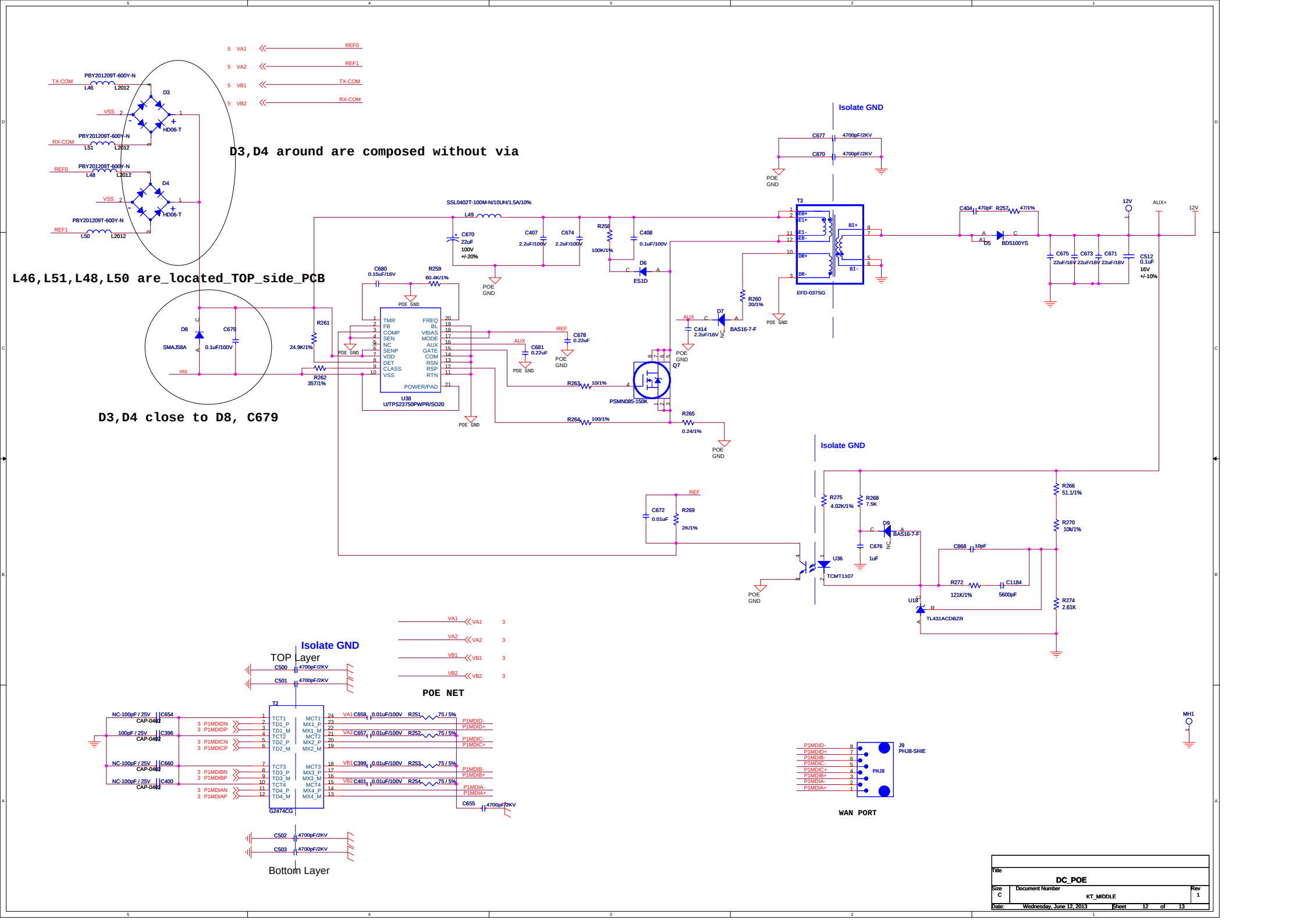
D3,D4 around are composed without via
L46,L51,L48,L50 are_located_TOP_side_PCB
D3,D4 close to D8, C679
Isolate GND
POE NET
WAN PORT
TOP Layer
Bottom Layer
DC_POE
KT_MIDDLE
Wednesday, June 12, 2013