

The available evaluation boards come pre-populated with the load share controllers, VDD decoupling capacitors, and components for external controller shut down. This circuitry is shown in Figure 1 as U1, C5, Q1, R1, and R4 for the first module, U2, C7, Q2, R2, R5 for the second, and U3, C9, Q3, R3, and R6 for the third. By applying a 2-V signal onto the SD terminal of any of the three modules, the 2N7002 transistor is turned on, shorting CS+ to ground, resulting in that module's disconnect feature to be enabled. With the disconnect feature enabled, the UCC39002 disconnects itself from the load share bus and its adjust current is zero.

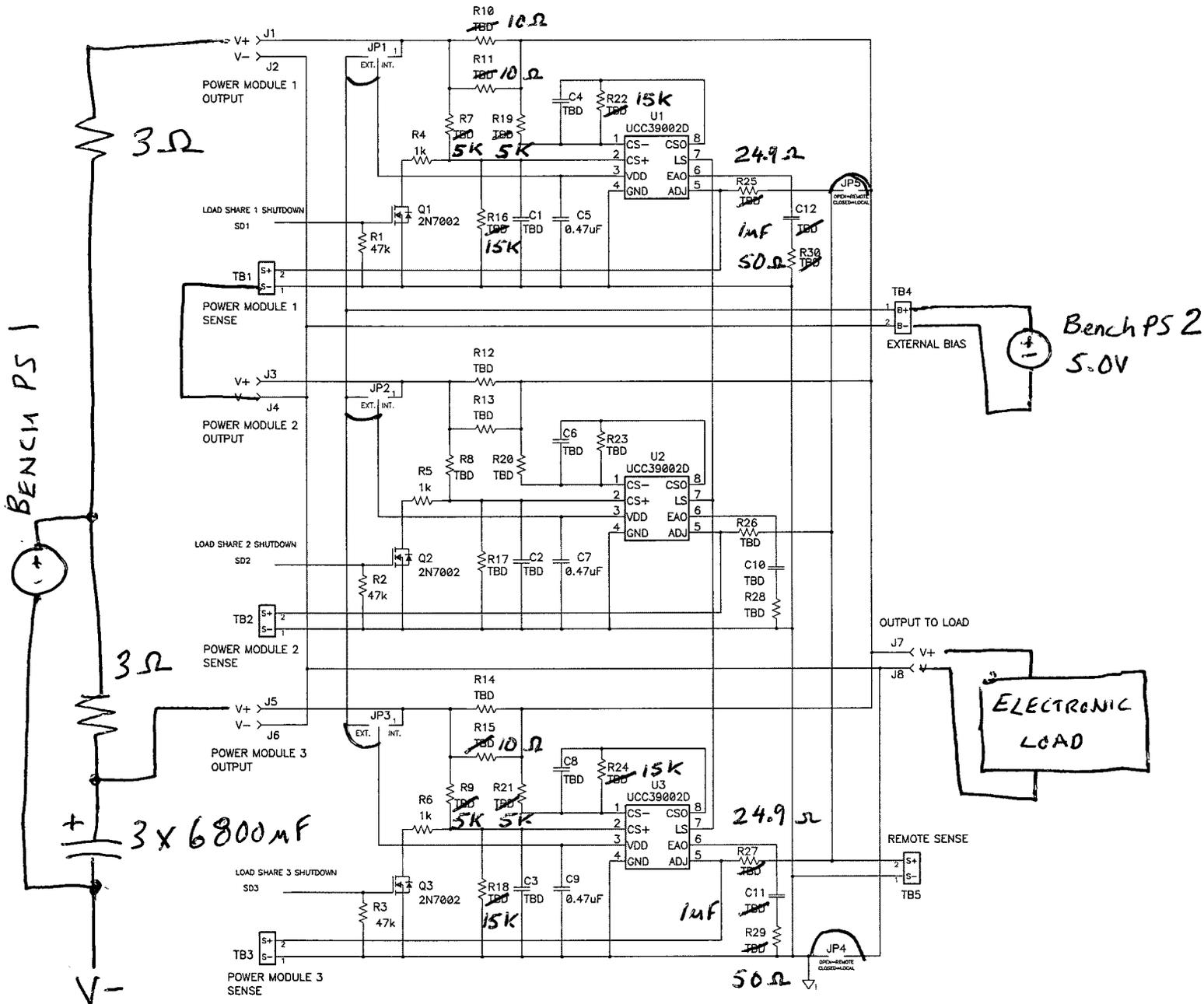


Figure 1. UCC39002 Load-Share Schematic