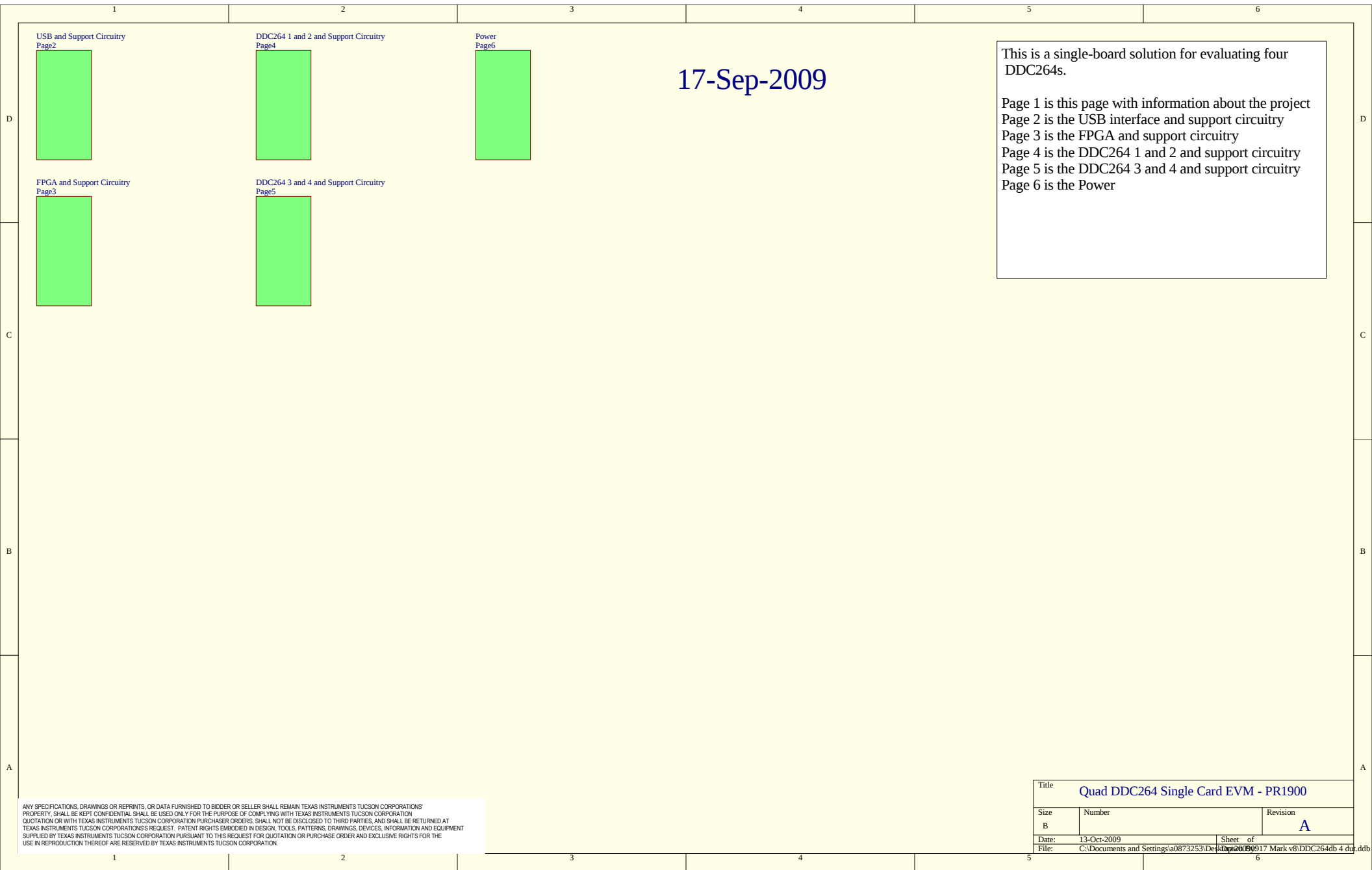




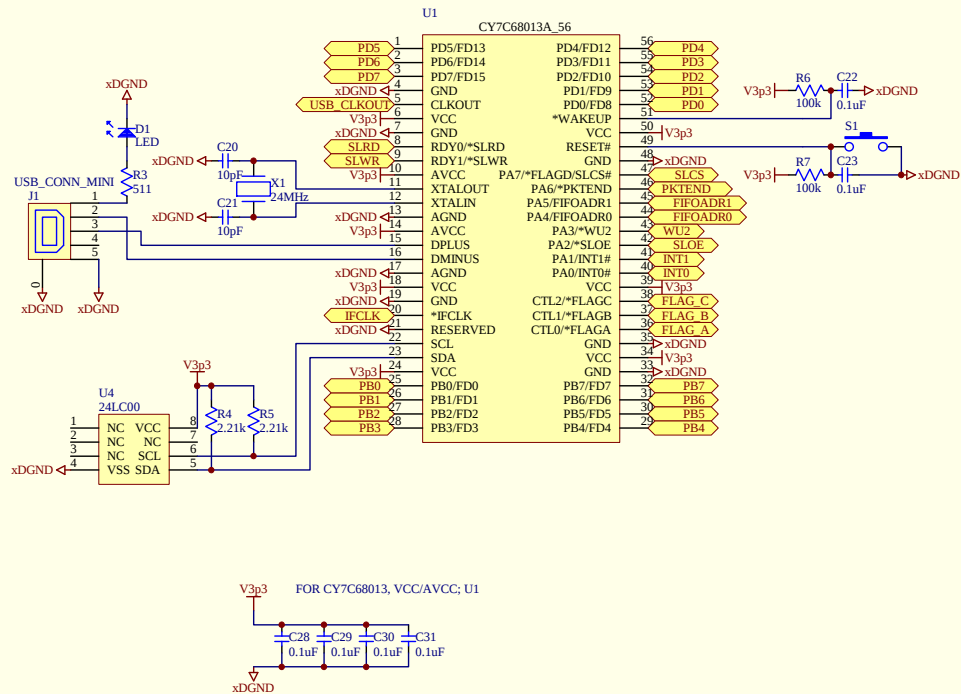
This is a single-board solution for evaluating four DDC264s.

Page 1 is this page with information about the project
Page 2 is the USB interface and support circuitry
Page 3 is the FPGA and support circuitry
Page 4 is the DDC264 1 and 2 and support circuitry
Page 5 is the DDC264 3 and 4 and support circuitry
Page 6 is the Power

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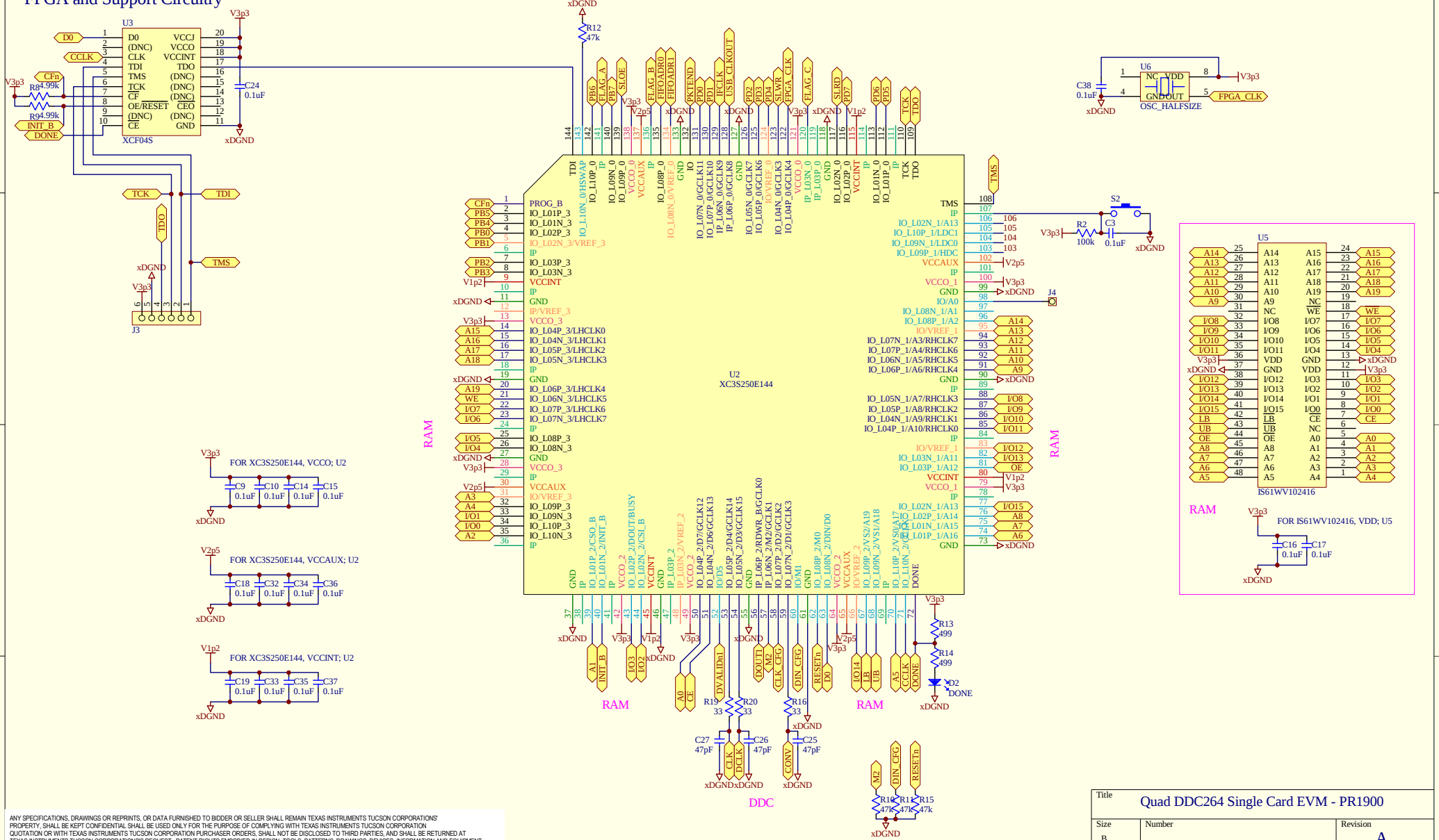
USB and Support Circuitry



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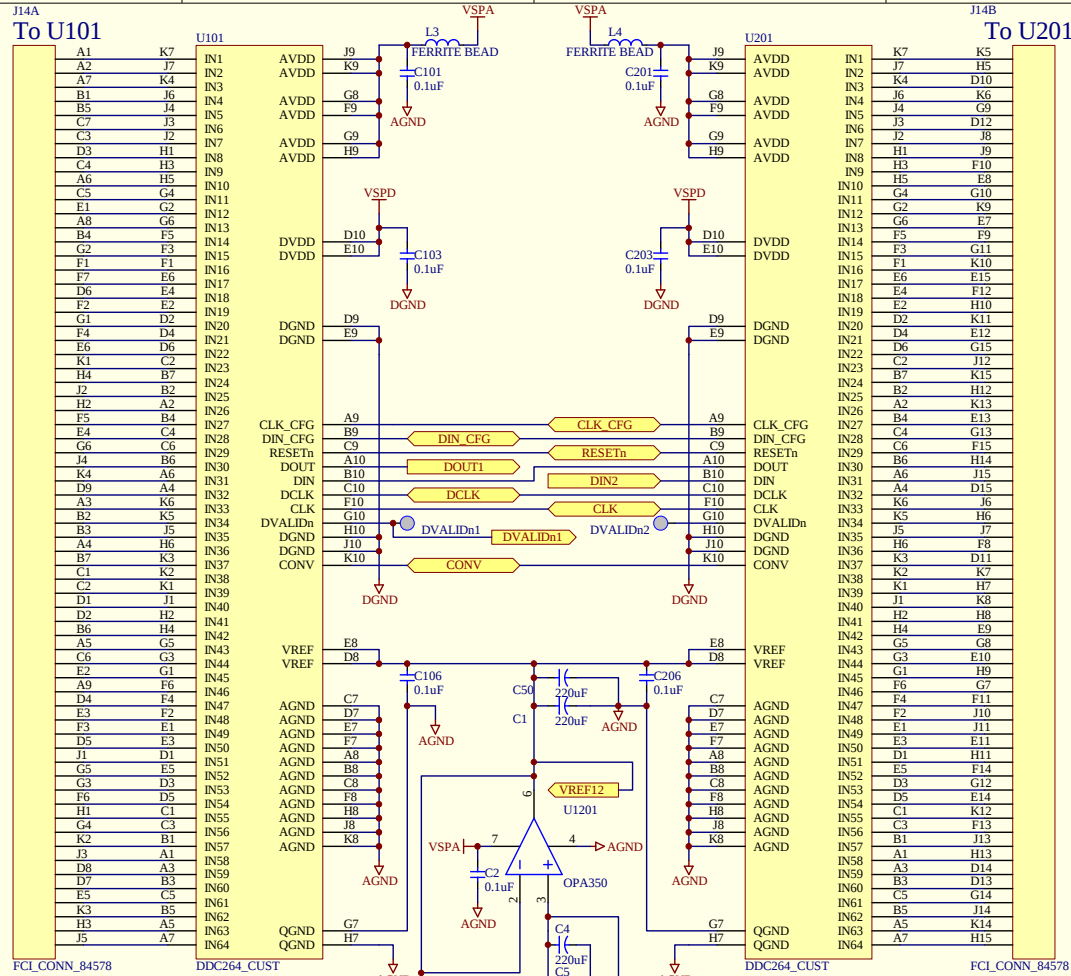
U3



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DDC264 1 and 2 and Support Circuitry



NOTES:

- * C50 IS OPTIONAL.
- * POPULATE EITHER U1201 OR U8.
- * POPULATE EITHER U1202 OR U9.

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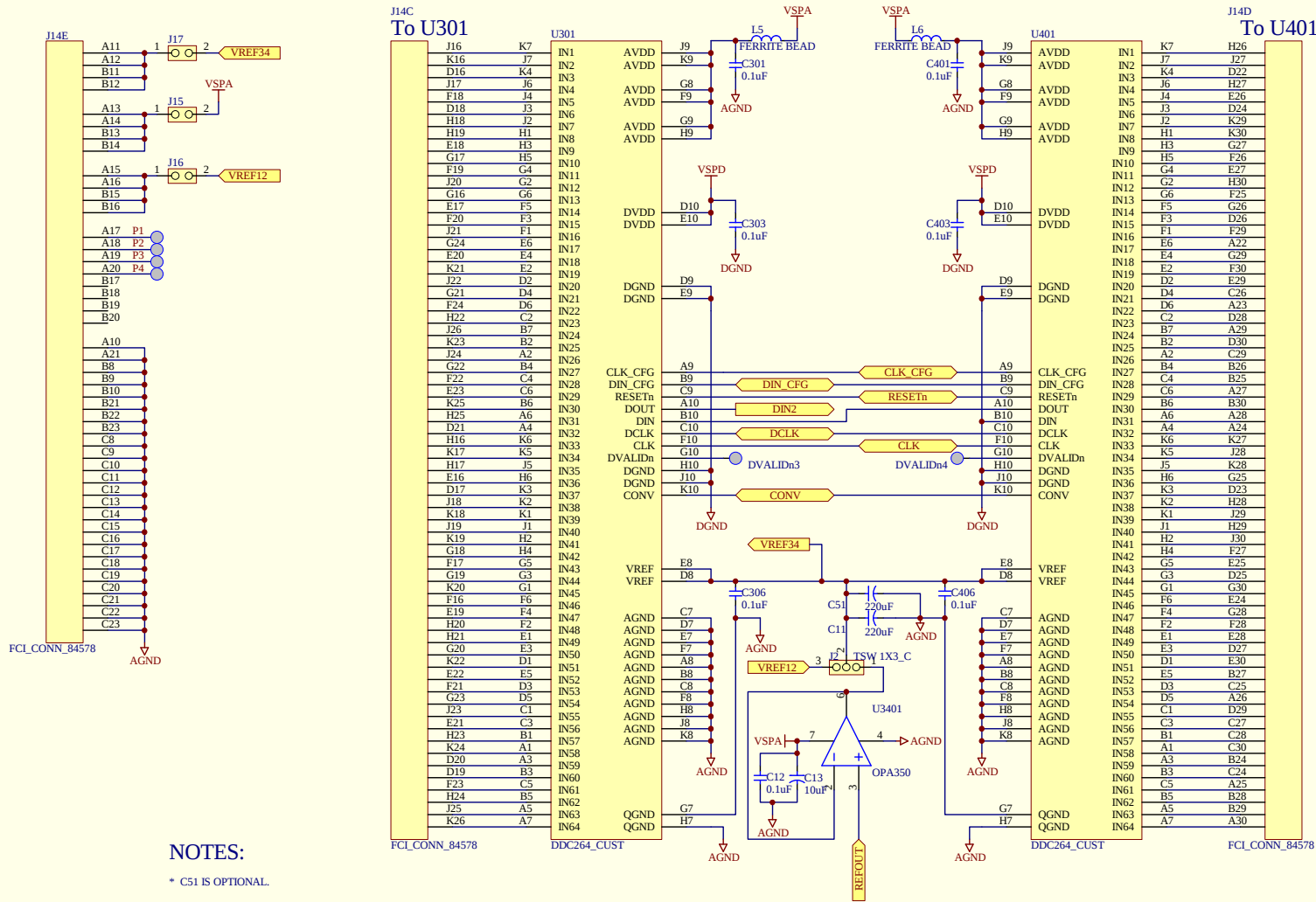
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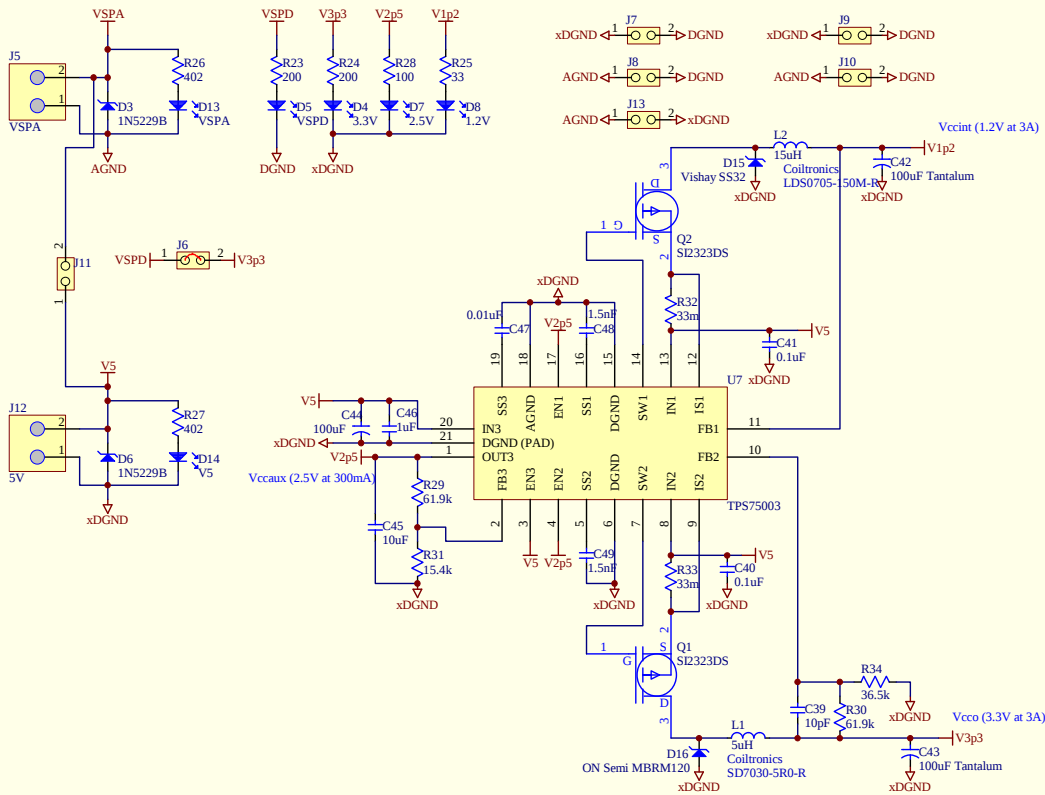
DDC264 3 and 4 and Support Circuitry



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Power



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