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 TMS320C2000 Linker PC v6.1.0

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>> Linked Wed Aug 07 15:02:22 2013

OUTPUT FILE NAME: <Piccolo\_CLA\_TemplateProject.out>

ENTRY POINT SYMBOL: "\_c\_int00" address: 003f7b47

MEMORY CONFIGURATION

 name origin length used unused attr fill

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PAGE 0:

 RAML0 00008000 00000800 0000001f 000007e1 RWIX

 RAML3 00009000 00001000 00000a84 0000057c RWIX

 OTP 003d7800 00000400 00000000 00000400 RWIX

 FLASHH 003e8000 00002000 00000000 00002000 RWIX

 FLASHG 003ea000 00002000 00000000 00002000 RWIX

 FLASHF 003ec000 00002000 00000000 00002000 RWIX

 FLASHE 003ee000 00002000 00000000 00002000 RWIX

 FLASHD 003f0000 00002000 0000001f 00001fe1 RWIX

 FLASHC 003f2000 00002000 00000a84 0000157c RWIX

 FLASHA 003f6000 00001f80 00001f71 0000000f RWIX

 CSM\_RSVD 003f7f80 00000076 00000000 00000076 RWIX

 BEGIN 003f7ff6 00000002 00000002 00000000 RWIX

 CSM\_PWL 003f7ff8 00000008 00000000 00000008 RWIX

 IQTABLES 003fe000 00000b50 00000000 00000b50 RWIX

 IQTABLES2 003feb50 0000008c 00000000 0000008c RWIX

 IQTABLES3 003febdc 000000aa 00000000 000000aa RWIX

 BOOTROM 003ff27c 00000d44 00000000 00000d44 RWIX

 RESET 003fffc0 00000002 00000000 00000002 RWIX

 VECTORS 003fffc2 0000003e 00000000 0000003e RWIX

PAGE 1:

 BOOT\_RSVD 00000000 00000050 00000000 00000050 RWIX

 RAMM0 00000050 000003b0 00000300 000000b0 RWIX

 RAMM1 00000400 00000400 000003d0 00000030 RWIX

 DEV\_EMU 00000880 00000105 00000004 00000101 RWIX

 SYS\_PWR\_CTL 00000985 00000003 00000003 00000000 RWIX

 FLASH\_REGS 00000a80 00000060 00000008 00000058 RWIX

 CSM 00000ae0 00000010 00000010 00000000 RWIX

 ADC\_RESULT 00000b00 00000020 00000020 00000000 RWIX

 CPU\_TIMER0 00000c00 00000008 00000008 00000000 RWIX

 CPU\_TIMER1 00000c08 00000008 00000008 00000000 RWIX

 CPU\_TIMER2 00000c10 00000008 00000008 00000000 RWIX

 PIE\_CTRL 00000ce0 00000020 0000001a 00000006 RWIX

 PIE\_VECT 00000d00 00000100 00000100 00000000 RWIX

 CLA1 00001400 00000080 00000040 00000040 RWIX

 CLA\_CPU\_MSGRAM 00001480 00000080 00000010 00000070 RWIX

 CPU\_CLA\_MSGRAM 00001500 00000080 00000012 0000006e RWIX

 ECANA 00006000 00000040 00000034 0000000c RWIX

 ECANA\_LAM 00006040 00000040 00000040 00000000 RWIX

 ECANA\_MOTS 00006080 00000040 00000040 00000000 RWIX

 ECANA\_MOTO 000060c0 00000040 00000040 00000000 RWIX

 ECANA\_MBOX 00006100 00000100 00000100 00000000 RWIX

 COMP1 00006400 00000020 00000014 0000000c RWIX

 COMP2 00006420 00000020 00000014 0000000c RWIX

 COMP3 00006440 00000020 00000014 0000000c RWIX

 EPWM1 00006800 00000040 00000040 00000000 RWIX

 EPWM2 00006840 00000040 00000040 00000000 RWIX

 EPWM3 00006880 00000040 00000040 00000000 RWIX

 EPWM4 000068c0 00000040 00000040 00000000 RWIX

 EPWM5 00006900 00000040 00000040 00000000 RWIX

 EPWM6 00006940 00000040 00000040 00000000 RWIX

 EPWM7 00006980 00000040 00000040 00000000 RWIX

 ECAP1 00006a00 00000020 00000020 00000000 RWIX

 HRCAP1 00006ac0 00000020 00000020 00000000 RWIX

 HRCAP2 00006ae0 00000020 00000020 00000000 RWIX

 EQEP1 00006b00 00000040 00000040 00000000 RWIX

 LINA 00006c00 00000080 0000004a 00000036 RWIX

 GPIOCTRL 00006f80 00000040 00000040 00000000 RWIX

 GPIODAT 00006fc0 00000020 00000020 00000000 RWIX

 GPIOINT 00006fe0 00000020 0000000c 00000014 RWIX

 SYSTEM 00007010 00000020 00000020 00000000 RWIX

 SPIA 00007040 00000010 00000010 00000000 RWIX

 SCIA 00007050 00000010 00000010 00000000 RWIX

 NMIINTRUPT 00007060 00000010 00000010 00000000 RWIX

 XINTRUPT 00007070 00000010 00000010 00000000 RWIX

 ADC 00007100 00000080 00000050 00000030 RWIX

 SPIB 00007740 00000010 00000010 00000000 RWIX

 I2CA 00007900 00000040 00000022 0000001e RWIX

 CLARAM0 00008800 00000400 000001e0 00000220 RWIX

 CLARAM1 00008c00 00000400 00000000 00000400 RWIX

 PARTID 003d7e80 00000001 00000001 00000000 RWIX

 FLASHB 003f4000 00002000 00000000 00002000 RWIX

 CSM\_PWL 003f7ff8 00000008 00000008 00000000 RWIX

SECTION ALLOCATION MAP

 output attributes/

section page origin length input sections

-------- ---- ---------- ---------- ----------------

.pinit 0 003f6000 00000000 UNINITIALIZED

.switch 0 00000000 00000080 FAILED TO ALLOCATE

ramfuncs 0 003f0000 0000001f RUN ADDR = 00008000

 003f0000 0000001b HVACI\_Sensorless\_CLA-DevInit\_F2803x.obj (ramfuncs)

 003f001b 00000004 DSP2803x\_usDelay.obj (ramfuncs)

Cla1Prog 0 003f2000 00000a84 RUN ADDR = 00009000

 003f2000 00000a84 HVACI\_Sensorless\_CLA-Tasks\_C.obj (Cla1Prog:retain)

.text 0 003f6000 00001c6f

 003f6000 000007c2 dp\_if.obj (.text)

 003f67c2 00000629 HVACI\_Sensorless\_CLA-Main.obj (.text)

 003f6deb 000003f3 Modbus\_RTU.obj (.text)

 003f71de 0000022b HVACI\_Sensorless\_CLA-DevInit\_F2803x.obj (.text)

 003f7409 000001bf ADC\_SOC\_Cnf.obj (.text)

 003f75c8 0000019a ProfibusMain.obj (.text)

 003f7762 00000133 dp\_isr.obj (.text)

 003f7895 00000101 DpAppl.obj (.text)

 003f7996 000000ca PWM\_1ch\_Cnf.obj (.text)

 003f7a60 000000a2 DpDiag.obj (.text)

 003f7b02 00000045 DpPrm.obj (.text)

 003f7b47 00000044 rts2800\_ml.lib : boot.obj (.text)

 003f7b8b 0000003c DpCfg.obj (.text)

 003f7bc7 00000025 rts2800\_ml.lib : fs\_toi.obj (.text)

 003f7bec 00000021 : memcpy\_ff.obj (.text)

 003f7c0d 0000001a : i\_tofs.obj (.text)

 003f7c27 00000019 : args\_main.obj (.text)

 003f7c40 00000019 : exit.obj (.text)

 003f7c59 00000009 : \_lock.obj (.text)

 003f7c62 00000008 DSP2803x\_CodeStartBranch.obj (.text)

 003f7c6a 00000005 HVACI\_Sensorless\_CLA-DevInit\_F2803x.obj (.text:retain)

.econst 0 003f7c6f 00000201

 003f7c6f 00000100 Modbus\_RTU.obj (.econst:\_auchCRCHi)

 003f7d6f 00000100 Modbus\_RTU.obj (.econst:\_auchCRCLo)

 003f7e6f 00000001 DpCfg.obj (.econst:\_DpApplDefCfg)

.cinit 0 003f7e70 00000101

 003f7e70 000000e3 HVACI\_Sensorless\_CLA-Main.obj (.cinit)

 003f7f53 0000000a rts2800\_ml.lib : \_lock.obj (.cinit)

 003f7f5d 0000000a : exit.obj (.cinit)

 003f7f67 00000008 Modbus\_RTU.obj (.cinit)

 003f7f6f 00000002 --HOLE-- [fill = 0]

codestart

\* 0 003f7ff6 00000002

 003f7ff6 00000002 DSP2803x\_CodeStartBranch.obj (codestart)

.reset 0 003fffc0 00000002 DSECT

 003fffc0 00000002 rts2800\_ml.lib : boot.obj (.reset)

vectors 0 003fffc2 00000000 DSECT

.stack 1 00000050 00000300 UNINITIALIZED

 00000050 00000300 --HOLE--

.ebss 1 00000400 000003d0 UNINITIALIZED

 00000400 000001a4 Modbus\_RTU.obj (.ebss)

 000005a4 00000004 rts2800\_ml.lib : \_lock.obj (.ebss)

 000005a8 00000004 : exit.obj (.ebss)

 000005ac 00000002 ProfibusMain.obj (.ebss)

 000005ae 00000012 --HOLE--

 000005c0 00000140 HVACI\_Sensorless\_CLA-Main.obj (.ebss)

 00000700 000000d0 DpAppl.obj (.ebss)

DevEmuRegsFile

\* 1 00000880 00000004 UNINITIALIZED

 00000880 00000004 DSP2803x\_GlobalVariableDefs.obj (DevEmuRegsFile)

SysPwrCtrlRegsFile

\* 1 00000985 00000003 UNINITIALIZED

 00000985 00000003 DSP2803x\_GlobalVariableDefs.obj (SysPwrCtrlRegsFile)

FlashRegsFile

\* 1 00000a80 00000008 UNINITIALIZED

 00000a80 00000008 DSP2803x\_GlobalVariableDefs.obj (FlashRegsFile)

CsmRegsFile

\* 1 00000ae0 00000010 UNINITIALIZED

 00000ae0 00000010 DSP2803x\_GlobalVariableDefs.obj (CsmRegsFile)

AdcResultFile

\* 1 00000b00 00000020 UNINITIALIZED

 00000b00 00000020 DSP2803x\_GlobalVariableDefs.obj (AdcResultFile)

CpuTimer0RegsFile

\* 1 00000c00 00000008 UNINITIALIZED

 00000c00 00000008 DSP2803x\_GlobalVariableDefs.obj (CpuTimer0RegsFile)

CpuTimer1RegsFile

\* 1 00000c08 00000008 UNINITIALIZED

 00000c08 00000008 DSP2803x\_GlobalVariableDefs.obj (CpuTimer1RegsFile)

CpuTimer2RegsFile

\* 1 00000c10 00000008 UNINITIALIZED

 00000c10 00000008 DSP2803x\_GlobalVariableDefs.obj (CpuTimer2RegsFile)

PieCtrlRegsFile

\* 1 00000ce0 0000001a UNINITIALIZED

 00000ce0 0000001a DSP2803x\_GlobalVariableDefs.obj (PieCtrlRegsFile)

PieVectTableFile

\* 1 00000d00 00000100 UNINITIALIZED

 00000d00 00000100 DSP2803x\_GlobalVariableDefs.obj (PieVectTableFile)

EmuKeyVar

\* 1 00000d00 00000001 UNINITIALIZED

 00000d00 00000001 DSP2803x\_GlobalVariableDefs.obj (EmuKeyVar)

EmuBModeVar

\* 1 00000d01 00000001 UNINITIALIZED

 00000d01 00000001 DSP2803x\_GlobalVariableDefs.obj (EmuBModeVar)

FlashCallbackVar

\* 1 00000d02 00000002 UNINITIALIZED

 00000d02 00000002 DSP2803x\_GlobalVariableDefs.obj (FlashCallbackVar)

FlashScalingVar

\* 1 00000d04 00000002 UNINITIALIZED

 00000d04 00000002 DSP2803x\_GlobalVariableDefs.obj (FlashScalingVar)

Cla1RegsFile

\* 1 00001400 00000040 UNINITIALIZED

 00001400 00000040 DSP2803x\_GlobalVariableDefs.obj (Cla1RegsFile)

Cla1ToCpuMsgRAM

\* 1 00001480 00000010 UNINITIALIZED

 00001480 00000010 HVACI\_Sensorless\_CLA-Tasks\_C.obj (Cla1ToCpuMsgRAM)

CpuToCla1MsgRAM

\* 1 00001500 00000012 UNINITIALIZED

 00001500 00000012 HVACI\_Sensorless\_CLA-Tasks\_C.obj (CpuToCla1MsgRAM)

ECanaRegsFile

\* 1 00006000 00000034 UNINITIALIZED

 00006000 00000034 DSP2803x\_GlobalVariableDefs.obj (ECanaRegsFile)

ECanaLAMRegsFile

\* 1 00006040 00000040 UNINITIALIZED

 00006040 00000040 DSP2803x\_GlobalVariableDefs.obj (ECanaLAMRegsFile)

ECanaMOTSRegsFile

\* 1 00006080 00000040 UNINITIALIZED

 00006080 00000040 DSP2803x\_GlobalVariableDefs.obj (ECanaMOTSRegsFile)

ECanaMOTORegsFile

\* 1 000060c0 00000040 UNINITIALIZED

 000060c0 00000040 DSP2803x\_GlobalVariableDefs.obj (ECanaMOTORegsFile)

ECanaMboxesFile

\* 1 00006100 00000100 UNINITIALIZED

 00006100 00000100 DSP2803x\_GlobalVariableDefs.obj (ECanaMboxesFile)

Comp1RegsFile

\* 1 00006400 00000014 UNINITIALIZED

 00006400 00000014 DSP2803x\_GlobalVariableDefs.obj (Comp1RegsFile)

Comp2RegsFile

\* 1 00006420 00000014 UNINITIALIZED

 00006420 00000014 DSP2803x\_GlobalVariableDefs.obj (Comp2RegsFile)

Comp3RegsFile

\* 1 00006440 00000014 UNINITIALIZED

 00006440 00000014 DSP2803x\_GlobalVariableDefs.obj (Comp3RegsFile)

EPwm1RegsFile

\* 1 00006800 00000040 UNINITIALIZED

 00006800 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm1RegsFile)

EPwm2RegsFile

\* 1 00006840 00000040 UNINITIALIZED

 00006840 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm2RegsFile)

EPwm3RegsFile

\* 1 00006880 00000040 UNINITIALIZED

 00006880 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm3RegsFile)

EPwm4RegsFile

\* 1 000068c0 00000040 UNINITIALIZED

 000068c0 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm4RegsFile)

EPwm5RegsFile

\* 1 00006900 00000040 UNINITIALIZED

 00006900 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm5RegsFile)

EPwm6RegsFile

\* 1 00006940 00000040 UNINITIALIZED

 00006940 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm6RegsFile)

EPwm7RegsFile

\* 1 00006980 00000040 UNINITIALIZED

 00006980 00000040 DSP2803x\_GlobalVariableDefs.obj (EPwm7RegsFile)

ECap1RegsFile

\* 1 00006a00 00000020 UNINITIALIZED

 00006a00 00000020 DSP2803x\_GlobalVariableDefs.obj (ECap1RegsFile)

HRCap1RegsFile

\* 1 00006ac0 00000020 UNINITIALIZED

 00006ac0 00000020 DSP2803x\_GlobalVariableDefs.obj (HRCap1RegsFile)

HRCap2RegsFile

\* 1 00006ae0 00000020 UNINITIALIZED

 00006ae0 00000020 DSP2803x\_GlobalVariableDefs.obj (HRCap2RegsFile)

EQep1RegsFile

\* 1 00006b00 00000040 UNINITIALIZED

 00006b00 00000040 DSP2803x\_GlobalVariableDefs.obj (EQep1RegsFile)

LinaRegsFile

\* 1 00006c00 0000004a UNINITIALIZED

 00006c00 0000004a DSP2803x\_GlobalVariableDefs.obj (LinaRegsFile)

GpioCtrlRegsFile

\* 1 00006f80 00000040 UNINITIALIZED

 00006f80 00000040 DSP2803x\_GlobalVariableDefs.obj (GpioCtrlRegsFile)

GpioDataRegsFile

\* 1 00006fc0 00000020 UNINITIALIZED

 00006fc0 00000020 DSP2803x\_GlobalVariableDefs.obj (GpioDataRegsFile)

GpioIntRegsFile

\* 1 00006fe0 0000000c UNINITIALIZED

 00006fe0 0000000c DSP2803x\_GlobalVariableDefs.obj (GpioIntRegsFile)

SysCtrlRegsFile

\* 1 00007010 00000020 UNINITIALIZED

 00007010 00000020 DSP2803x\_GlobalVariableDefs.obj (SysCtrlRegsFile)

SpiaRegsFile

\* 1 00007040 00000010 UNINITIALIZED

 00007040 00000010 DSP2803x\_GlobalVariableDefs.obj (SpiaRegsFile)

SciaRegsFile

\* 1 00007050 00000010 UNINITIALIZED

 00007050 00000010 DSP2803x\_GlobalVariableDefs.obj (SciaRegsFile)

NmiIntruptRegsFile

\* 1 00007060 00000010 UNINITIALIZED

 00007060 00000010 DSP2803x\_GlobalVariableDefs.obj (NmiIntruptRegsFile)

XIntruptRegsFile

\* 1 00007070 00000010 UNINITIALIZED

 00007070 00000010 DSP2803x\_GlobalVariableDefs.obj (XIntruptRegsFile)

AdcRegsFile

\* 1 00007100 00000050 UNINITIALIZED

 00007100 00000050 DSP2803x\_GlobalVariableDefs.obj (AdcRegsFile)

SpibRegsFile

\* 1 00007740 00000010 UNINITIALIZED

 00007740 00000010 DSP2803x\_GlobalVariableDefs.obj (SpibRegsFile)

I2caRegsFile

\* 1 00007900 00000022 UNINITIALIZED

 00007900 00000022 DSP2803x\_GlobalVariableDefs.obj (I2caRegsFile)

CLAscratch

\* 1 00008800 00000100 UNINITIALIZED

 00008800 00000100 --HOLE--

ClaDataRam0

\* 1 00008900 000000e0 UNINITIALIZED

 00008900 000000e0 HVACI\_Sensorless\_CLA-Tasks\_C.obj (ClaDataRam0)

PartIdRegsFile

\* 1 003d7e80 00000001 UNINITIALIZED

 003d7e80 00000001 DSP2803x\_GlobalVariableDefs.obj (PartIdRegsFile)

CsmPwlFile

\* 1 003f7ff8 00000008 UNINITIALIZED

 003f7ff8 00000008 DSP2803x\_GlobalVariableDefs.obj (CsmPwlFile)

GLOBAL SYMBOLS: SORTED ALPHABETICALLY BY Name

address name

-------- ----

003f6000 .text

003f7c40 C$$EXIT

003f7bc7 FS$$TOI

003f7c0d I$$TOFS

003f69ef \_A0

003f6a38 \_A1

003f6a62 \_A2

003f6a68 \_A3

003f6a70 \_A4

00000608 \_ACQPS

003f6b53 \_ADCOffset

003f7409 \_ADC\_SOC\_CNF

000005ee \_A\_Task\_Ptr

00007100 \_AdcRegs

00000b00 \_AdcResult

003f6fe7 \_AddrCheck

000005ea \_Alpha\_State\_Ptr

003f6a0c \_B0

003f6a76 \_B1

003f6b02 \_B2

003f6b08 \_B3

000005ec \_B\_Task\_Ptr

003f6a22 \_C0

003f6b35 \_C1

003f6b41 \_C2

003f6b47 \_C3

003f6b4d \_C4

003f732a \_CLA\_C\_Init

0000040c \_CRCCheck

000005f0 \_C\_Task\_Ptr

00000640 \_ChSel

00009000 \_Cla1Prog\_Start

00001400 \_Cla1Regs

00009000 \_Cla1Task1

0000981a \_Cla1Task2

00009822 \_Cla1Task3

0000982a \_Cla1Task4

00009832 \_Cla1Task5

0000983a \_Cla1Task6

00009842 \_Cla1Task7

0000984a \_Cla1Task8

003f2a84 \_Cla1funcsLoadEnd

003f2000 \_Cla1funcsLoadStart

00009000 \_Cla1funcsRunStart

000005d4 \_CommsOKflg

00000600 \_Comp

00006400 \_Comp1Regs

00006420 \_Comp2Regs

00006440 \_Comp3Regs

003f7718 \_CopyFromVpc3

003f76d6 \_CopyToVpc3

00000c00 \_CpuTimer0Regs

00000c08 \_CpuTimer1Regs

00000c10 \_CpuTimer2Regs

003f6ef4 \_Crc16

003f7ff8 \_CsmPwl

00000ae0 \_CsmRegs

0000150c \_CurrentFrqRef

00001504 \_CurrentStpRef

003f67c2 \_DLOG\_BuffInit

00001501 \_DMC\_CLA\_BUILDLEVEL

0000801b \_DSP28x\_usDelay

00000880 \_DevEmuRegs

003f71de \_DeviceInit

003f7e6f \_DpApplDefCfg

003f75ee \_DpAppl\_ClrResetVPC3Channel1

UNDEFED \_DpAppl\_EnableInterruptVPC3Channel1

003f798a \_DpAppl\_IsrBaudrateDetect

003f7985 \_DpAppl\_IsrDxOut

003f7977 \_DpAppl\_IsrGoLeaveDataExchange

003f798b \_DpAppl\_IsrNewGlobalControlCommand

003f798f \_DpAppl\_IsrNewSetSlaveAddress

003f7989 \_DpAppl\_IsrNewWdDpTimeout

003f7995 \_DpAppl\_IsrTimerClock

003f78d7 \_DpAppl\_ProfibusInit

003f7923 \_DpAppl\_ProfibusMain

003f75e7 \_DpAppl\_SetResetVPC3Channel1

003f7b97 \_DpCfg\_ChkNewCfgData

003f7b8b \_DpCfg\_Init

003f7a62 \_DpDiag\_Alarm

003f7a60 \_DpDiag\_AlarmInit

003f7a61 \_DpDiag\_Init

003f7af5 \_DpDiag\_IsrDiagBufferChanged

003f7aa7 \_DpDiag\_PrmOk

003f7ae3 \_DpDiag\_ResetDiagnosticBuffer

003f7ad7 \_DpDiag\_ResetStatDiag

003f7abf \_DpDiag\_SetCfgOk

003f7b1b \_DpPrm\_ChkNewPrmData

003f7b02 \_DpPrm\_Init

00006040 \_ECanaLAMRegs

000060c0 \_ECanaMOTORegs

00006080 \_ECanaMOTSRegs

00006100 \_ECanaMboxes

00006000 \_ECanaRegs

00006a00 \_ECap1Regs

00006800 \_EPwm1Regs

00006840 \_EPwm2Regs

00006880 \_EPwm3Regs

000068c0 \_EPwm4Regs

00006900 \_EPwm5Regs

00006940 \_EPwm6Regs

00006980 \_EPwm7Regs

00006b00 \_EQep1Regs

00000d01 \_EmuBMode

00000d00 \_EmuKey

00001500 \_EnableFlag

000005d1 \_FSuperFlag

003f7972 \_FatalError

00000a80 \_FlashRegs

00000d04 \_Flash\_CPUScaleFactor

00000d02 \_Flash\_CallbackPtr

0000150a \_FrqSyncRef

003f6e92 \_GetCmdByte

003f6000 \_GetFirmwareVersion

003f70e8 \_GetRegisterVal

00006f80 \_GpioCtrlRegs

00006fc0 \_GpioDataRegs

00006fe0 \_GpioIntRegs

000005e2 \_Gui\_CurrentFrqRef

000005e1 \_Gui\_CurrentStpRef

000005e7 \_Gui\_DMC\_CLA\_BUILDLEVEL

000005d8 \_Gui\_Dgain\_I

000005dc \_Gui\_Dgain\_P

000005d5 \_Gui\_Dgain\_V

000005c7 \_Gui\_FaultMode

000005e3 \_Gui\_FrqSyncRef

000005c3 \_Gui\_GFIn

000005c4 \_Gui\_GFIp

000005c1 \_Gui\_Idc

000005da \_Gui\_Igain\_I

000005e5 \_Gui\_Igain\_P

000005d7 \_Gui\_Igain\_V

000005c2 \_Gui\_Iout

000005e4 \_Gui\_Pgain\_I

000005d6 \_Gui\_Pgain\_P

000005d9 \_Gui\_Pgain\_V

000005c8 \_Gui\_Pin

000005c9 \_Gui\_Pout

000005e9 \_Gui\_PowerFrqRef

000005df \_Gui\_PowerStpRef

000005c5 \_Gui\_SetPntRef

000005c0 \_Gui\_Vdc

000005de \_Gui\_VoltageFrqRef

000005e0 \_Gui\_VoltageStpRef

000005d0 \_Gui\_Vout

000005c6 \_Gui\_wSysMode

000005e8 \_Gui\_wTurnOff

000005e6 \_Gui\_wTurnOn

00006ac0 \_HRCap1Regs

00006ae0 \_HRCap2Regs

00007900 \_I2caRegs

003f7c6a \_ISR\_ILLEGAL

00008000 \_InitFlash

000089de \_IsrTicker

000005cd \_LedBlinkCnt

00006c00 \_LinaRegs

003f73f4 \_MemCopy

003f700c \_ModbusExcep

00007060 \_NmiIntruptRegs

003f7380 \_PLLset

003f7996 \_PWM\_1ch\_CNF

003d7e80 \_PartIdRegs

003f73b6 \_PieCntlInit

00000ce0 \_PieCtrlRegs

00000d00 \_PieVectTable

003f73d5 \_PieVectTableInit

00001510 \_PowerFrqRef

000089dc \_PowerLoopCount

00001508 \_PowerStpRef

003f6f85 \_PresetMultipleRegisters

003f75e6 \_ProfibusDPV0

003f6ffc \_QuantityCheck

003f001f \_RamfuncsLoadEnd

003f0000 \_RamfuncsLoadStart

00008000 \_RamfuncsRunStart

0000040e \_RcvBytNum

003f6f1f \_ReadHoldRegisters

000004c0 \_ReceiveArray

003f6ee7 \_ResetSCI

00000403 \_ResolveDoneFlag

003f6e93 \_ResolveModbus

00000405 \_RxChar

003f6deb \_SCIA\_Init

000005d2 \_SPIResetTimer

00007050 \_SciaRegs

00000540 \_SendArray

00000400 \_SendDoneflag

00000406 \_SendOnFlag

000005d3 \_SerialCommsTimer

003f6e2b \_SerialHostCommsModbusRtu

00001502 \_SetPntRef

003f702a \_SetRegisterVal

00000408 \_SndBytNum

0000040a \_SndBytNumCnt

00007040 \_SpiaRegs

00007740 \_SpibRegs

UNDEFED \_Swap16

003f75dd \_SwapWord

00007010 \_SysCtrlRegs

00000985 \_SysPwrCtrlRegs

003f775b \_TestVpc3\_01

00000628 \_TrigSel

00000404 \_TxChar

003f6520 \_VPC3\_AssignAuxBuffer

003f618b \_VPC3\_CalculateInpOutpLength

003f6713 \_VPC3\_CheckDiagBufferChanged

003f6676 \_VPC3\_GetDiagBufPtr

003f64c5 \_VPC3\_GetDinBufPtr

003f6490 \_VPC3\_GetDoutBufPtr

003f665c \_VPC3\_GetErrorCounter

003f67b6 \_VPC3\_GetFreeMemory

003f6509 \_VPC3\_GetMasterAddress

003f622b \_VPC3\_InitBufferStructure

003f6080 \_VPC3\_Initialization

003f64f0 \_VPC3\_InputDataUpdate

003f602e \_VPC3\_MemoryTest

003f7762 \_VPC3\_Poll

003f67c1 \_VPC3\_ProcessDpv1StateMachine

003f67ae \_VPC3\_SetAlarm

003f6101 \_VPC3\_SetConstants

003f672d \_VPC3\_SetDiagnosis

003f6645 \_VPC3\_SetIoDataLength

003f60e2 \_VPC3\_Start

003f60f1 \_VPC3\_Stop

003f6698 \_VPC3\_UpdateDiagnosis

000005f6 \_VTimer0

000005f2 \_VTimer1

000005fa \_VTimer2

0000150e \_VoltageFrqRef

000089da \_VoltageLoopCount

00001506 \_VoltageStpRef

00000700 \_Vpc3AsicAddress

003f76b3 \_Vpc3MemCmp

003f7674 \_Vpc3MemSet

003f7642 \_Vpc3Read

003f75f5 \_Vpc3Wait\_1ms

003f760c \_Vpc3Write

003f7378 \_WDogDisable

00007070 \_XIntruptRegs

00000350 \_\_STACK\_END

00000300 \_\_STACK\_SIZE

00000001 \_\_TI\_args\_main

ffffffff \_\_\_binit\_\_

ffffffff \_\_\_c\_args\_\_

003f7e70 \_\_\_cinit\_\_

003f7c6f \_\_\_etext\_\_

003f7bec \_\_\_memcpy\_ff

ffffffff \_\_\_pinit\_\_

003f6000 \_\_\_text\_\_

003f7c27 \_\_args\_main

00008900 \_\_cla\_scratchpad\_end

00008800 \_\_cla\_scratchpad\_start

000005a8 \_\_cleanup\_ptr

000005aa \_\_dtors\_ptr

000005a6 \_\_lock

003f7c61 \_\_nop

003f7c5d \_\_register\_lock

003f7c59 \_\_register\_unlock

00000050 \_\_stack

000005a4 \_\_unlock

003f7c40 \_abort

000006a0 \_arrayGetList

003f7c6f \_auchCRCHi

003f7d6f \_auchCRCLo

000005ca \_bValue

003f7b47 \_c\_int00

000005cb \_cnt2

000005ce \_convstatus

000005ac \_dummy

00000618 \_ePWM

003f7c42 \_exit

00000402 \_flagDoneGetChar

00000401 \_flagStartGetChar

0000891a \_ipark1

00000407 \_k

00001480 \_lsw

003f67d4 \_main

00000440 \_nData4Test

00000702 \_pDpSystem

00000706 \_pVpc3

00000704 \_pVpc3Channel1

0000890c \_park1

00008932 \_pi\_id

0000894e \_pi\_iq

000089be \_pi\_power\_frq

000089a2 \_pi\_power\_stp

00008986 \_pi\_voltage\_frq

0000896a \_pi\_voltage\_stp

000005cc \_ptest

00008928 \_pwm1

00008900 \_rg1

0000070c \_sDpAppl

00000740 \_sDpSystemChannel1

003f6d65 \_sFaultMode

003f6d76 \_sLedControl

003f6ccc \_sNormalInvMode

003f6b7f \_sPowerOnMode

003f6c2c \_sSoftStartMode

003f6bb4 \_sStandByMode

00000708 \_sVpc3Error

00000780 \_sVpc3OnlyForInit

003f75c8 \_spib\_init

000006e0 \_varGetList

00000680 \_varSetBtnList

000006c0 \_varSetSldrList

00000650 \_varSetTxtList

000005cf \_wSysMode

ffffffff binit

003f7e70 cinit

003f7ff6 code\_start

003f7c6f etext

ffffffff pinit

GLOBAL SYMBOLS: SORTED BY Symbol Address

address name

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00000001 \_\_TI\_args\_main

00000050 \_\_stack

00000300 \_\_STACK\_SIZE

00000350 \_\_STACK\_END

00000400 \_SendDoneflag

00000401 \_flagStartGetChar

00000402 \_flagDoneGetChar

00000403 \_ResolveDoneFlag

00000404 \_TxChar

00000405 \_RxChar

00000406 \_SendOnFlag

00000407 \_k

00000408 \_SndBytNum

0000040a \_SndBytNumCnt

0000040c \_CRCCheck

0000040e \_RcvBytNum

00000440 \_nData4Test

000004c0 \_ReceiveArray

00000540 \_SendArray

000005a4 \_\_unlock

000005a6 \_\_lock

000005a8 \_\_cleanup\_ptr

000005aa \_\_dtors\_ptr

000005ac \_dummy

000005c0 \_Gui\_Vdc

000005c1 \_Gui\_Idc

000005c2 \_Gui\_Iout

000005c3 \_Gui\_GFIn

000005c4 \_Gui\_GFIp

000005c5 \_Gui\_SetPntRef

000005c6 \_Gui\_wSysMode

000005c7 \_Gui\_FaultMode

000005c8 \_Gui\_Pin

000005c9 \_Gui\_Pout

000005ca \_bValue

000005cb \_cnt2

000005cc \_ptest

000005cd \_LedBlinkCnt

000005ce \_convstatus

000005cf \_wSysMode

000005d0 \_Gui\_Vout

000005d1 \_FSuperFlag

000005d2 \_SPIResetTimer

000005d3 \_SerialCommsTimer

000005d4 \_CommsOKflg

000005d5 \_Gui\_Dgain\_V

000005d6 \_Gui\_Pgain\_P

000005d7 \_Gui\_Igain\_V

000005d8 \_Gui\_Dgain\_I

000005d9 \_Gui\_Pgain\_V

000005da \_Gui\_Igain\_I

000005dc \_Gui\_Dgain\_P

000005de \_Gui\_VoltageFrqRef

000005df \_Gui\_PowerStpRef

000005e0 \_Gui\_VoltageStpRef

000005e1 \_Gui\_CurrentStpRef

000005e2 \_Gui\_CurrentFrqRef

000005e3 \_Gui\_FrqSyncRef

000005e4 \_Gui\_Pgain\_I

000005e5 \_Gui\_Igain\_P

000005e6 \_Gui\_wTurnOn

000005e7 \_Gui\_DMC\_CLA\_BUILDLEVEL

000005e8 \_Gui\_wTurnOff

000005e9 \_Gui\_PowerFrqRef

000005ea \_Alpha\_State\_Ptr

000005ec \_B\_Task\_Ptr

000005ee \_A\_Task\_Ptr

000005f0 \_C\_Task\_Ptr

000005f2 \_VTimer1

000005f6 \_VTimer0

000005fa \_VTimer2

00000600 \_Comp

00000608 \_ACQPS

00000618 \_ePWM

00000628 \_TrigSel

00000640 \_ChSel

00000650 \_varSetTxtList

00000680 \_varSetBtnList

000006a0 \_arrayGetList

000006c0 \_varSetSldrList

000006e0 \_varGetList

00000700 \_Vpc3AsicAddress

00000702 \_pDpSystem

00000704 \_pVpc3Channel1

00000706 \_pVpc3

00000708 \_sVpc3Error

0000070c \_sDpAppl

00000740 \_sDpSystemChannel1

00000780 \_sVpc3OnlyForInit

00000880 \_DevEmuRegs

00000985 \_SysPwrCtrlRegs

00000a80 \_FlashRegs

00000ae0 \_CsmRegs

00000b00 \_AdcResult

00000c00 \_CpuTimer0Regs

00000c08 \_CpuTimer1Regs

00000c10 \_CpuTimer2Regs

00000ce0 \_PieCtrlRegs

00000d00 \_EmuKey

00000d00 \_PieVectTable

00000d01 \_EmuBMode

00000d02 \_Flash\_CallbackPtr

00000d04 \_Flash\_CPUScaleFactor

00001400 \_Cla1Regs

00001480 \_lsw

00001500 \_EnableFlag

00001501 \_DMC\_CLA\_BUILDLEVEL

00001502 \_SetPntRef

00001504 \_CurrentStpRef

00001506 \_VoltageStpRef

00001508 \_PowerStpRef

0000150a \_FrqSyncRef

0000150c \_CurrentFrqRef

0000150e \_VoltageFrqRef

00001510 \_PowerFrqRef

00006000 \_ECanaRegs

00006040 \_ECanaLAMRegs

00006080 \_ECanaMOTSRegs

000060c0 \_ECanaMOTORegs

00006100 \_ECanaMboxes

00006400 \_Comp1Regs

00006420 \_Comp2Regs

00006440 \_Comp3Regs

00006800 \_EPwm1Regs

00006840 \_EPwm2Regs

00006880 \_EPwm3Regs

000068c0 \_EPwm4Regs

00006900 \_EPwm5Regs

00006940 \_EPwm6Regs

00006980 \_EPwm7Regs

00006a00 \_ECap1Regs

00006ac0 \_HRCap1Regs

00006ae0 \_HRCap2Regs

00006b00 \_EQep1Regs

00006c00 \_LinaRegs

00006f80 \_GpioCtrlRegs

00006fc0 \_GpioDataRegs

00006fe0 \_GpioIntRegs

00007010 \_SysCtrlRegs

00007040 \_SpiaRegs

00007050 \_SciaRegs

00007060 \_NmiIntruptRegs

00007070 \_XIntruptRegs

00007100 \_AdcRegs

00007740 \_SpibRegs

00007900 \_I2caRegs

00008000 \_InitFlash

00008000 \_RamfuncsRunStart

0000801b \_DSP28x\_usDelay

00008800 \_\_cla\_scratchpad\_start

00008900 \_\_cla\_scratchpad\_end

00008900 \_rg1

0000890c \_park1

0000891a \_ipark1

00008928 \_pwm1

00008932 \_pi\_id

0000894e \_pi\_iq

0000896a \_pi\_voltage\_stp

00008986 \_pi\_voltage\_frq

000089a2 \_pi\_power\_stp

000089be \_pi\_power\_frq

000089da \_VoltageLoopCount

000089dc \_PowerLoopCount

000089de \_IsrTicker

00009000 \_Cla1Prog\_Start

00009000 \_Cla1Task1

00009000 \_Cla1funcsRunStart

0000981a \_Cla1Task2

00009822 \_Cla1Task3

0000982a \_Cla1Task4

00009832 \_Cla1Task5

0000983a \_Cla1Task6

00009842 \_Cla1Task7

0000984a \_Cla1Task8

003d7e80 \_PartIdRegs

003f0000 \_RamfuncsLoadStart

003f001f \_RamfuncsLoadEnd

003f2000 \_Cla1funcsLoadStart

003f2a84 \_Cla1funcsLoadEnd

003f6000 .text

003f6000 \_GetFirmwareVersion

003f6000 \_\_\_text\_\_

003f602e \_VPC3\_MemoryTest

003f6080 \_VPC3\_Initialization

003f60e2 \_VPC3\_Start

003f60f1 \_VPC3\_Stop

003f6101 \_VPC3\_SetConstants

003f618b \_VPC3\_CalculateInpOutpLength

003f622b \_VPC3\_InitBufferStructure

003f6490 \_VPC3\_GetDoutBufPtr

003f64c5 \_VPC3\_GetDinBufPtr

003f64f0 \_VPC3\_InputDataUpdate

003f6509 \_VPC3\_GetMasterAddress

003f6520 \_VPC3\_AssignAuxBuffer

003f6645 \_VPC3\_SetIoDataLength

003f665c \_VPC3\_GetErrorCounter

003f6676 \_VPC3\_GetDiagBufPtr

003f6698 \_VPC3\_UpdateDiagnosis

003f6713 \_VPC3\_CheckDiagBufferChanged

003f672d \_VPC3\_SetDiagnosis

003f67ae \_VPC3\_SetAlarm

003f67b6 \_VPC3\_GetFreeMemory

003f67c1 \_VPC3\_ProcessDpv1StateMachine

003f67c2 \_DLOG\_BuffInit

003f67d4 \_main

003f69ef \_A0

003f6a0c \_B0

003f6a22 \_C0

003f6a38 \_A1

003f6a62 \_A2

003f6a68 \_A3

003f6a70 \_A4

003f6a76 \_B1

003f6b02 \_B2

003f6b08 \_B3

003f6b35 \_C1

003f6b41 \_C2

003f6b47 \_C3

003f6b4d \_C4

003f6b53 \_ADCOffset

003f6b7f \_sPowerOnMode

003f6bb4 \_sStandByMode

003f6c2c \_sSoftStartMode

003f6ccc \_sNormalInvMode

003f6d65 \_sFaultMode

003f6d76 \_sLedControl

003f6deb \_SCIA\_Init

003f6e2b \_SerialHostCommsModbusRtu

003f6e92 \_GetCmdByte

003f6e93 \_ResolveModbus

003f6ee7 \_ResetSCI

003f6ef4 \_Crc16

003f6f1f \_ReadHoldRegisters

003f6f85 \_PresetMultipleRegisters

003f6fe7 \_AddrCheck

003f6ffc \_QuantityCheck

003f700c \_ModbusExcep

003f702a \_SetRegisterVal

003f70e8 \_GetRegisterVal

003f71de \_DeviceInit

003f732a \_CLA\_C\_Init

003f7378 \_WDogDisable

003f7380 \_PLLset

003f73b6 \_PieCntlInit

003f73d5 \_PieVectTableInit

003f73f4 \_MemCopy

003f7409 \_ADC\_SOC\_CNF

003f75c8 \_spib\_init

003f75dd \_SwapWord

003f75e6 \_ProfibusDPV0

003f75e7 \_DpAppl\_SetResetVPC3Channel1

003f75ee \_DpAppl\_ClrResetVPC3Channel1

003f75f5 \_Vpc3Wait\_1ms

003f760c \_Vpc3Write

003f7642 \_Vpc3Read

003f7674 \_Vpc3MemSet

003f76b3 \_Vpc3MemCmp

003f76d6 \_CopyToVpc3

003f7718 \_CopyFromVpc3

003f775b \_TestVpc3\_01

003f7762 \_VPC3\_Poll

003f78d7 \_DpAppl\_ProfibusInit

003f7923 \_DpAppl\_ProfibusMain

003f7972 \_FatalError

003f7977 \_DpAppl\_IsrGoLeaveDataExchange

003f7985 \_DpAppl\_IsrDxOut

003f7989 \_DpAppl\_IsrNewWdDpTimeout

003f798a \_DpAppl\_IsrBaudrateDetect

003f798b \_DpAppl\_IsrNewGlobalControlCommand

003f798f \_DpAppl\_IsrNewSetSlaveAddress

003f7995 \_DpAppl\_IsrTimerClock

003f7996 \_PWM\_1ch\_CNF

003f7a60 \_DpDiag\_AlarmInit

003f7a61 \_DpDiag\_Init

003f7a62 \_DpDiag\_Alarm

003f7aa7 \_DpDiag\_PrmOk

003f7abf \_DpDiag\_SetCfgOk

003f7ad7 \_DpDiag\_ResetStatDiag

003f7ae3 \_DpDiag\_ResetDiagnosticBuffer

003f7af5 \_DpDiag\_IsrDiagBufferChanged

003f7b02 \_DpPrm\_Init

003f7b1b \_DpPrm\_ChkNewPrmData

003f7b47 \_c\_int00

003f7b8b \_DpCfg\_Init

003f7b97 \_DpCfg\_ChkNewCfgData

003f7bc7 FS$$TOI

003f7bec \_\_\_memcpy\_ff

003f7c0d I$$TOFS

003f7c27 \_\_args\_main

003f7c40 C$$EXIT

003f7c40 \_abort

003f7c42 \_exit

003f7c59 \_\_register\_unlock

003f7c5d \_\_register\_lock

003f7c61 \_\_nop

003f7c6a \_ISR\_ILLEGAL

003f7c6f \_\_\_etext\_\_

003f7c6f \_auchCRCHi

003f7c6f etext

003f7d6f \_auchCRCLo

003f7e6f \_DpApplDefCfg

003f7e70 \_\_\_cinit\_\_

003f7e70 cinit

003f7ff6 code\_start

003f7ff8 \_CsmPwl

ffffffff \_\_\_binit\_\_

ffffffff \_\_\_c\_args\_\_

ffffffff \_\_\_pinit\_\_

ffffffff binit

ffffffff pinit

UNDEFED \_DpAppl\_EnableInterruptVPC3Channel1

UNDEFED \_Swap16

[312 symbols]