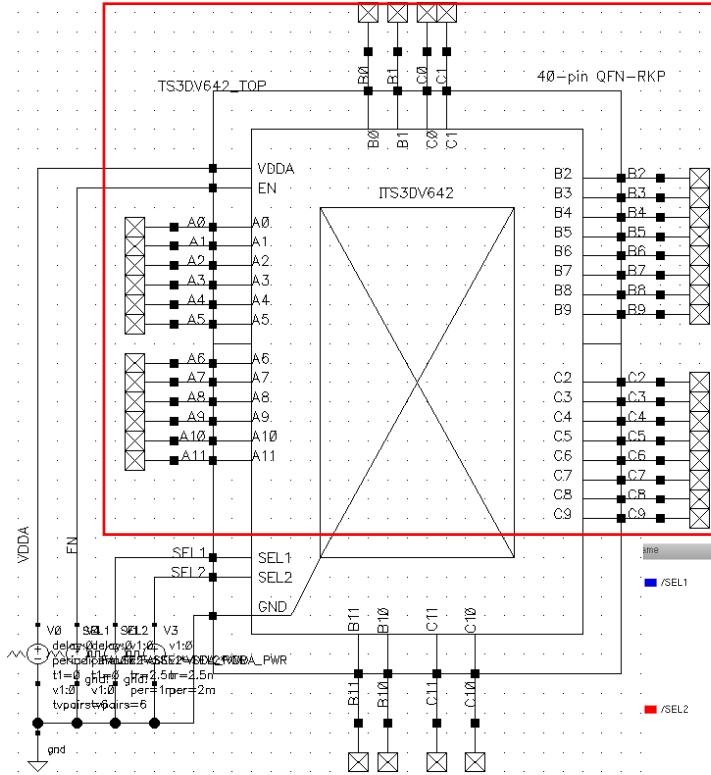


TS3DV642 simulation testbench & sample result

TOPLEVEL testbench for HSPICE modeling



<Question1>

What is the difference between ① and ②?

-in case of ①: A0-A11 and B0-B11 are enable, and C0-C11 are Hi-Z, right ?

-in case of ②: A0-A11 are enable, and B0-B11 are C0-C11 are Hi-Z, right ?

EN	SEL1	SEL2	FUNCTION
L	X	X	Switch Disabled. All Channel Hi-Z.
H	L	L	D0+/D0- to D0+A/D0-A ON. All the other channels Hi-Z.
H	L	H	D0+/D0- to D0+B/D0-B ON. All the other channels Hi-Z.
H	H	L	Channel A Enabled. Channel B Hi-Z.
H	H	H	Channel B Enabled. Channel A Hi-Z.

Table 1- Functional Table

<Question2>

There are no D0+/D0-, D0+A/D0-A, D0+B/-B on the left schematic.

As our recognition, D0+/- means A0,A1, D0+A/D0-A means B0,B1 and D0+B/D0-B means C0,C1. Is our recognition correct?

