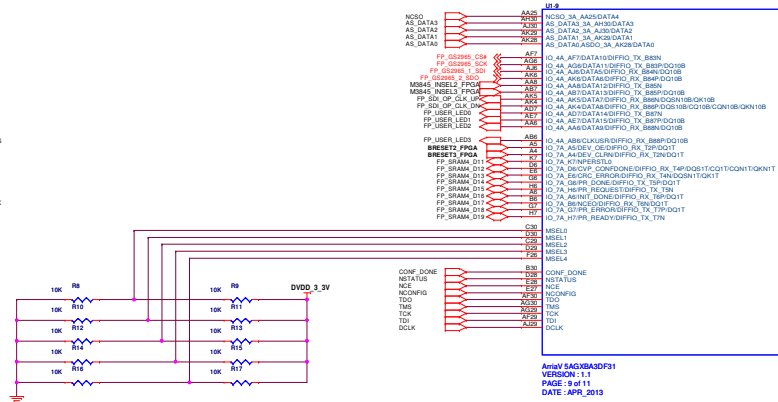
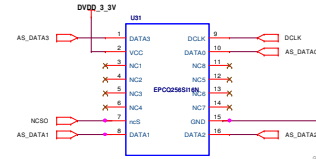
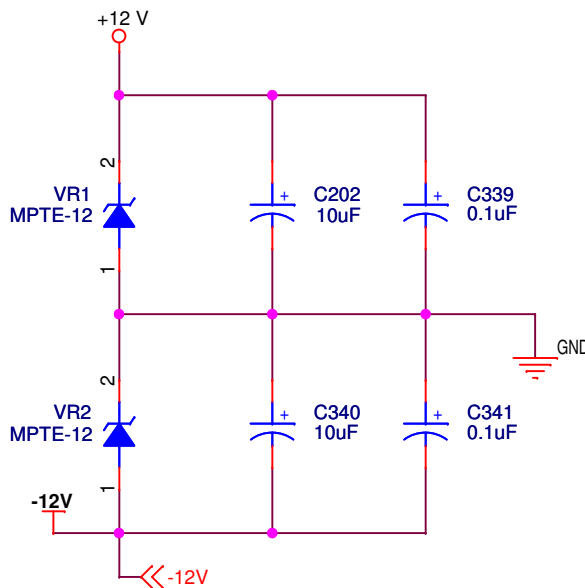
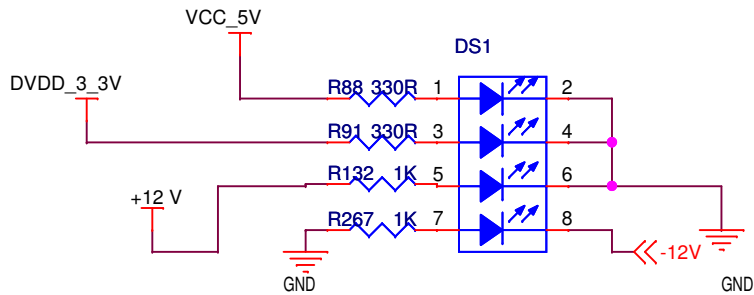
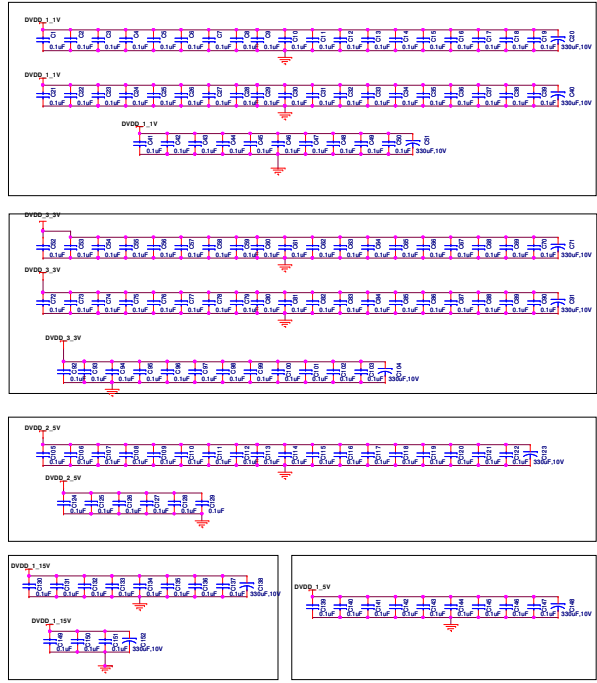
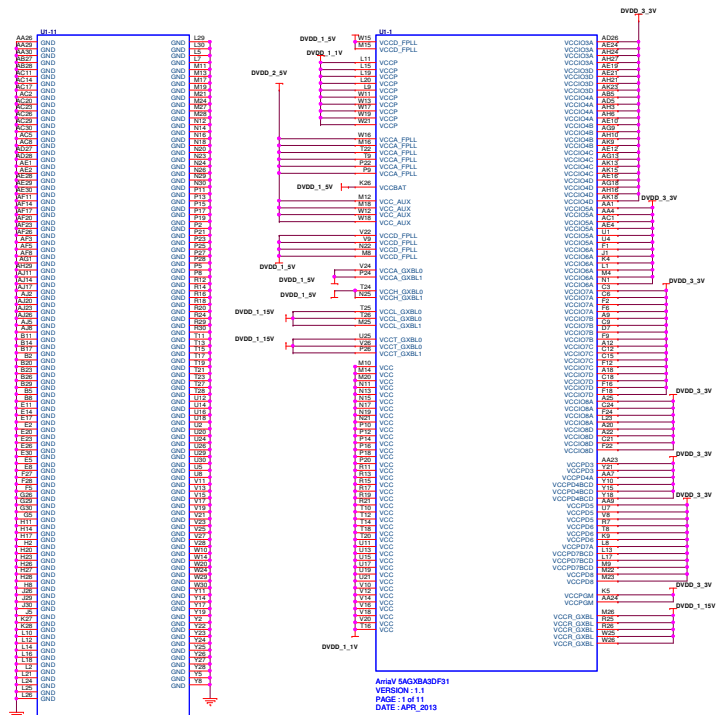


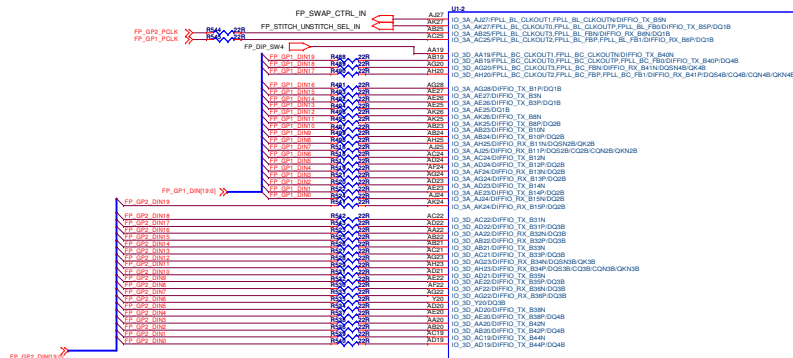


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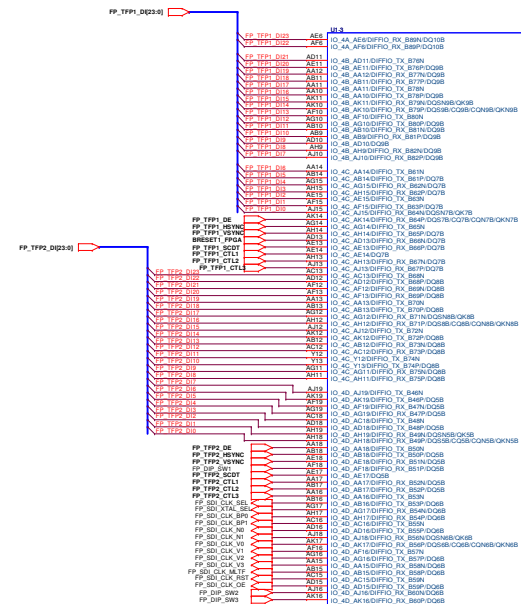
Note : 22R Series termination is added for FP_GP1_DIN19 and FP_GP1_PCLK

GS2962 SIGNALS



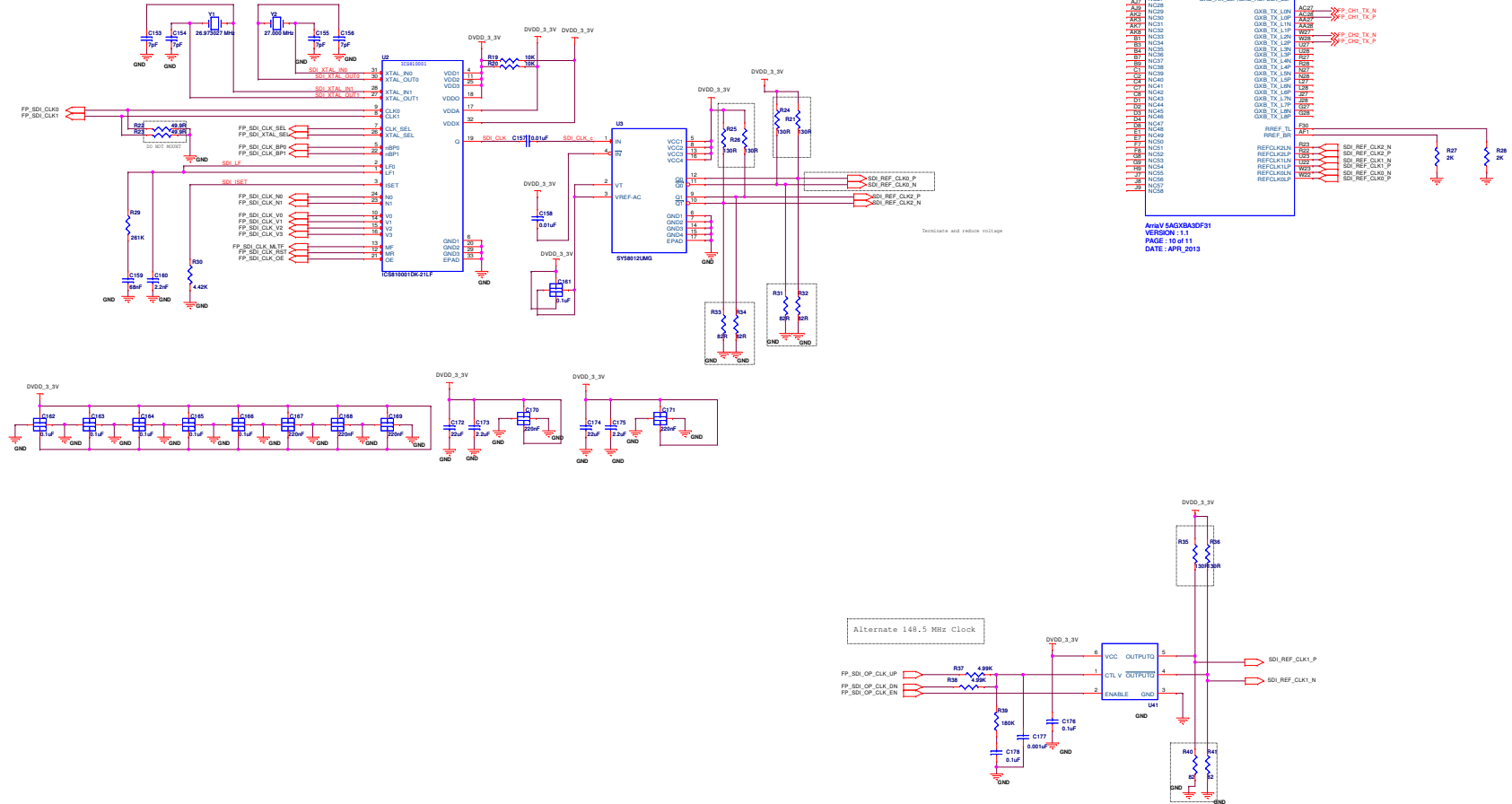
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TFP401A & ICS810001 SIGNALS

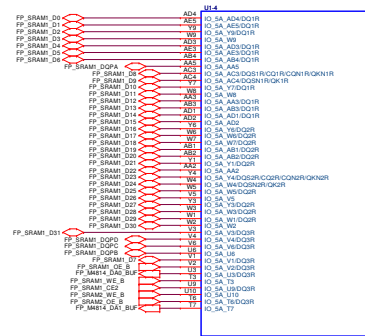


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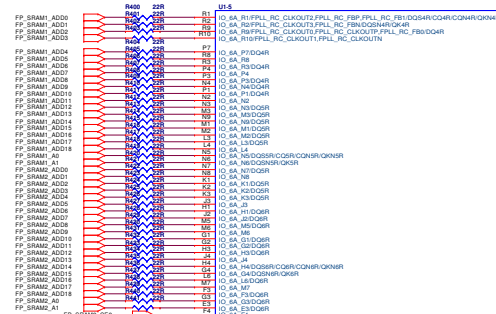
SD / HD VIDEO CLOCK GENERATOR



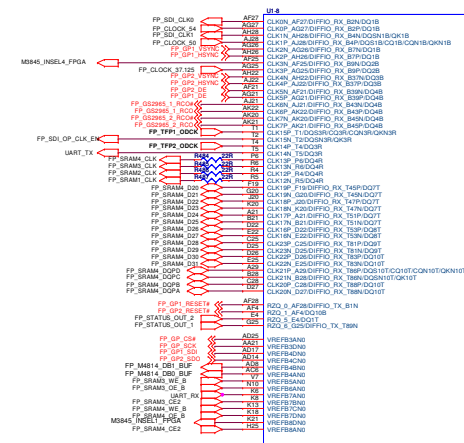
Note : 22R Series termination is added for all SRAM Address lines and clock.



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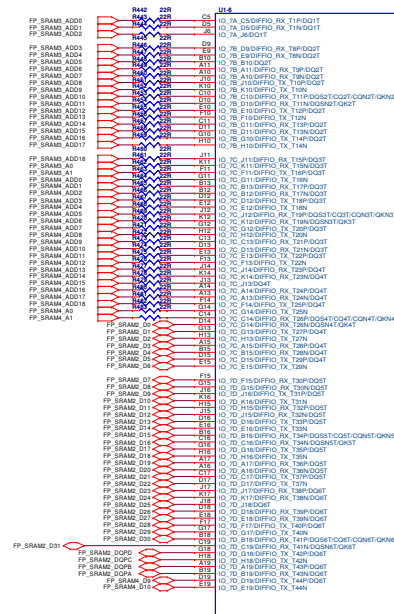
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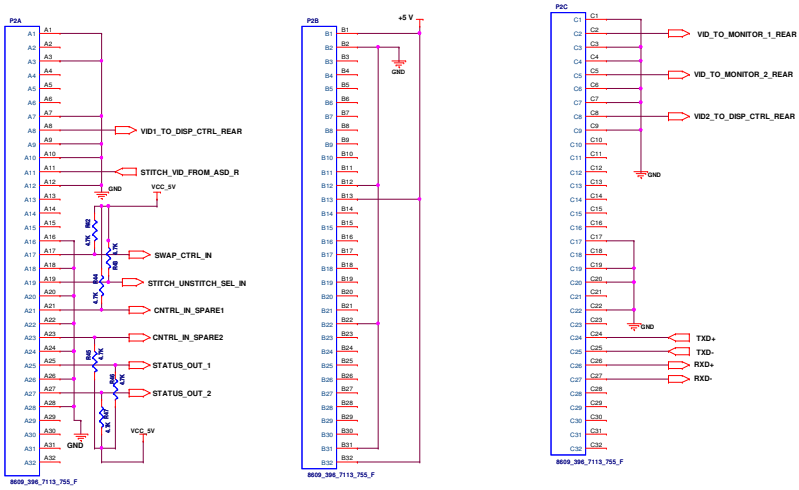
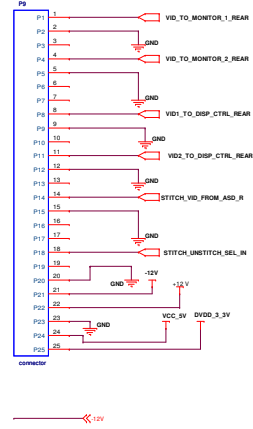
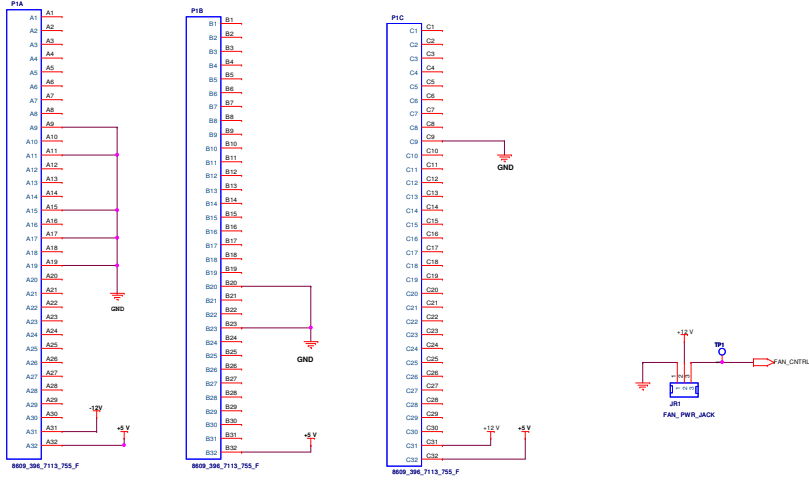


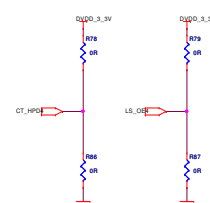
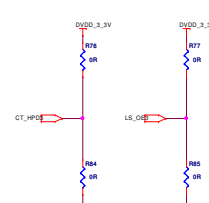
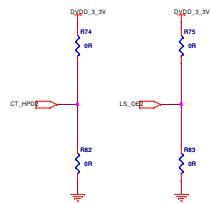
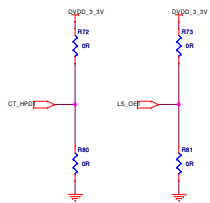
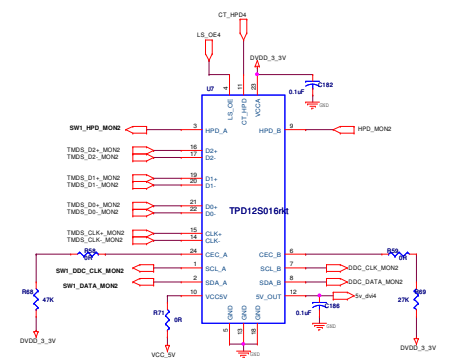
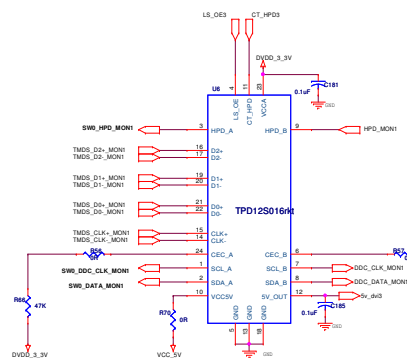
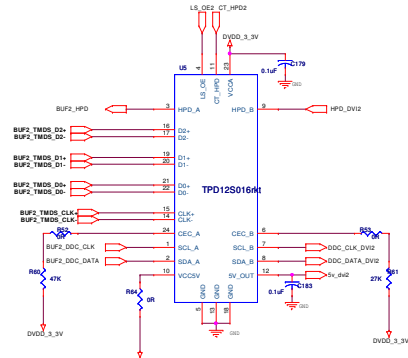
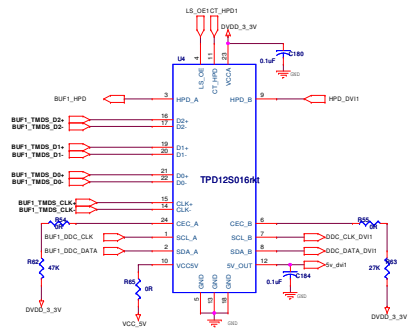
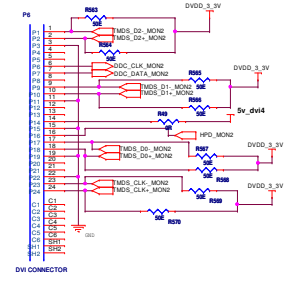
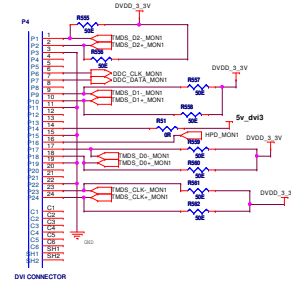
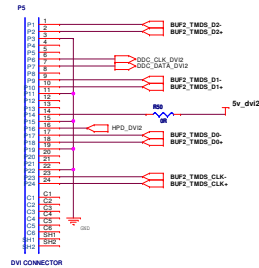
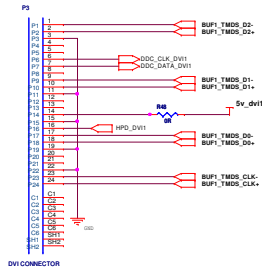
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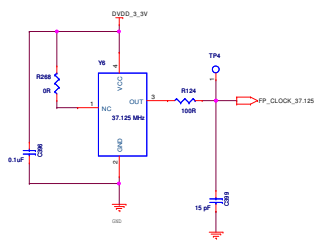
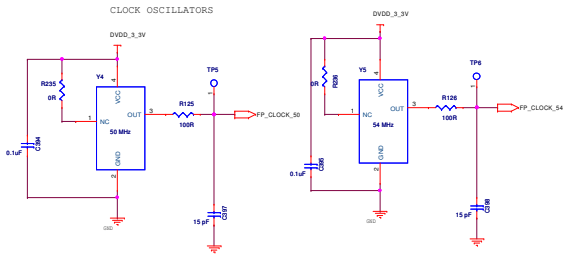


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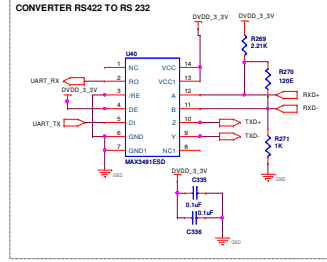
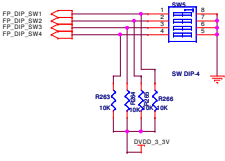
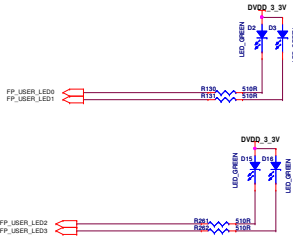
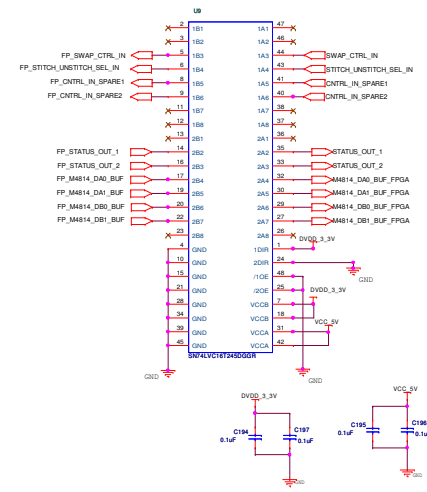
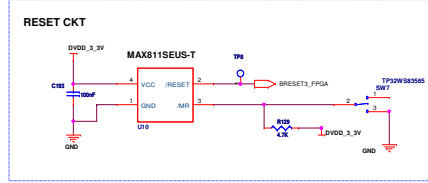
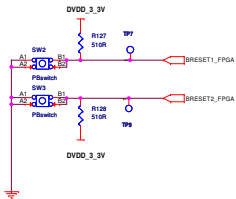
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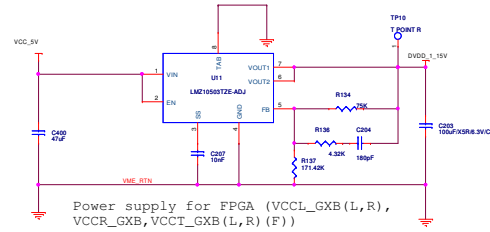


2.54 mm Male berg connectors (J7,J8)

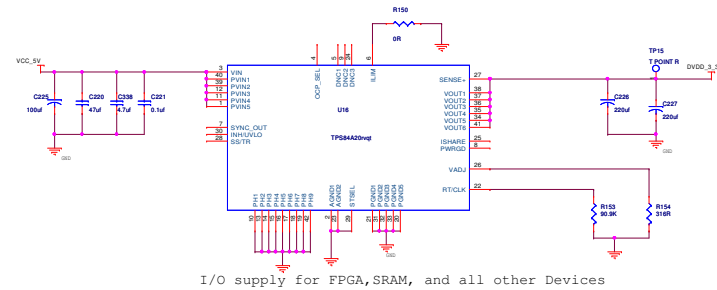
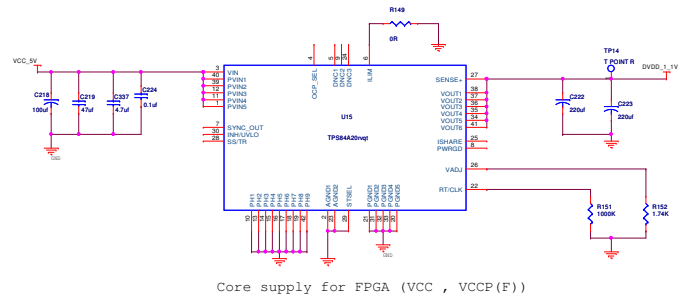
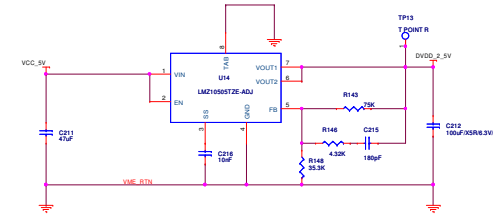
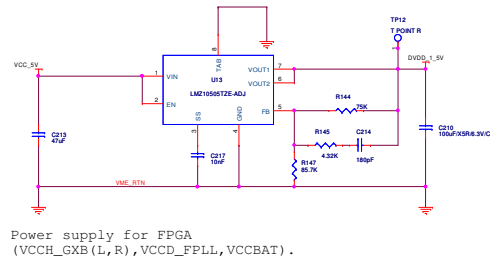
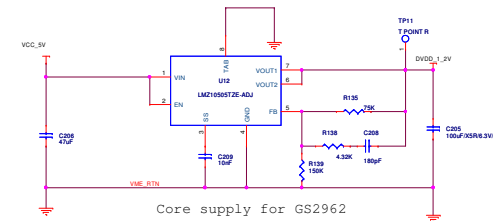
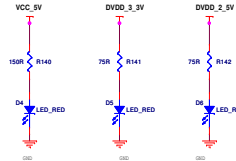
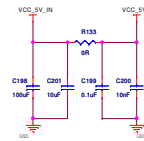


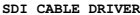
2.54 mm Male berg connector (J4)

POWER SUPPLY CIRCUIT

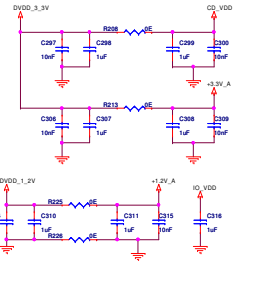


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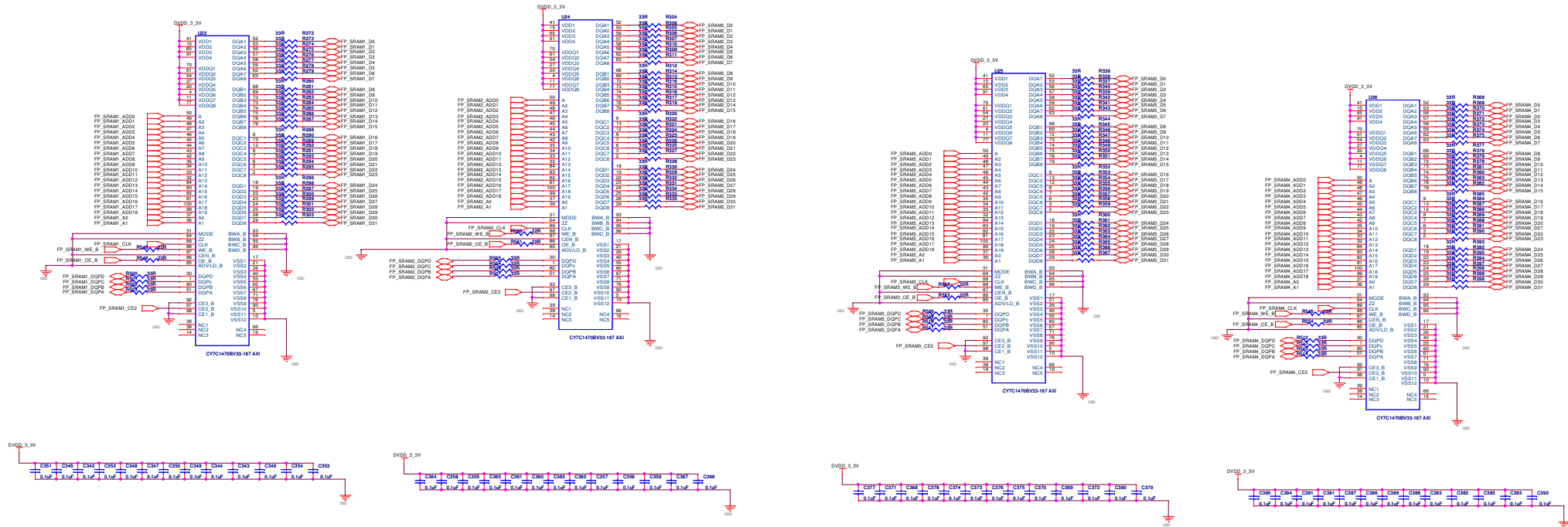


The schematic diagram illustrates the power supply and signal connections for the GS2962-0E3 board. Key components and connections include:

- Power Supply:** The board is powered by a +1.2V_A supply. The input is connected to the VDD pin of the GS2962-0E3 chip. The output of the chip is connected to the VDD pin of the GS2962-0E3 chip.
- Capacitors:** Several capacitors are used for decoupling and filtering, including C304 (100pF), C305 (100pF), C306 (100pF), C307 (100pF), C308 (100pF), C309 (100pF), C310 (100pF), C311 (100pF), C312 (100pF), C313 (100pF), C314 (100pF), C315 (100pF), C316 (100pF), C317 (100pF), C318 (100pF), C319 (100pF), C320 (100pF), C321 (100pF), C322 (100pF), C323 (100pF), C324 (100pF), C325 (100pF), C326 (100pF), C327 (100pF), C328 (100pF), C329 (100pF), C330 (100pF), C331 (100pF), C332 (100pF), C333 (100pF), C334 (100pF), C335 (100pF), C336 (100pF), C337 (100pF), C338 (100pF), C339 (100pF), C340 (100pF), C341 (100pF), C342 (100pF), C343 (100pF), C344 (100pF), C345 (100pF), C346 (100pF), C347 (100pF), C348 (100pF), C349 (100pF), C350 (100pF), C351 (100pF), C352 (100pF), C353 (100pF), C354 (100pF), C355 (100pF), C356 (100pF), C357 (100pF), C358 (100pF), C359 (100pF), C360 (100pF), C361 (100pF), C362 (100pF), C363 (100pF), C364 (100pF), C365 (100pF), C366 (100pF), C367 (100pF), C368 (100pF), C369 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Note : 33R Series termination is added for all SRAM data lines and DATA parity lines.



Note : 22R Series termination is removed from FP_TFP2_ODCK

