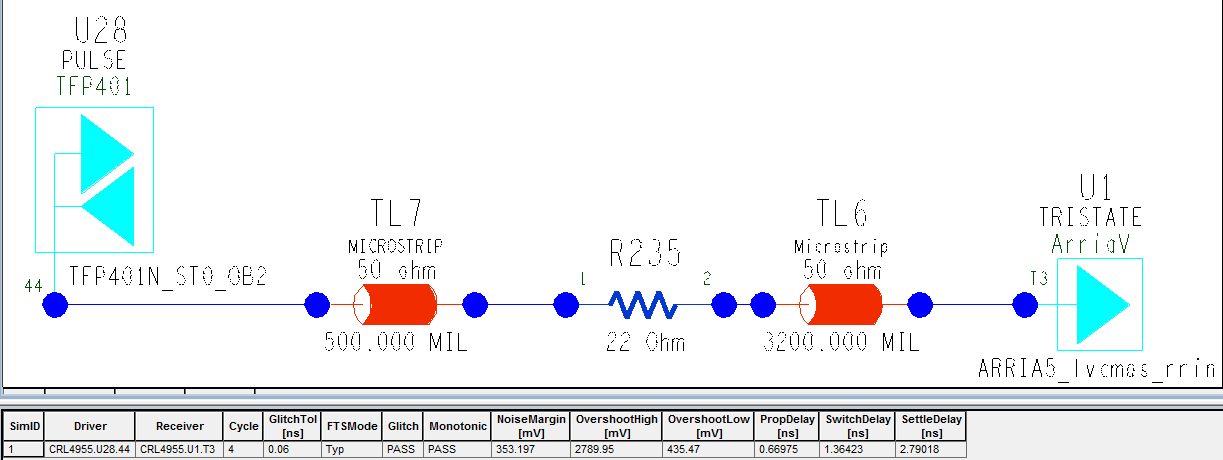
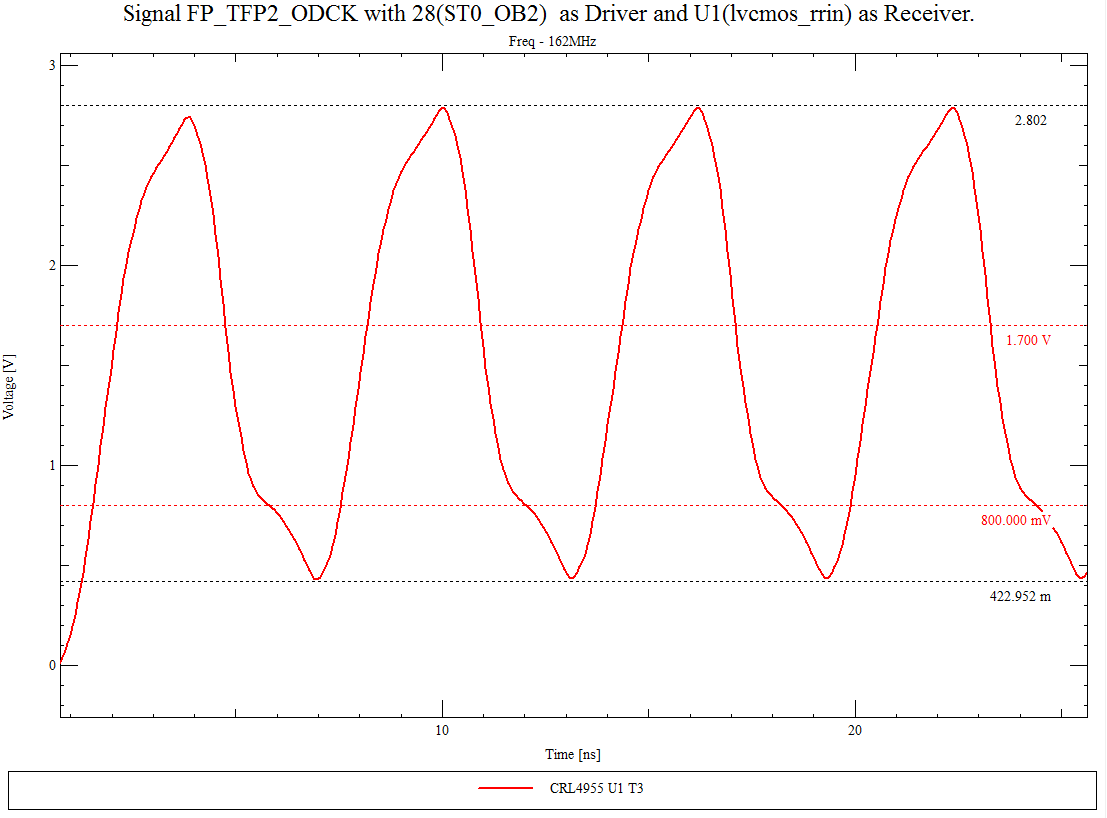
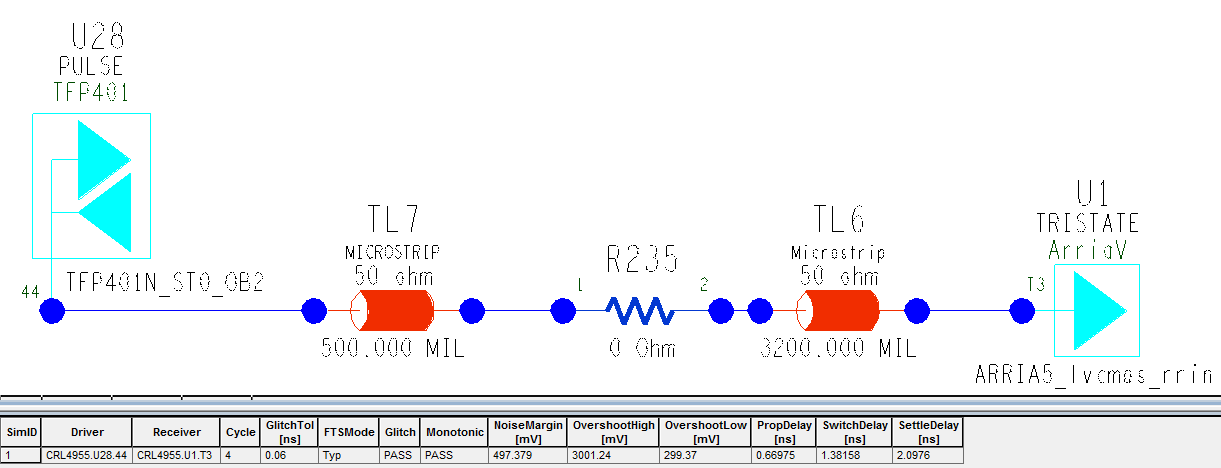
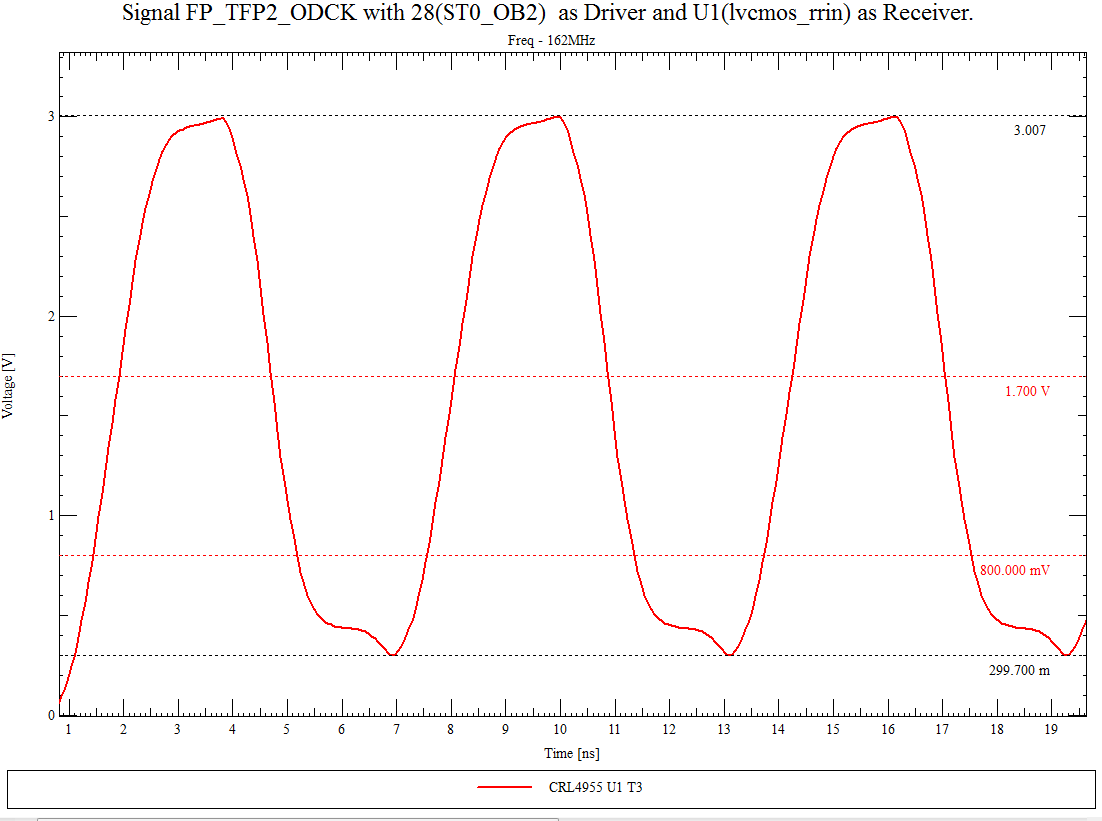
**Topology & waveform for signal FP\_TFP2\_ODCK with 28(ST0\_OB2) as Driver and U1(lvcmos\_rrin) as Receiver. Freq – 162MHz.**





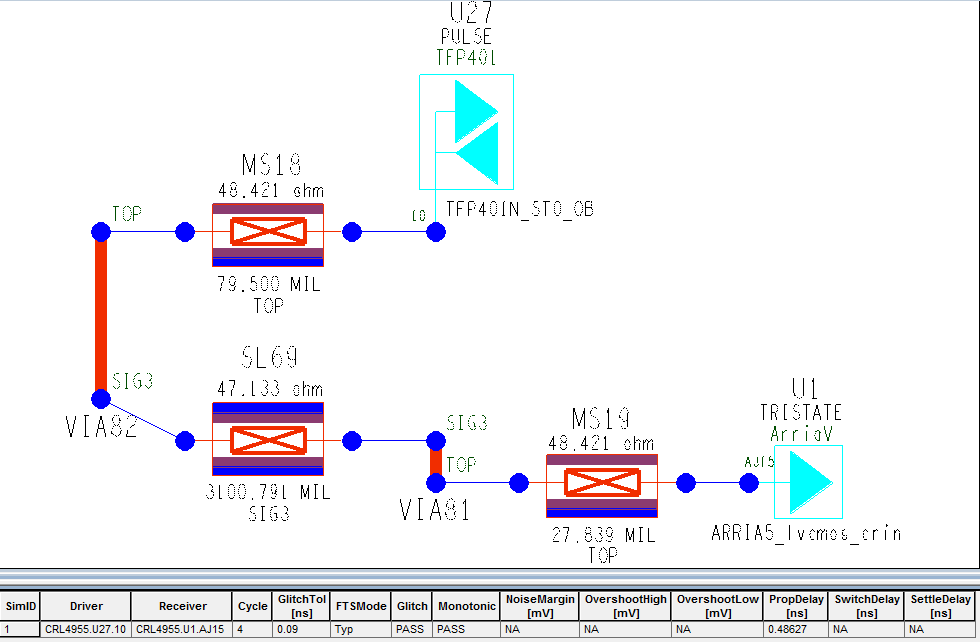
**Topology & waveform for signal FP\_TFP2\_ODCK with 28(ST0\_OB2) as Driver and U1(lvcmos\_rrin) as Receiver. Freq – 162MHz.**

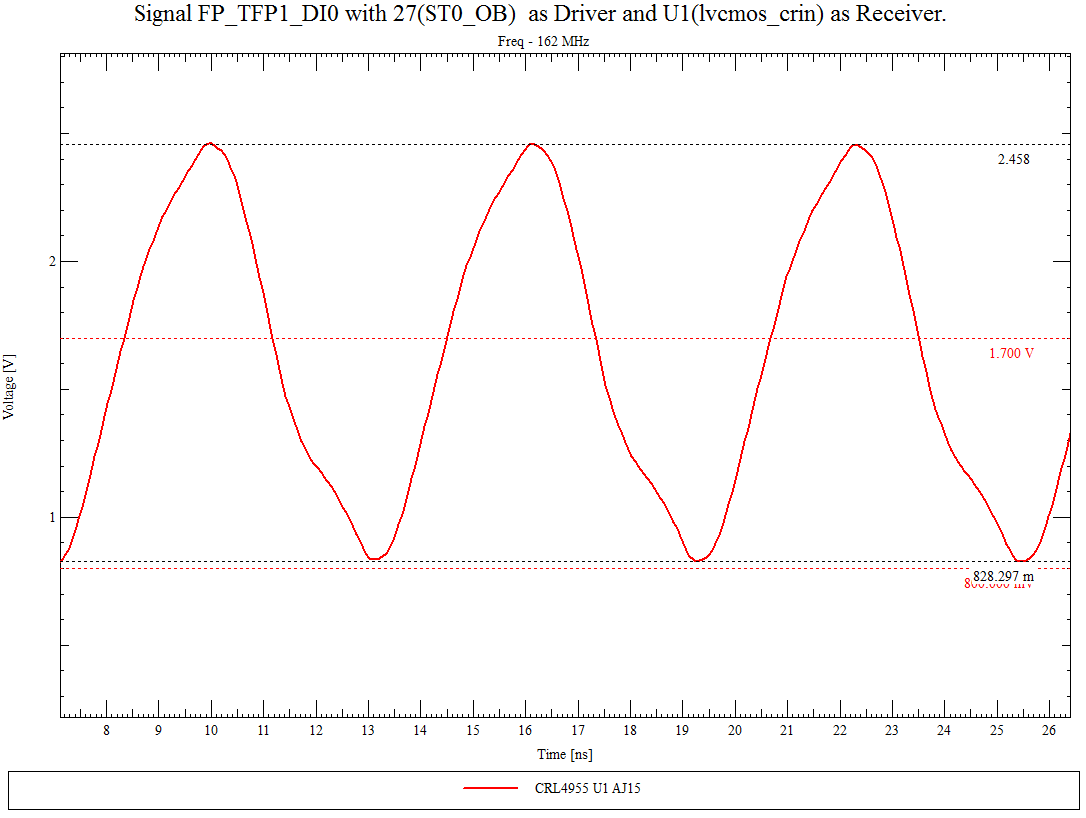
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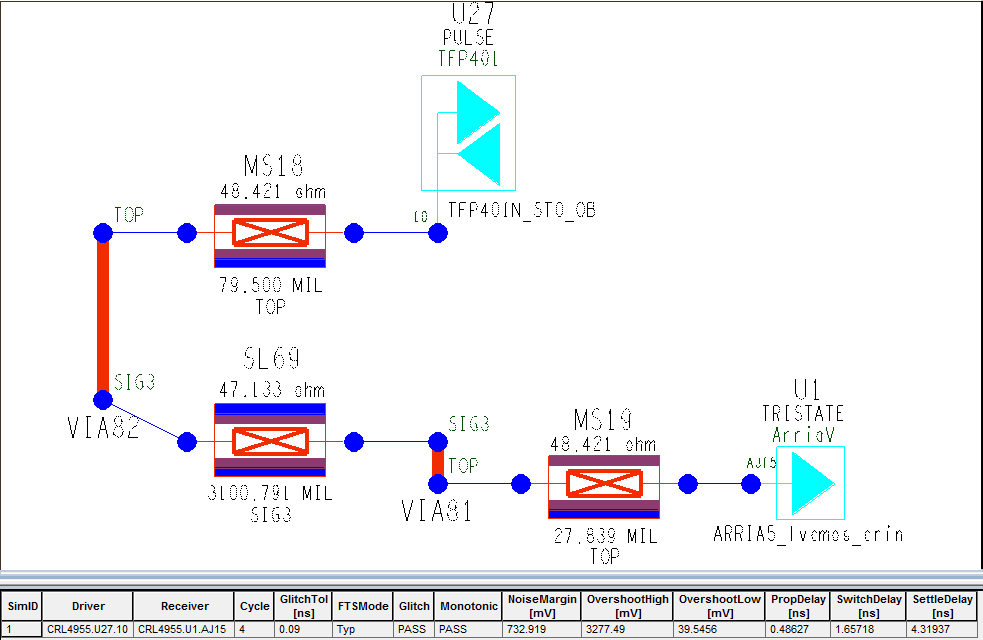
**Note: Without 22E Series termination**

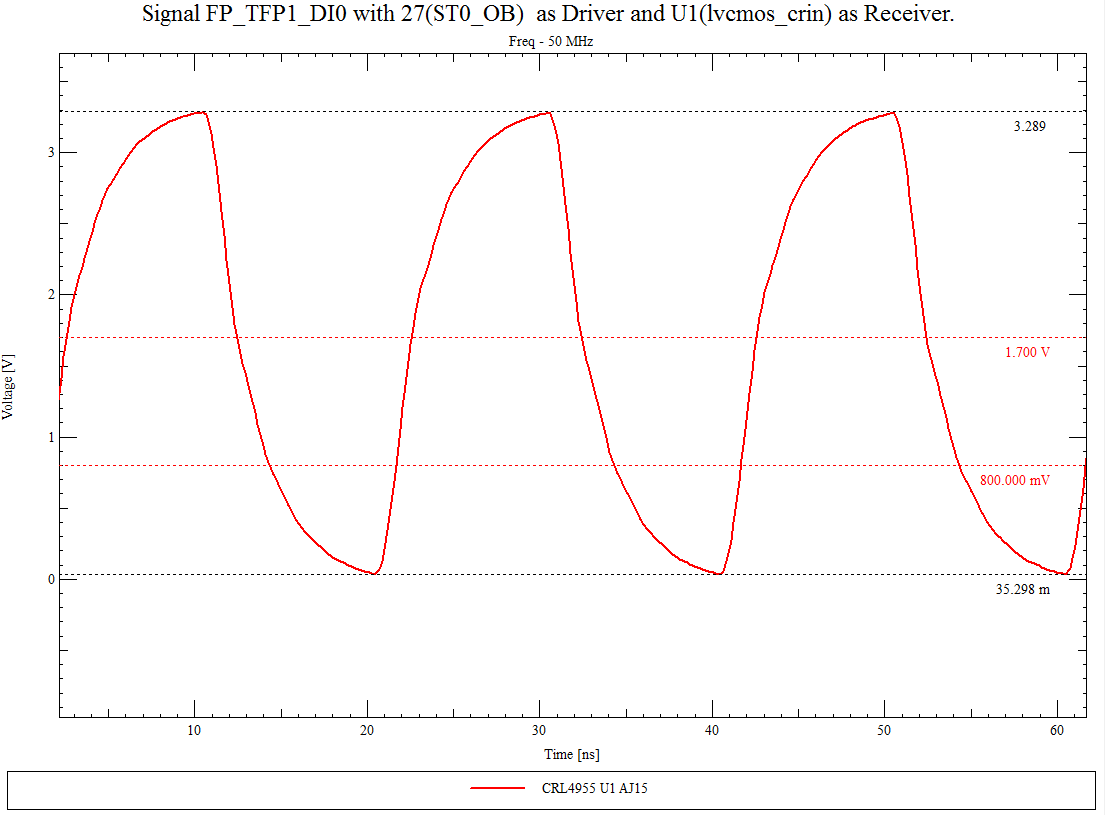
**Topology & waveform for signal FP\_TFP1\_DI0 with 27(ST0\_OB) as Driver and U1(lvcmos\_crin) as Receiver. Freq – 162MHz.**

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**Topology & waveform for signal FP\_TFP1\_DI0 with 27(ST0\_OB) as Driver and U1(lvcmos\_crin) as Receiver. Freq – 50MHz.**

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**Note : Able to See 3.3 V for Lesser frequency.**