Customer schematic & The waveform which failed in the stop condition

=Master side=

3.3V_A

R203 RES103
R204 RES103

I2C_SCK_CPU
(SCK is typo.)

R201 RES103

SG

+3.3V
TCA4311ADGKR
U301   1/2

3	SCLIN	SCLOUT	2
6	SDAIN	SDAOUT	7
1	EN	READY	5

TCA4311

3.3V_B

R202 RES472
R205 RES472
R206 RES472

=Slave side=

I2C_SCK
I2C_SDA
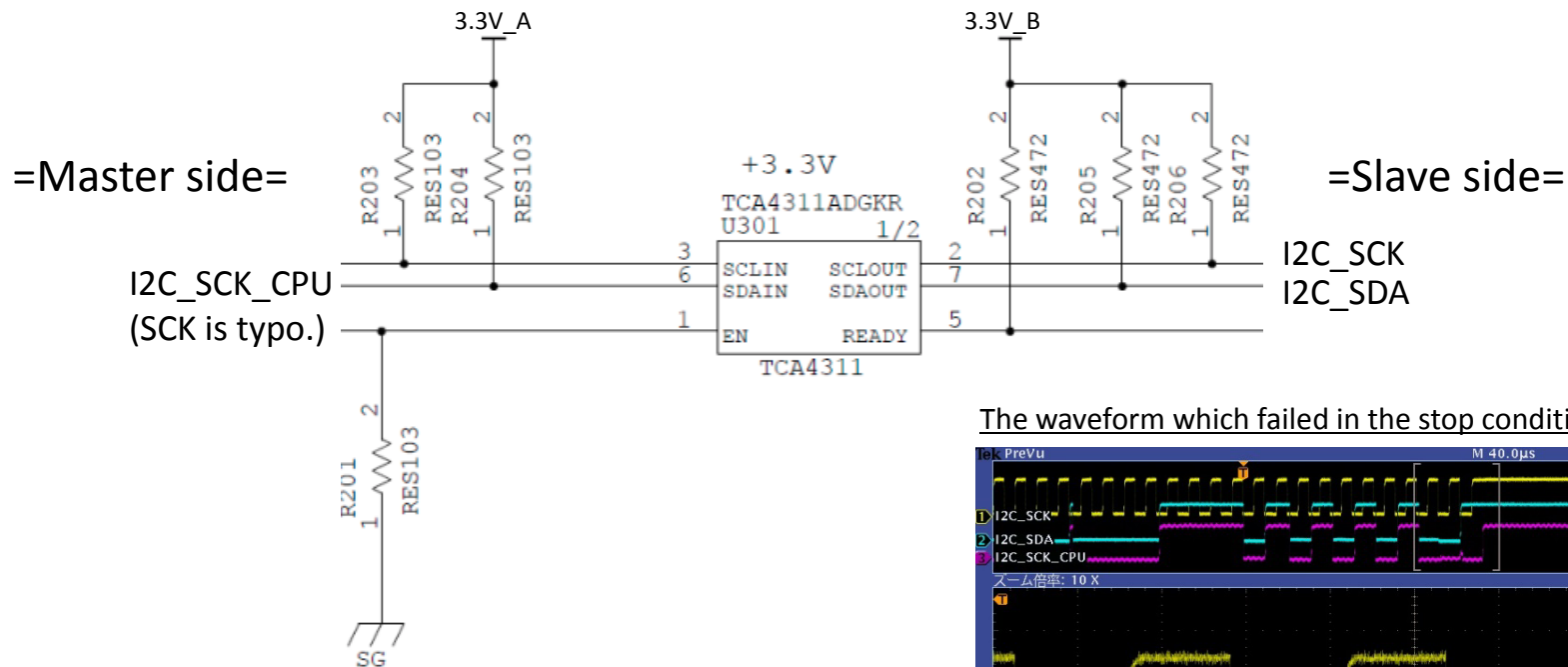
The waveform which failed in the stop condition
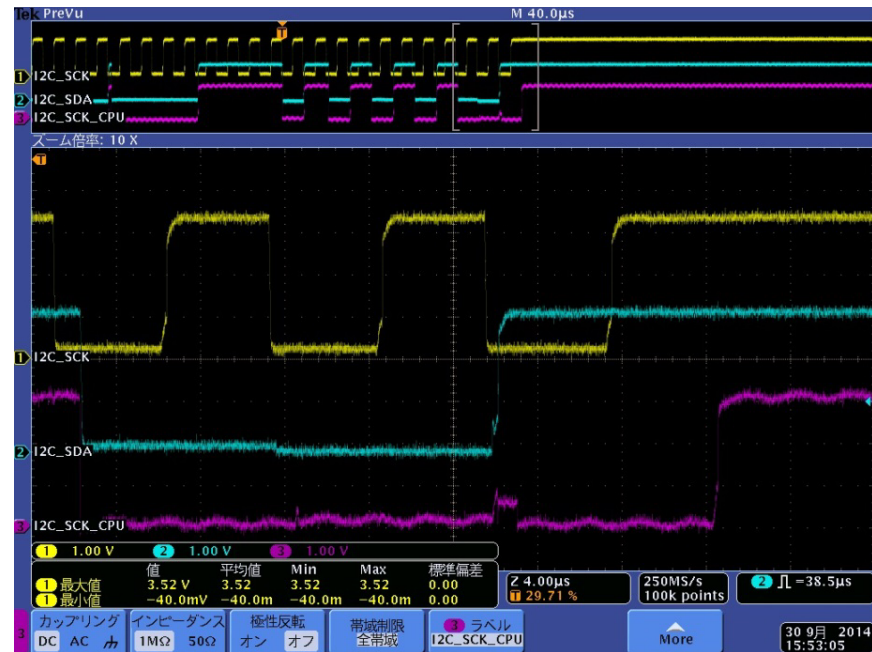
[Q1]
・Why did it fail in the stop condition in the slave side?
・Please tell me what they should do to operate it normally.

[Q2]
It worked normally when they changed R203&R204 to 4.7kΩ.
Why does it seems to be ?
Can you explain the reason ?

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