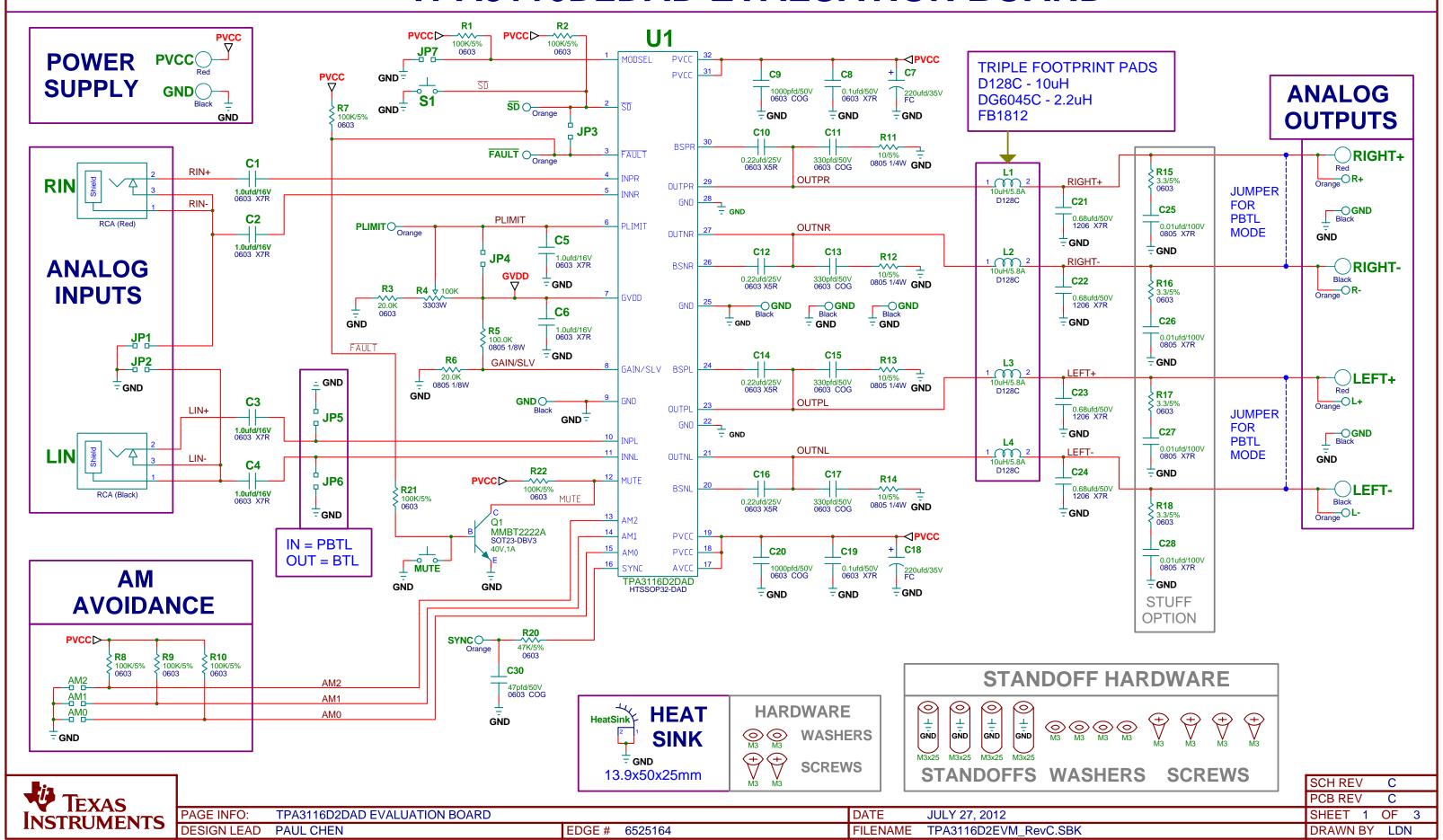
TPA3116D2DAD EVALUATION BOARD



TPA3116D2DAD EVALUATION BOARD

REVISION HISTORY					
REVISION	DESCRIPTION	DATE	APPROVAL		
Α	RELEASE	NOVEMBER 8, 2011	PC		
В	1. DELETE CR1, CR2, CR3, CR4. 2. DISCONNECT U1-1. 3. ADD R1 AND JP7 TO U1-1. 4. ADD 47K OHM RESISTOR BETWEEN SYNC(TP) AND U1-16. 5. ADD 47pfd CAP BETWEEN SYNC(TP) AND GND. 6. CHANGE R3 TO 20K OHMS.	APRIL 16, 2012	PC		
С	1. R7 AND MUTE SWITCH DISCONNECTED. 2. R7 CONNECTED BETWEEN PVCC NET AND FAULT NET. 3. R21(100K) ADDED AND CONNECTED BETWEEN FAULT NET AND Q1-B. 4. Q1 ADDED(MMBT2222A) AND CONNECTED AS FOLLOWS: Q1-B TO MUTE SWITCH AND R21-2 Q1-E TO GND Q1-C TO U1-12.(MUTE NET) 5. R22(100K) ADDED AND CONNECTED U1-12(MUTE NET) AND GVDD NET.	JULY 27, 2012	PC		

SCH RE	V	С	
PCB REV		С	
SHEET	2	OF	3
DRAWN	BY	LDN	

PAGE INFO: REVISION HISTORY JULY 27, 2012 DESIGN LEAD PAUL CHEN EDGE # 6525164 FILENAME TPA3116D2EVM_RevC.SBK

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