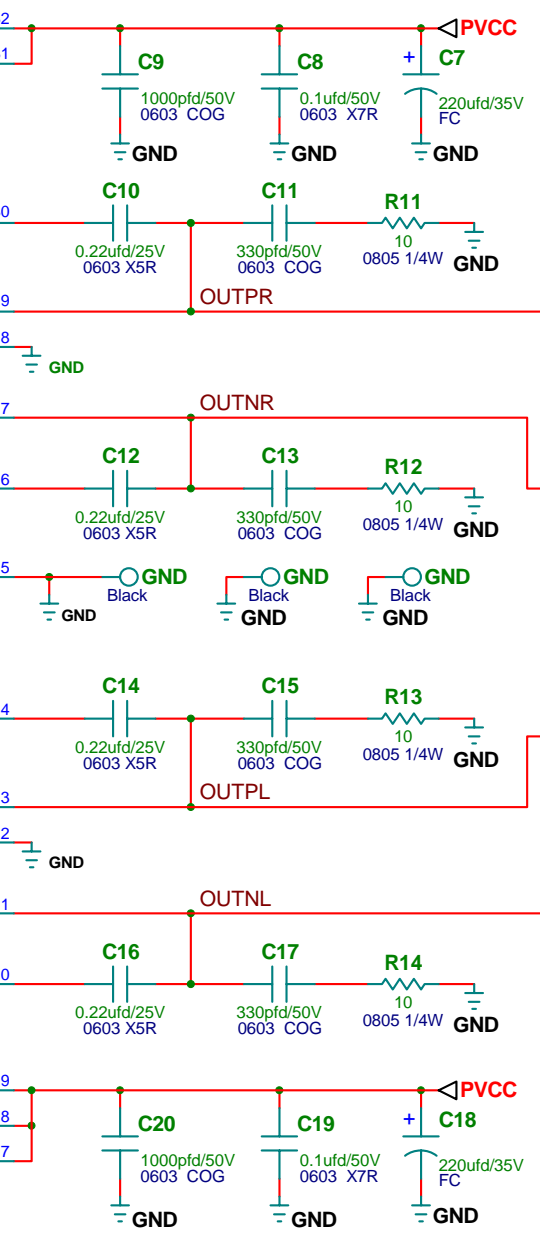
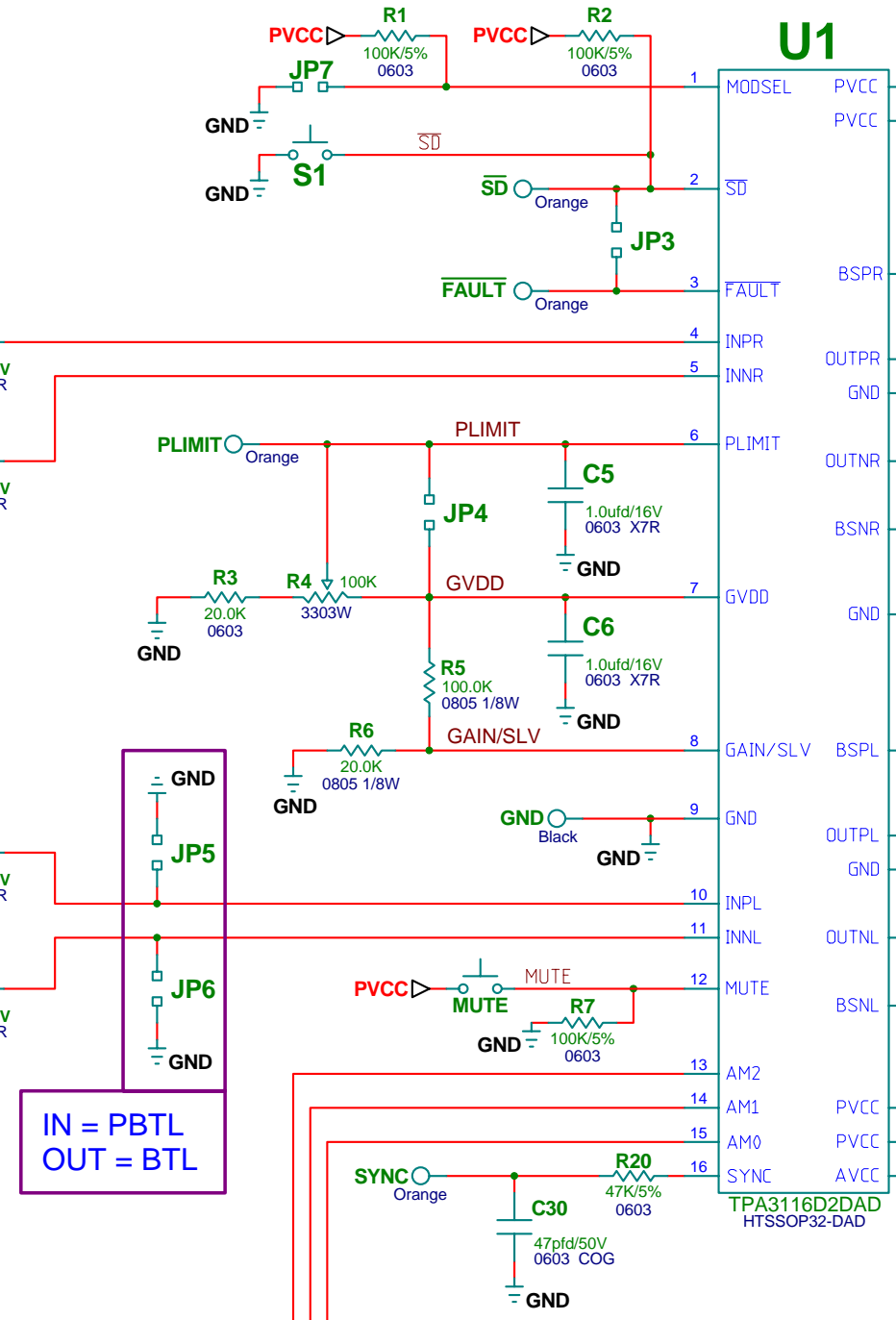
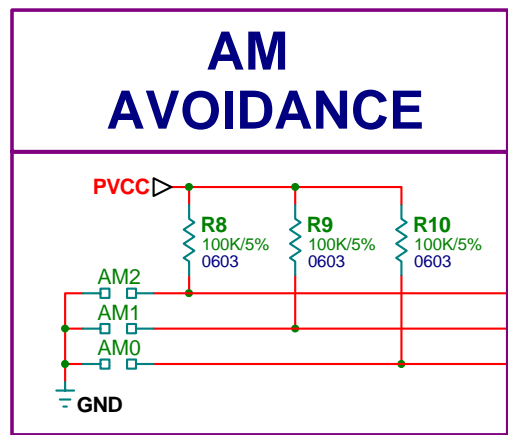
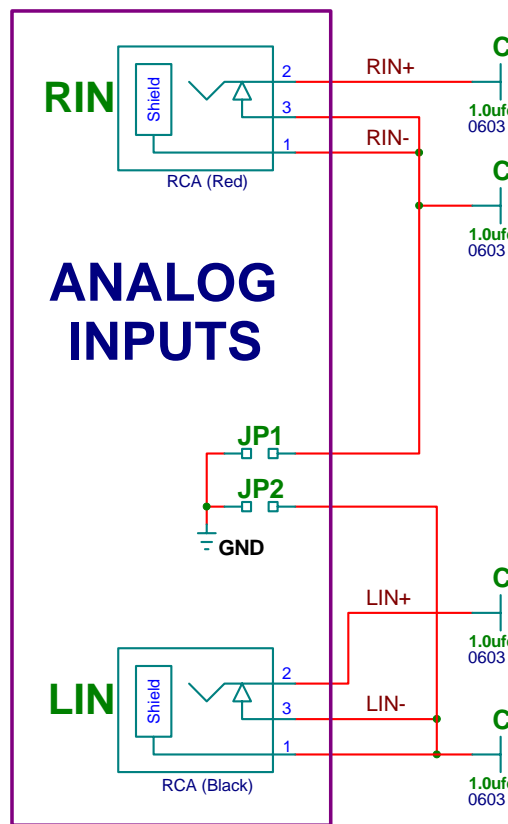
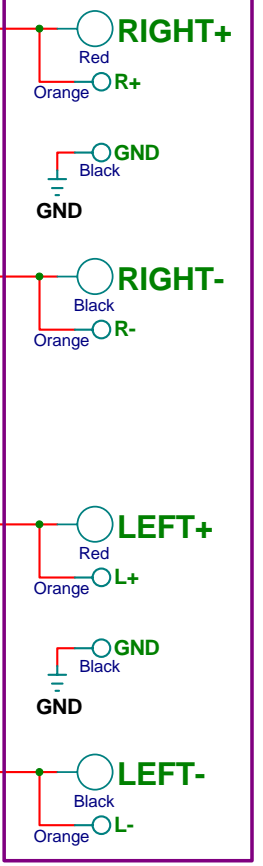


TPA3116D2DAD EVALUATION BOARD



TRIPLE FOOTPRINT PADS
 D128C - 10uH
 DG6045C - 2.2uH
 FB1812

ANALOG OUTPUTS

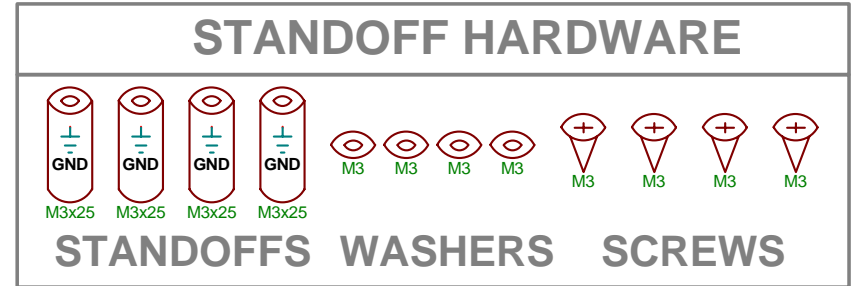
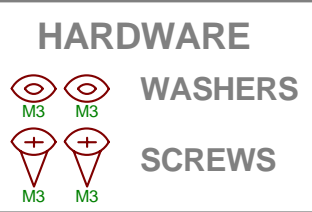
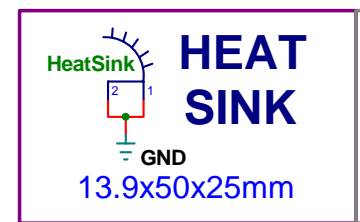


IN = PBTL
 OUT = BTL

JUMPER FOR PBTL MODE

JUMPER FOR PBTL MODE

STUFF OPTION



TPA3116D2DAD EVALUATION BOARD

REVISION HISTORY

REVISION	DESCRIPTION	DATE	APPROVAL
A	RELEASE	NOVEMBER 8, 2011	PC
B	1. DELETE CR1, CR2, CR3, CR4. 2. DISCONNECT U1-1. 3. ADD R1 AND JP7 TO U1-1. 4. ADD 47K OHM RESISTOR BETWEEN SYNC(TP) AND U1-16. 5. ADD 47pfd CAP BETWEEN SYNC(TP) AND GND. 6. CHANGE R3 TO 20K OHMS.	APRIL 16, 2012	PC

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