**This is production 6657 EVM with official V03 FPGA image. Switch in PCIE boot mode and power on, PCIE cannot link-up with a Linux PC**









****

**Put card into “no boot” mode**

**Now downgrade FPGA to V02**

FPGA upgrade tool V0.3 for EVM6678, base on TI MCSDK 2.00.05.17

platform\_init successfully

Enter the filename of binary (enter 'none' to skip):

C:\Data\Downloads\EVM\_FPGA\_v02.bit

file size 149630 bytes

file read complete!! 149630 / 149630

00 00 1e 00 00 00 00 80 00 00 00 00 00 00 00 00 00 00 1e 00 00 00 00 00 00 00 0e 00 00 00 00 40

00 00 14 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 09 00 00 00 00 40 00 00 06 00 00 00 00 40

00 00 0e 00 00 00 00 40 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

disable setctor proctection ok!

ID[0] = 0x1f

ID[1] = 0x24

ID[2] = 0x00

ID[3] = 0x00

ID is correct!

Page size = 256 byte

..............................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................................==============================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================================

00 00 1e 00 00 00 00 80 00 00 00 00 00 00 00 00 00 00 1e 00 00 00 00 00 00 00 0e 00 00 00 00 40

00 00 14 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 09 00 00 00 00 40 00 00 06 00 00 00 00 40

00 00 0e 00 00 00 00 40 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00 00 00 00 00 00 00 00

Verify successful! 0

FPGA upgate complete

**Put card into “no boot” mode**

**Then re-flash IBL, USE \ti\mcsdk\_2\_01\_02\_06\tools\program\_evm\binaries\evm6657l**

The out file is there: eepromwriter\_evm6657l.out

The text file is: eepromwriter\_input.txt (no need to edit this file)

file\_name = eeprom51.bin

bus\_addr = 81

start\_addr = 0

swap\_data = 0

IBL is: eeprom51.bin

Load out file, load .bin to memory **0x0c00\_0000**





**Run the .out file**

[C66xx\_0] EEPROM Writer Utility Version 01.00.00.05

Writing 47848 bytes from DSP memory address 0x0c000000 to EEPROM bus address 0x0051 starting from device address 0x0000 ...

Reading 47848 bytes from EEPROM bus address 0x0051 to DSP memory address 0x0c010000 starting from device address 0x0000 ...

Verifying data read ...

EEPROM programming completed successfully

**Put card back into “no boot” mode and into a Linux PC**

**When power up, PC should be in LL2**







**Link is up**





**Linux machine found it:**

