Fig1a OK （input 1kHz OdBFS）
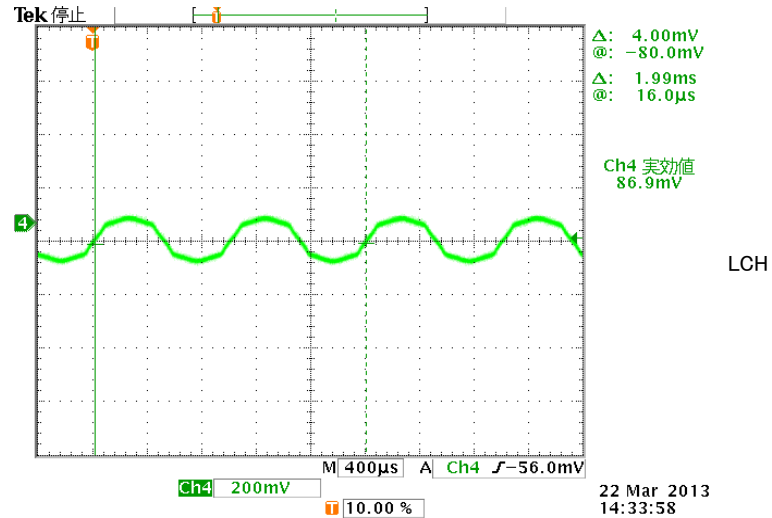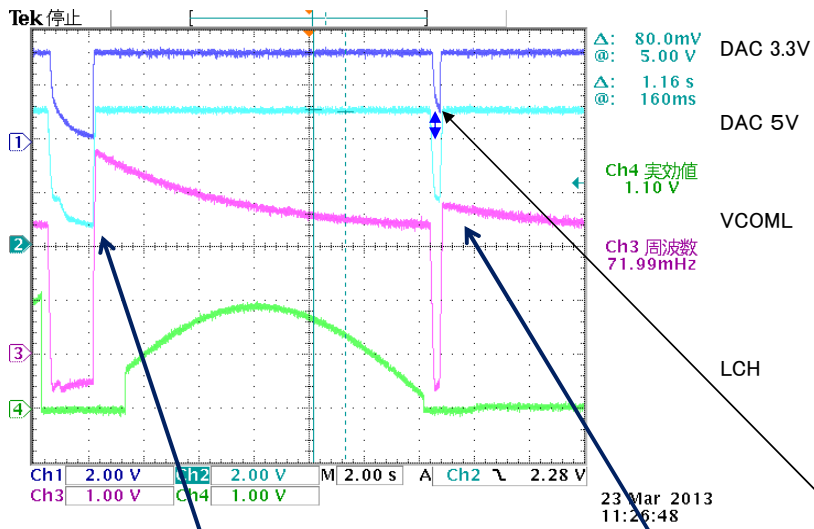
LCH

Ch4 実効値
2.36 V

Δ: 160mV
@: 220mV
Δ: 1.99ms
@: 16.0μs

M 400μs A Ch4 ƒ-60.0mV
Ch4 1.00 V   10.00 %
22 Mar 2013
14:35:35

Fig1b NG （input;1kHz OdBFS ）

LCH

Ch4 実効値
86.9mV

Δ: 4.00mV
@: -80.0mV
Δ: 1.99ms
@: 16.0μs

M 400μs A Ch4 ƒ-56.0mV
Ch4 200mV   10.00 %
22 Mar 2013
14:33:58

DAC 3.3V

DAC 5V

VCOML

LCH

Δ: 80.0mV
@: 5.00 V
Δ: 1.16 s
@: 160ms

Ch4 実効値
1.10 V

Ch3 周波数
71.99mHz

Ch1 2.00 V   Ch2 2.00 V   M 2.00 s A Ch2 2.28 V
Ch3 1.00 V   Ch4 1.00 V
23 Mar 2013
11:26:48

Fig2   VCC and VDD when OK and NG

Timing when OK
5V (VCC) of DAC recoverns from below 1V ; OK

Timing when NG
5V(VCC) recovers from 2V;NG   3.3V is below 1.5V

Problem:

*This phenomenon found when Temp= -10 ~ 0 deg-C

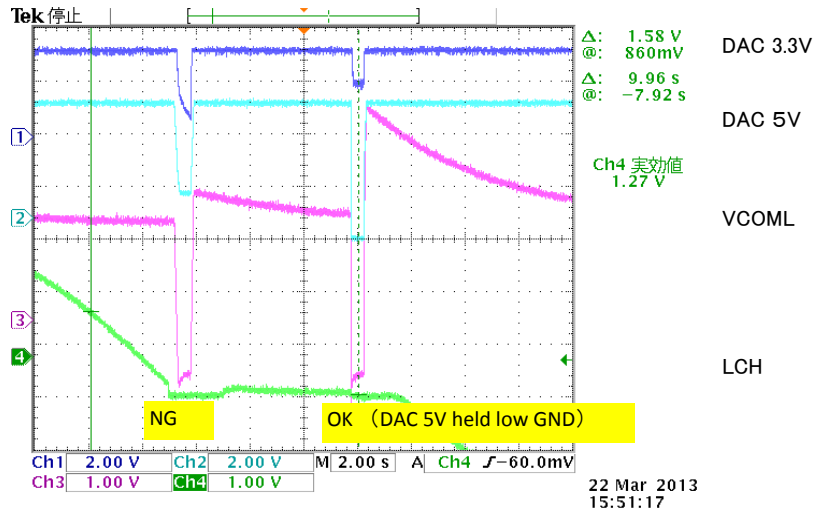*This phenomenon found when 3.3V/5V suspend in a few period.

*Assert RESET(/RST #14) does NOT solve probem.
(NG waveform stucks)

Fig3　VCC waveform when NG（Just FYI）