ADC12D1x00RFRB Utilization Summary				
Logic Utilization	**Used**	**Available**	**Utilization**	**Note(s)**
Number of Slice Flip Flops	1,496	21,504	6%	
Number of 4 input LUTs	1,126	21,504	5%	
Number of occupied Slices	1,224	10,752	11%	
Number of Slices containing only related logic	1,224	1,224	100%	
Number of Slices containing unrelated logic	0	1,224	0%	
Total Number of 4 input LUTs	1,272	21,504	5%	
Number used as logic	1,126			
Number used as a route-thru	146			
Number of bonded IOBs	305	448	68%	
IOB Flip Flops	193			
IOB Dual-Data Rate Flops	48			
IOB Master Pads	56			
IOB Slave Pads	56			
Number of BUFG/BUFGCTRLs	10	32	31%	
Number used as BUFGs	9			
Number used as BUFGCTRLs	1			
Number of FIFO16/RAMB16s	52	72	72%	
Number used as RAMB16s	52			
Number of DCM_ADVs	6	8	75%	
Average Fanout of Non-Clock Nets	2.96			