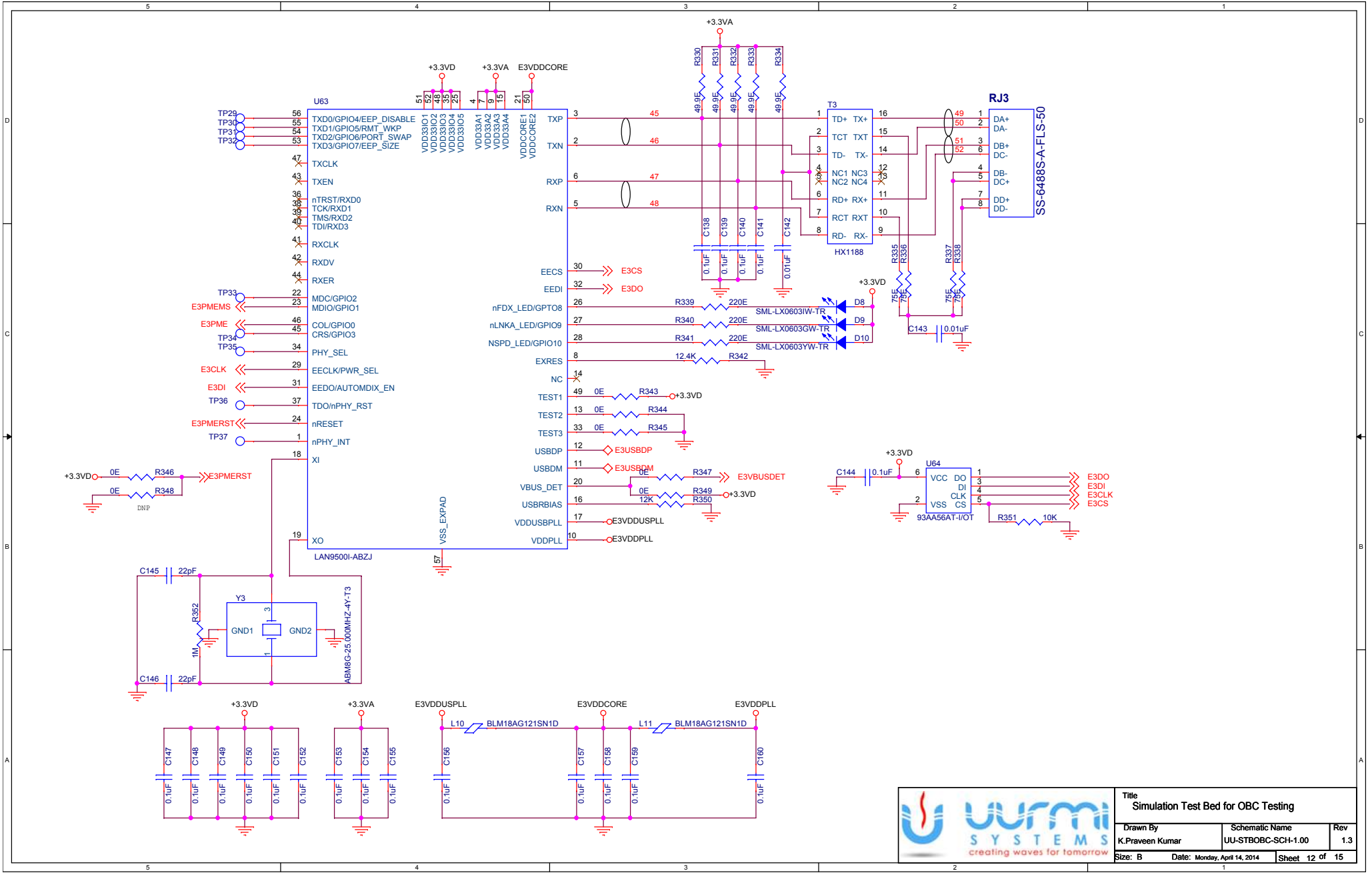
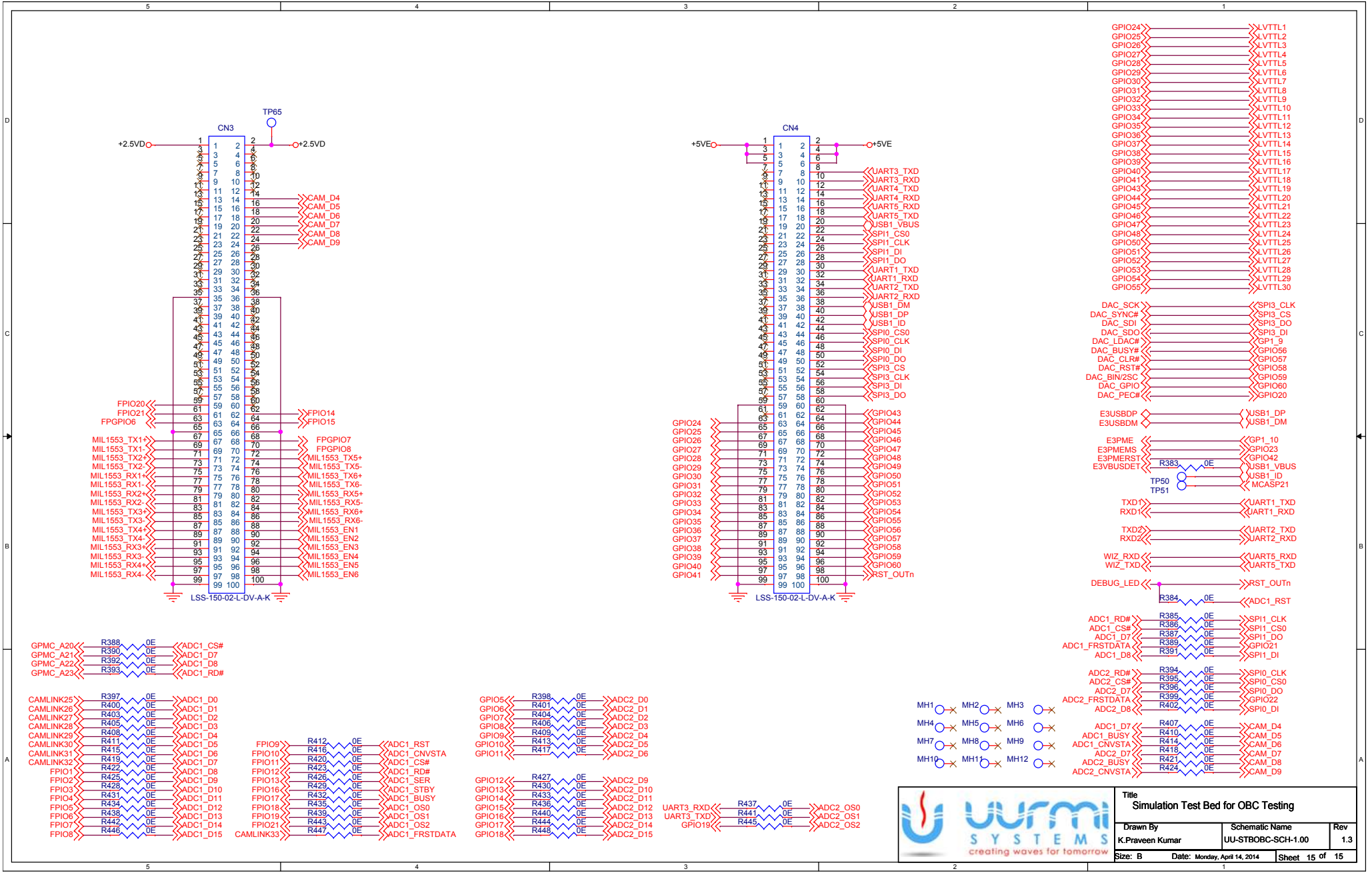


Title: Simulation Test Bed for OBC Testing			
Drawn By: K.Praveen Kumar	Schematic Name: UU-STBOBC-SCH-1.00		Rev: 1.3
Size: B	Date: Monday, April 14, 2014	Sheet 12 of 15	

UURMI SYSTEMS
creating waves for tomorrow


CN3
TP65
+2.5VD
+2.5VD
CAM_D4
CAM_D5
CAM_D6
CAM_D7
CAM_D8
CAM_D9
FPIO20
FPIO21
FPGPIO6
FPIO14
FPIO15
FPGPIO7
FPGPIO8
MIL1553_TX1+
MIL1553_TX1-
MIL1553_TX2+
MIL1553_TX2-
MIL1553_RX1+
MIL1553_RX1-
MIL1553_RX2+
MIL1553_RX2-
MIL1553_TX3+
MIL1553_TX3-
MIL1553_TX4+
MIL1553_TX4-
MIL1553_RX3+
MIL1553_RX3-
MIL1553_RX4+
MIL1553_RX4-
MIL1553_TX5+
MIL1553_TX5-
MIL1553_TX6+
MIL1553_TX6-
MIL1553_RX5+
MIL1553_RX5-
MIL1553_RX6+
MIL1553_RX6-
MIL1553_EN1
MIL1553_EN2
MIL1553_EN3
MIL1553_EN4
MIL1553_EN5
MIL1553_EN6
LSS-150-02-L-DV-A-K
CN4
+5VE
+5VE
UART3_TXD
UART3_RXD
UART4_TXD
UART4_RXD
UART5_RXD
UART5_TXD
USB1_VBUS
SPI1_CS0
SPI1_CLK
SPI1_DI
SPI1_DO
UART1_TXD
UART1_RXD
UART2_TXD
UART2_RXD
USB1_DM
USB1_DP
USB1_ID
SPI0_CS0
SPI0_CLK
SPI0_DI
SPI0_DO
SPI3_CS
SPI3_CLK
SPI3_DI
SPI3_DO
GPIO24
GPIO25
GPIO26
GPIO27
GPIO28
GPIO29
GPIO30
GPIO31
GPIO32
GPIO33
GPIO34
GPIO35
GPIO36
GPIO37
GPIO38
GPIO39
GPIO40
GPIO41
GPIO43
GPIO44
GPIO45
GPIO46
GPIO47
GPIO48
GPIO49
GPIO50
GPIO51
GPIO52
GPIO53
GPIO54
GPIO55
GPIO56
GPIO57
GPIO58
GPIO59
GPIO60
RST_OUTn
LSS-150-02-L-DV-A-K
GPIO24
GPIO25
GPIO26
GPIO27
GPIO28
GPIO29
GPIO30
GPIO31
GPIO32
GPIO33
GPIO34
GPIO35
GPIO36
GPIO37
GPIO38
GPIO39
GPIO40
GPIO41
LVTTL1
LVTTL2
LVTTL3
LVTTL4
LVTTL5
LVTTL6
LVTTL7
LVTTL8
LVTTL9
LVTTL10
LVTTL11
LVTTL12
LVTTL13
LVTTL14
LVTTL15
LVTTL16
LVTTL17
LVTTL18
LVTTL19
LVTTL20
LVTTL21
LVTTL22
LVTTL23
LVTTL24
LVTTL25
LVTTL26
LVTTL27
LVTTL28
LVTTL29
LVTTL30
DAC_SCK
DAC_SYNC#
DAC_SDI
DAC_SDO
DAC_LDAC#
DAC_BUSY#
DAC_CLR#
DAC_RST#
DAC_BIN/2SC
DAC_GPIO
DAC_PEC#
SPI3_CLK
SPI3_CS
SPI3_DO
SPI3_DI
GP1_9
GPIO56
GPIO57
GPIO58
GPIO59
GPIO60
GPIO20
E3USBDP
E3USBDM
USB1_DP
USB1_DM
E3PME
E3PMEMS
E3PMERST
E3VBUSDET
R383
0E
TP50
TP51
GP1_10
GPIO23
GPIO42
USB1_VBUS
USB1_ID
MCASP21
TXD1
RXD1
UART1_TXD
UART1_RXD
TXD2
RXD2
UART2_TXD
UART2_RXD
WIZ_RXD
WIZ_TXD
UART5_RXD
UART5_TXD
DEBUG_LED
RST_OUTn
R384
0E
ADC1_RST
GPMC_A20
GPMC_A21
GPMC_A22
GPMC_A23
R388
R390
R392
R393
0E
ADC1_CS#
ADC1_D7
ADC1_D8
ADC1_RD#
CAMLINK25
CAMLINK26
CAMLINK27
CAMLINK28
CAMLINK29
CAMLINK30
CAMLINK31
CAMLINK32
FPIO1
FPIO2
FPIO3
FPIO4
FPIO5
FPIO6
FPIO7
FPIO8
R397
R400
R403
R405
R408
R411
R415
R419
R422
R425
R428
R431
R434
R438
R442
R446
0E
ADC1_D0
ADC1_D1
ADC1_D2
ADC1_D3
ADC1_D4
ADC1_D5
ADC1_D6
ADC1_D7
ADC1_D8
ADC1_D9
ADC1_D10
ADC1_D11
ADC1_D12
ADC1_D13
ADC1_D14
ADC1_D15
FPIO9
FPIO10
FPIO11
FPIO12
FPIO13
FPIO16
FPIO17
FPIO18
FPIO19
FPIO21
CAMLINK33
R412
R416
R420
R423
R426
R429
R432
R435
R439
R443
R447
0E
ADC1_RST
ADC1_CNVSTA
ADC1_CS#
ADC1_RD#
ADC1_SER
ADC1_STBY
ADC1_BUSY
ADC1_OS0
ADC1_OS1
ADC1_OS2
ADC1_FRSTDATA
GPIO5
GPIO6
GPIO7
GPIO8
GPIO9
GPIO10
GPIO11
R398
R401
R404
R406
R409
R413
R417
0E
ADC2_D0
ADC2_D1
ADC2_D2
ADC2_D3
ADC2_D4
ADC2_D5
ADC2_D6
GPIO12
GPIO13
GPIO14
GPIO15
GPIO16
GPIO17
GPIO18
R427
R430
R433
R436
R440
R444
R448
0E
ADC2_D9
ADC2_D10
ADC2_D11
ADC2_D12
ADC2_D13
ADC2_D14
ADC2_D15
UART3_RXD
UART3_TXD
GPIO19
R437
R441
R445
0E
ADC2_OS0
ADC2_OS1
ADC2_OS2
MH1
MH2
MH3
MH4
MH5
MH6
MH7
MH8
MH9
MH10
MH11
MH12
ADC1_RD#
ADC1_CS#
ADC1_D7
ADC1_FRSTDATA
ADC1_D8
R385
R386
R387
R389
R391
0E
SPI1_CLK
SPI1_CS0
SPI1_DO
GPIO21
SPI1_DI
ADC2_RD#
ADC2_CS#
ADC2_D7
ADC2_FRSTDATA
ADC2_D8
R394
R395
R396
R399
R402
0E
SPI0_CLK
SPI0_CS0
SPI0_DO
GPIO22
SPI0_DI
ADC1_D7
ADC1_BUSY
ADC1_CNVSTA
ADC2_D7
ADC2_BUSY
ADC2_CNVSTA
R407
R410
R414
R418
R421
R424
0E
CAM_D4
CAM_D5
CAM_D6
CAM_D7
CAM_D8
CAM_D9
Title
Simulation Test Bed for OBC Testing
Drawn By
K.Praveen Kumar
Schematic Name
UU-STBOBC-SCH-1.00
Rev
1.3
Size: B
Date: Monday, April 14, 2014
Sheet 15 of 15