

ADS7828E

- The following slides show sampling of 2.2VDC input on channel-6.
- ADC setup: 5V external reference and single ended input.
- The first slide show single sampling and division of the time axes into 5 areas. The slides that follow showing zoom of each area.
- The last slide show multiply sampling.

Single sampling (VDC).

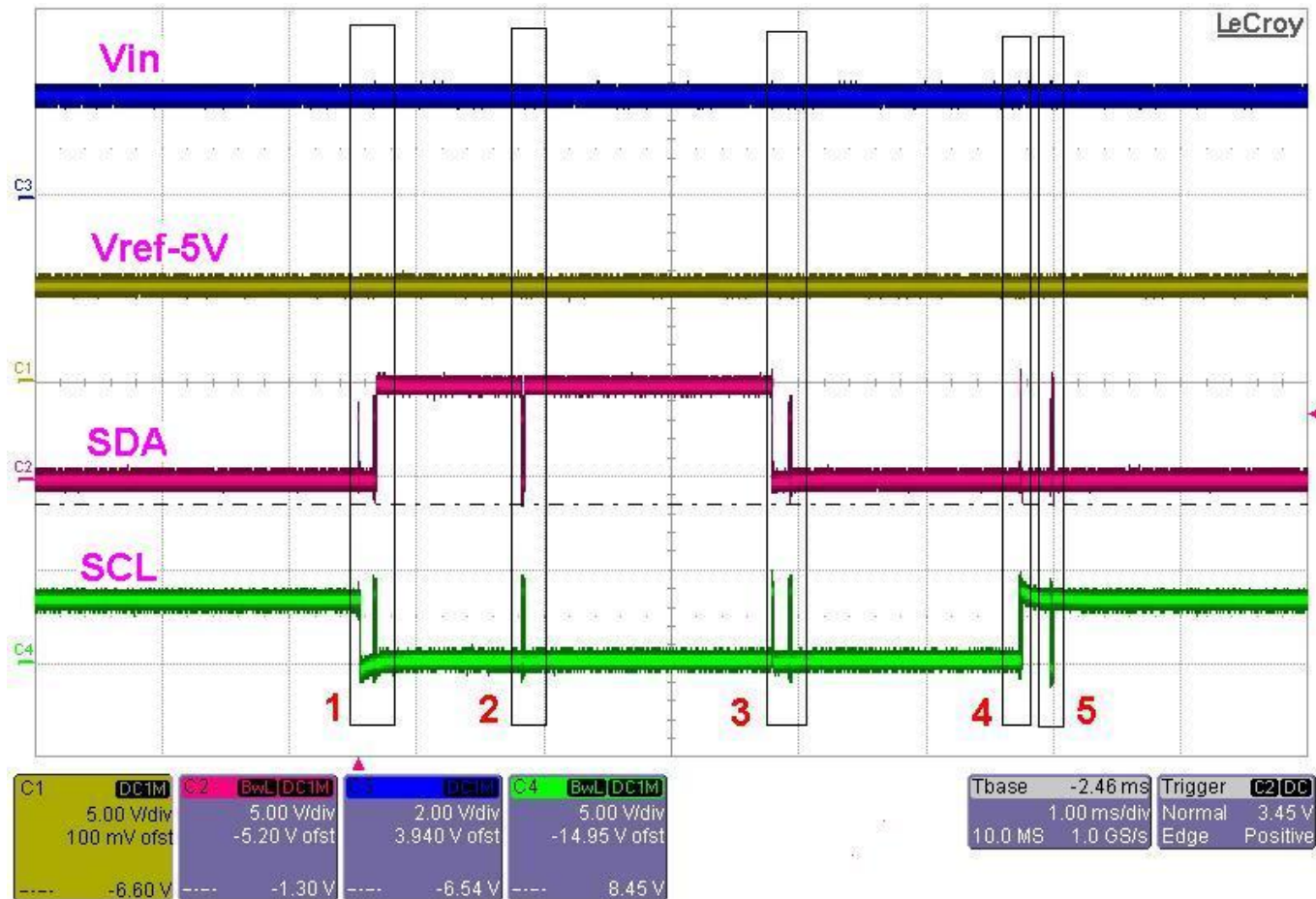
Area-1: Slave address (4AHex) + WR

Area-2: Command {Ch6,Vref ext} (B4)

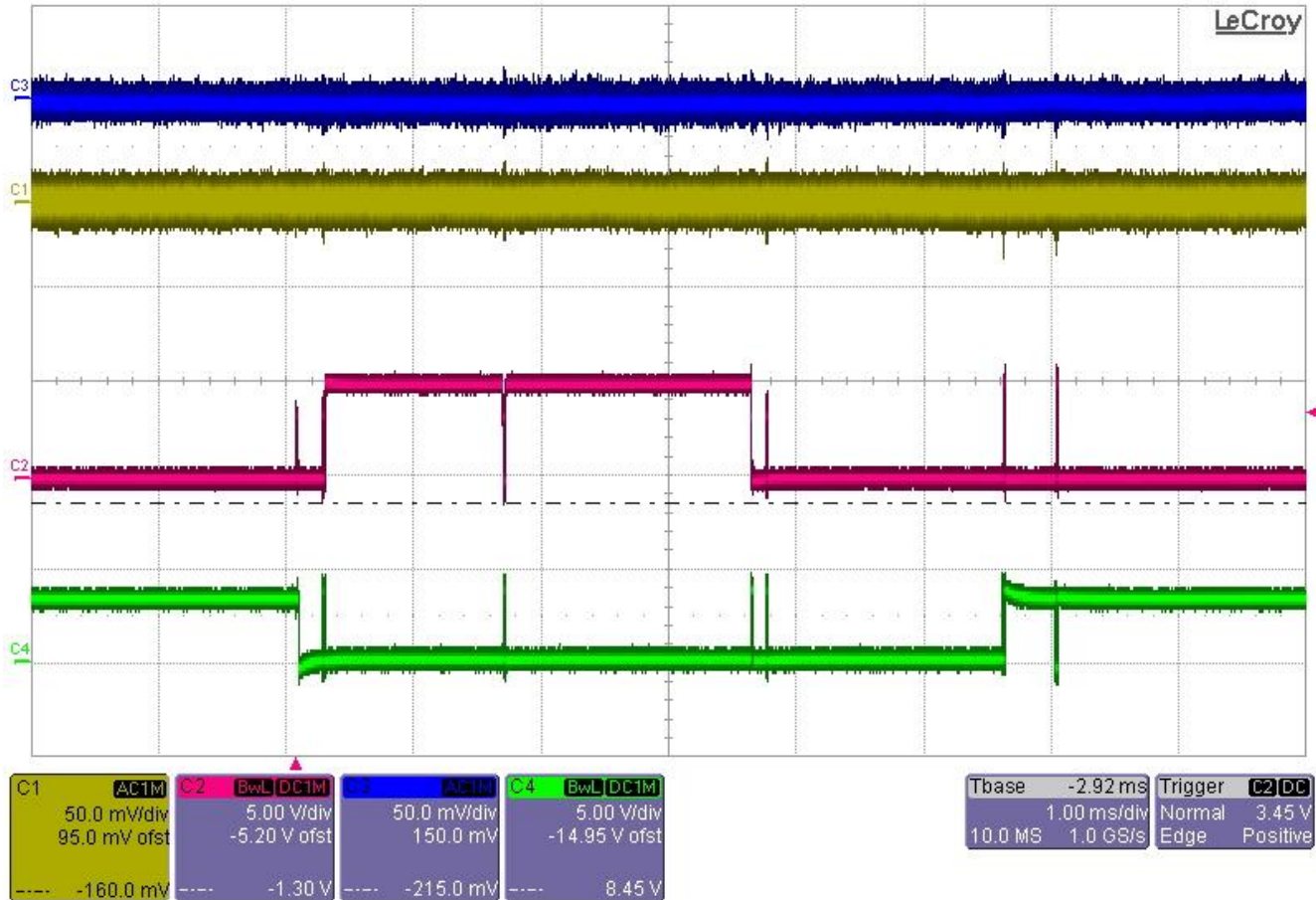
Area-3: Slave address (4AHex) + RD

Area-4: First byte (07Hex)

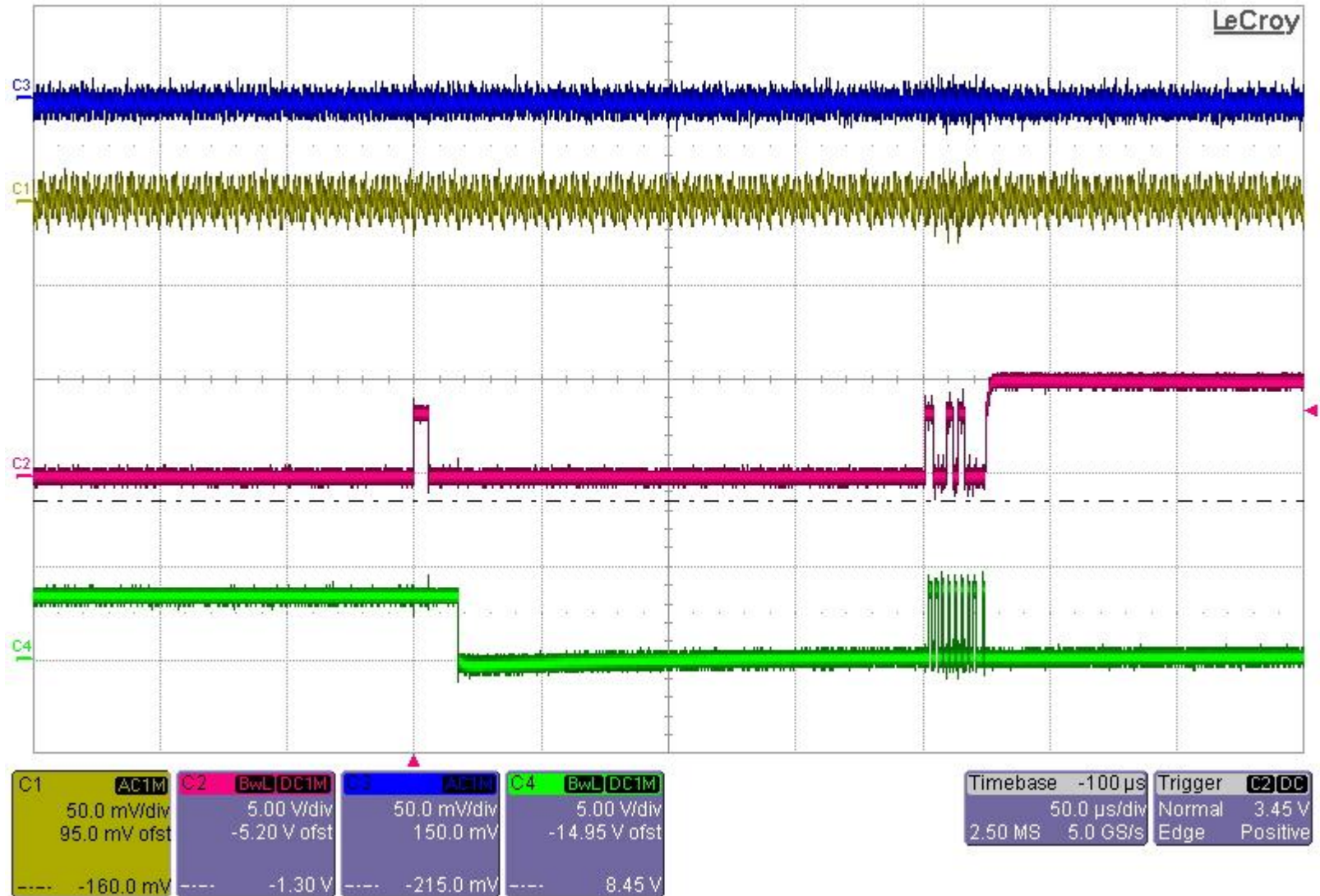
Area-5: Second byte (1BHex)



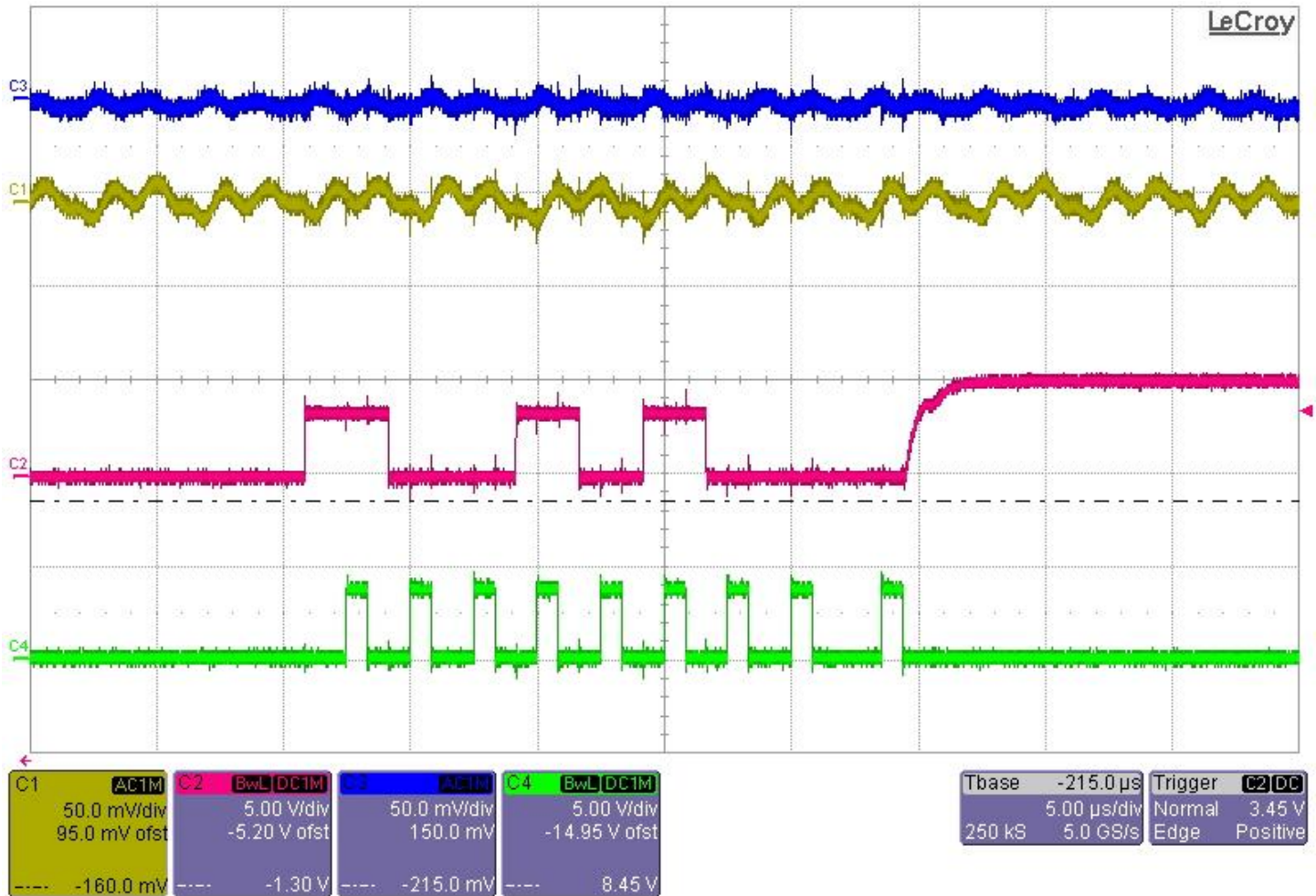
Single sampling VAC



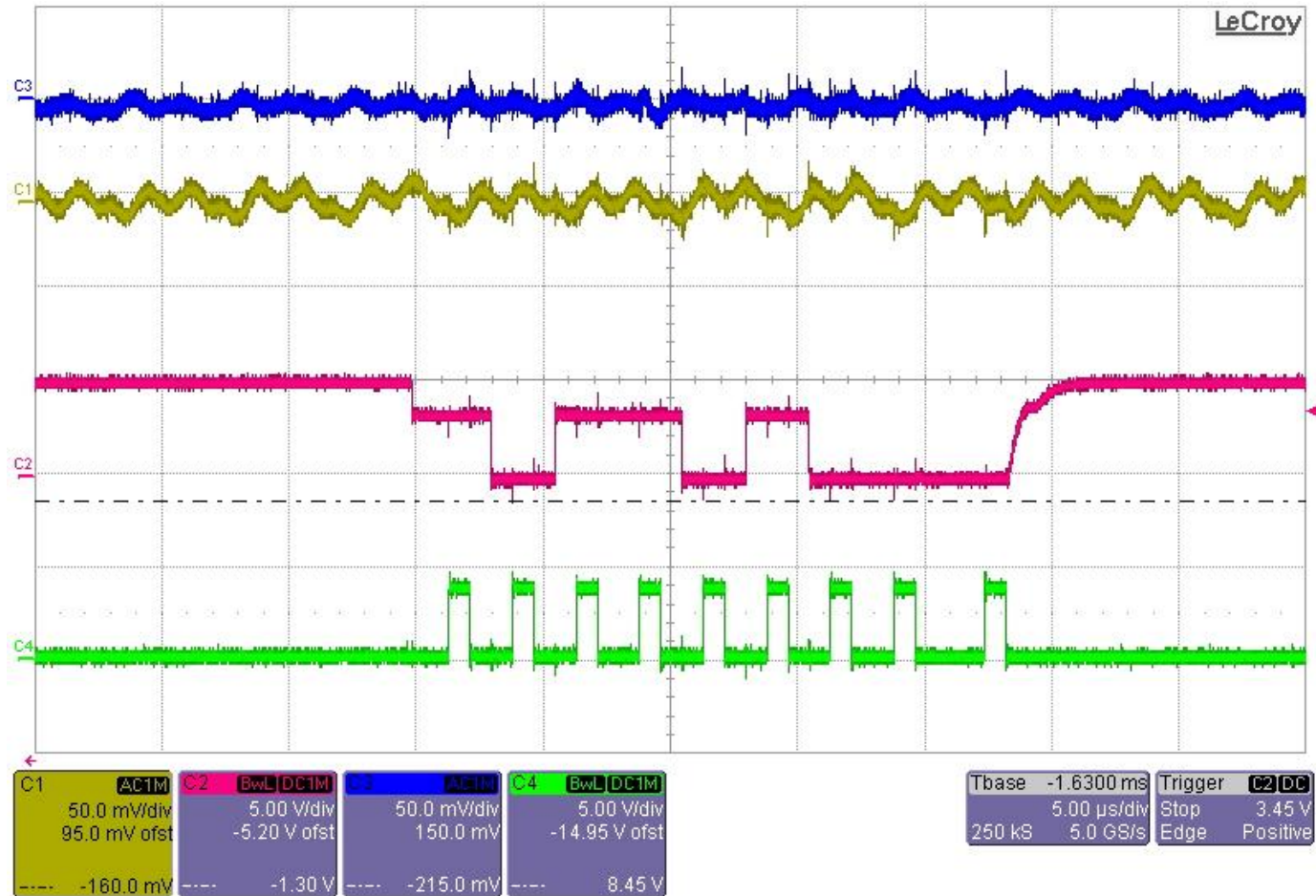
Zoom on area-1 (start bit)



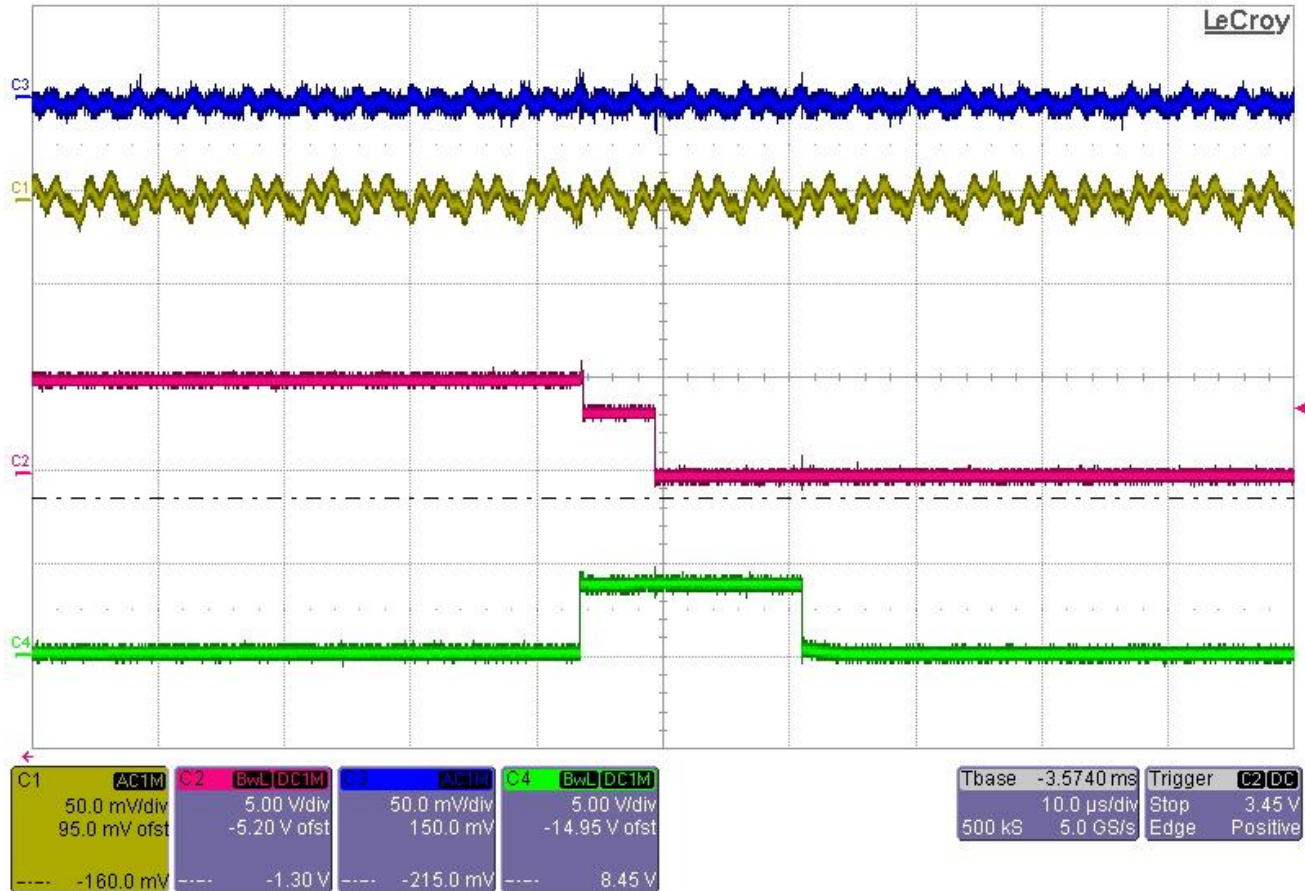
Zoom on Area-1 (slave address-4AHex+Wr)



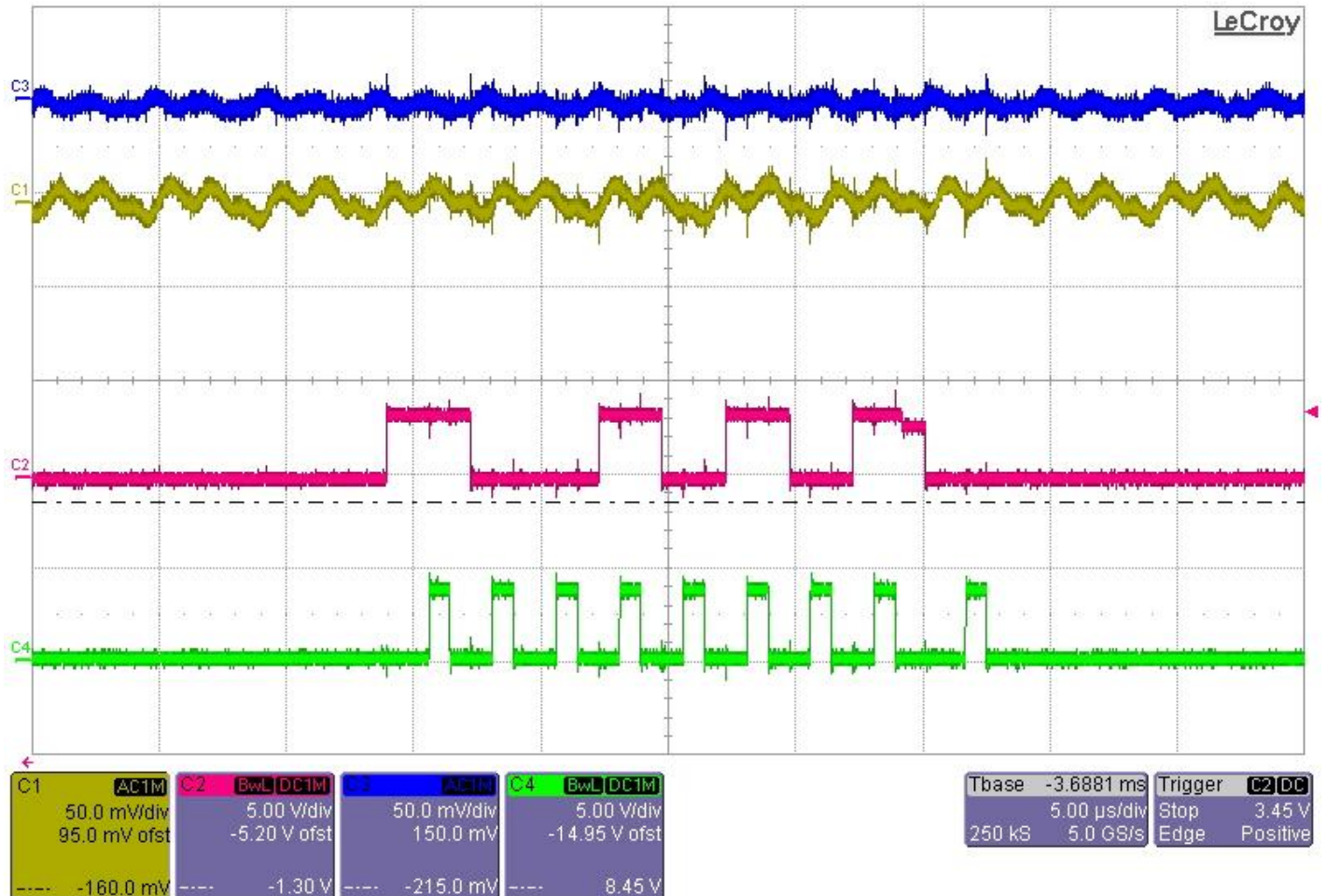
Zoom on Area-2 (Command-B4Hex)



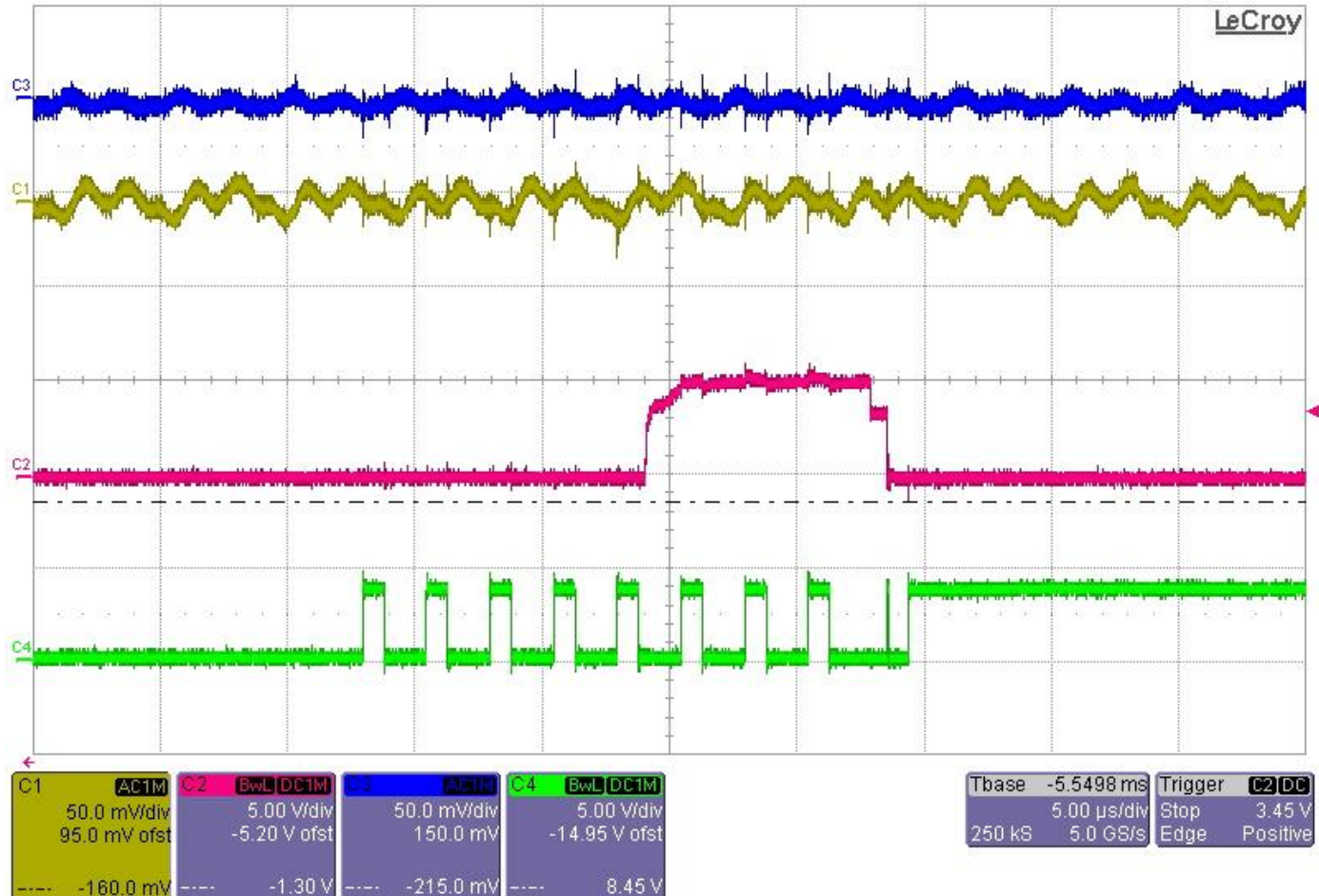
Zoom on Area-3 (Start bit)



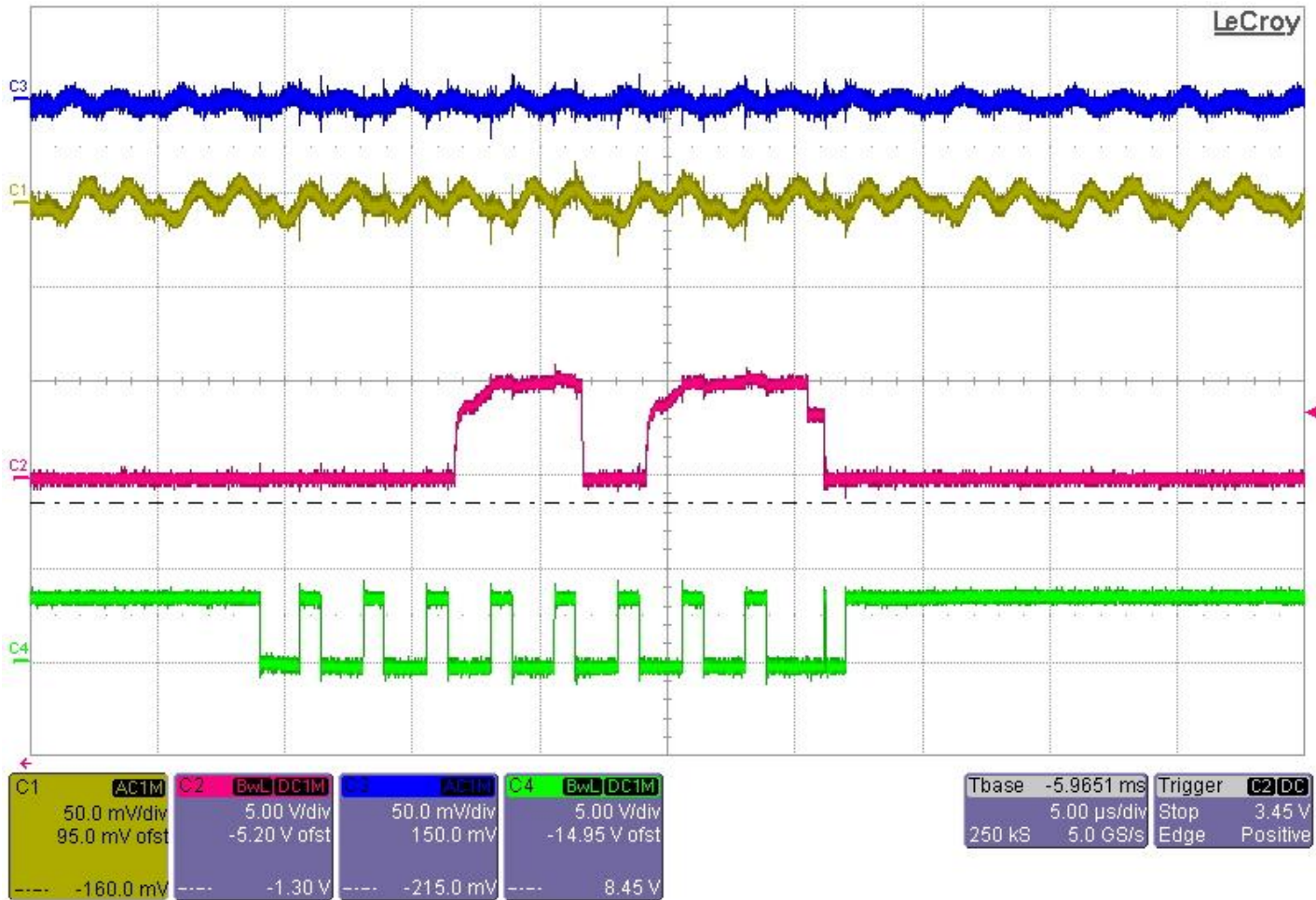
Zoom on Area-3 (slave address-4AHex+Rd)



Zoom on Area-4 (First byte (07Hex))



Zoom on Area-4 (Second byte (1BHex))



Multiply sampling

