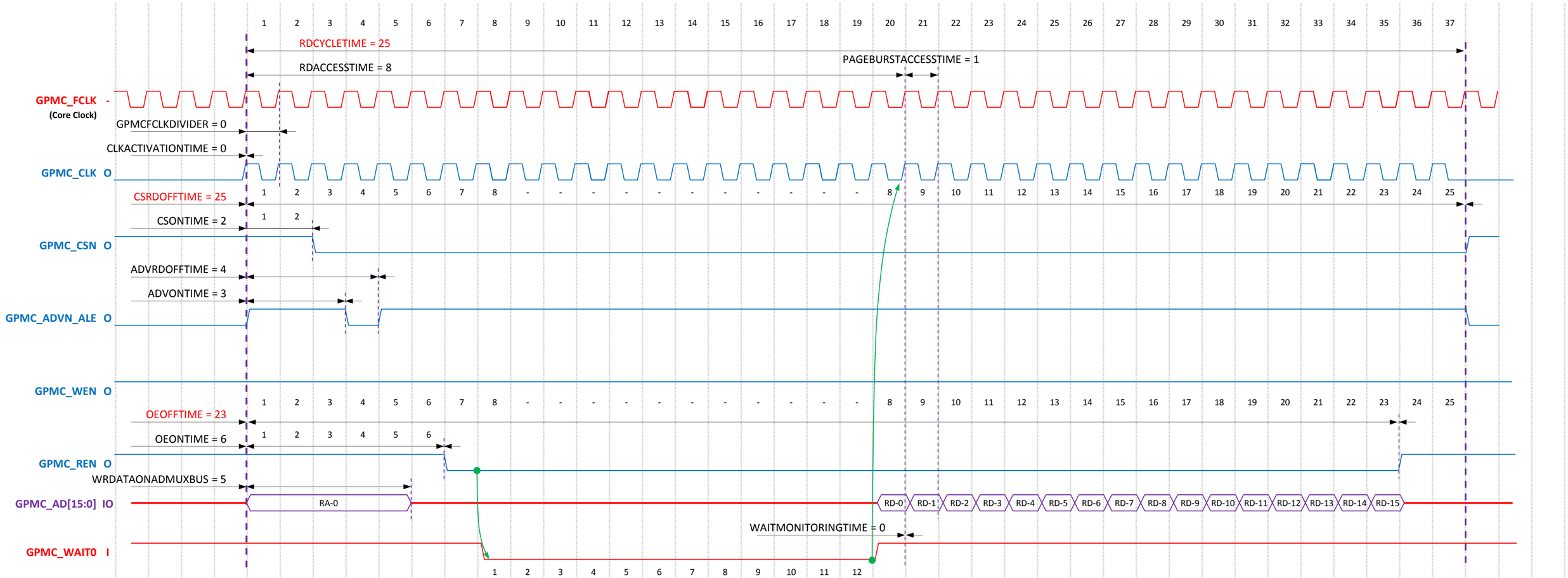

■Wait関連の設定

＜GPMC_IRQENABLE Register＞

WAIT0EDGEDETECTIONENABLE	1h	Wait0EdgeDetection event generates an interrupt if occurs

＜GPMC_CONFIG Register＞

WAIT0PINPOLARITY	0h	WAIT0 active low

＜GPMC_CONFIG1_0 Register＞

WAITREADMONITORING	1h	WAIT pin is not monitored for read accesses
WAITMONITORINGTIME	0h	WAIT pin is monitored with valid data
WAITPINSELECT	0h	WAIT input pin is WAIT0